

New ②

JAWAHARLAL NEHRU TECHNOLOGICAL UNIVERSITY ANANTAPUR
(Established by Govt.of A.P., Act.No.30 of 2008)
ANANTHAPURAMU - 515 002 (A.P.) INDIA

Dr.C.Sashidhar,
M.Tech, Ph.D.,



Phone : + 918554272433
FAX +918554 272437
Mobile: + 91 9908 088806
Email id:registrar@jntua.ac.in

Registrar

Procs . No. C2/JNTUA/Estt./TA & DA /2022

Dt.23-07-2022

Sub:- JNTUA, ATP-Estt - Permission to Dr.G.Mamatha, Co-ordinator, Skill Development Centre, JNTUA, ATP for organize for Collaborate with Siddartha Educational Academy group of Institutions , Gollapalli, Tirupathi for conduction of "Chip Solution program on 13th August, 2022-
Orders-Issued.

Read:- (1) Office Note dt.16-07-2022 & dt.23-7-2022 of the Co-ordinator, SDIC, JNTUA,ATP and approved by the Vice-Chancellor, dt. 21-07-2022

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ORDER:-

Dr.G.Mamatha, Assistant Professor in ECE Department & Co-ordinator , Skill Development Centre & Incubation Centre, JNTUA, Ananthapuramu in her Office Note read above the stated that for organize for Collaborate Programme with Siddartha Educational Academy group of Institutions, Gollapalli, Tirupathi for conduction of "Chip Solution program" in managing innovation and incubation activities towards Travelling Allowance & DA on 13th August, 2022. She also requested to utilize the car for the above purpose.

Under the above circumstances the Vice-Chancellor is pleased to accord permission to Dr.G.Mamatha, Assistant Professor in ECE Department & Co-ordinator, Skill Development Centre & Incubation Centre, JNTUA, Ananthapuramu in her Office Note read above the stated that for Collaborate programme with Siddartha Educational Academy group of Institutions , Gollapalli, Tirupathi for conduction of "Chip Solution program" towards Travelling Allowance & DA on 13th August, 2022 and permitted to utilize the car for the above purpose.

The expenditure shall be met from *U.D.F. Account* of the University.


REGISTRAR
J.N.T.U. Anantapur
ANANTAPURAMU-515002

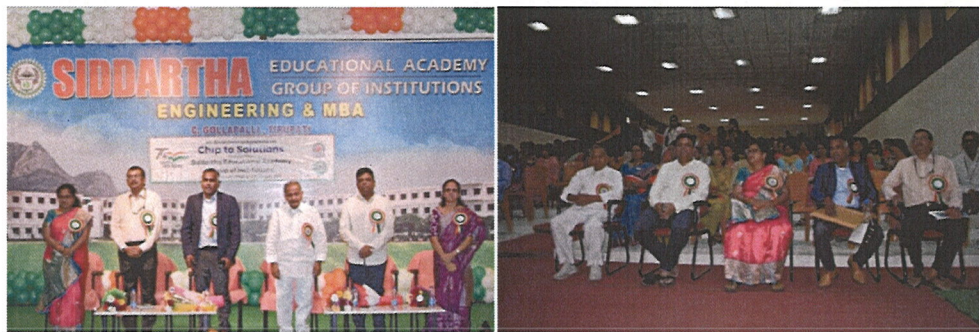

REGISTRAR


To

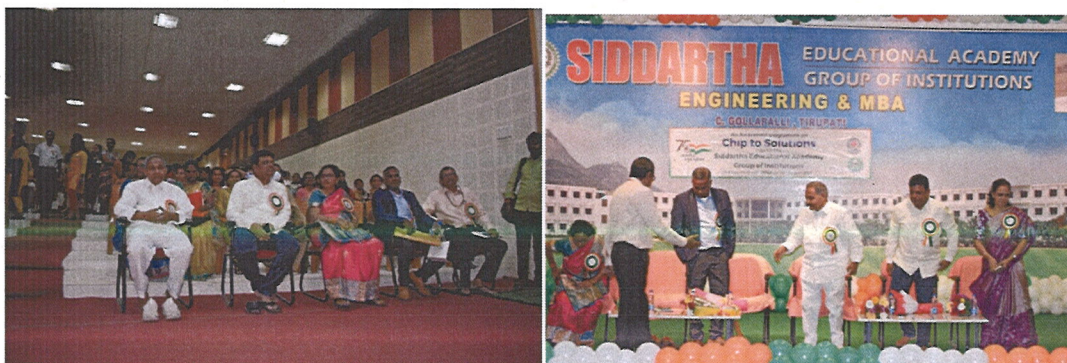
Dr.G.Mamatha, Asst.Prof.in ECE & Co-ordinator , SDIC, JNTUA, ATP for information & necessary action
Copy to D.R. (E) & D.R. (Accts.), JNTUA, ATP for information
Copy to P.S. to V.C., P.A. to Rector & P.A. to Registrar
Copy to file.

Report on "Chip to Solutions"

The awareness programme on "Chip to Solutions" on 13th August, 2022 is collaboration between JNTUA and Siddhartha Educational Academy group of Institutions, Gollapalli, Tirupati. Prof. C. Sashidhar, Registrar, JNTUA and Sri Ganesan Narayanasamy, Senior Technical Computing, IBM, New York, are the chief guests. Dr. G.Mamatha, Coordinator, SD & IC, JNTUA and Dr. K. Madhu Smitha Rani, Professor, Depart of Physics, SEAT, as its co-ordinators.



The main objective of this programme is to emphasize the importance of chip design and its solutions, COVID-19 pandemic one of the many consequences is slowdown in chip production. To bridge that India Semiconductor Mission was launched by India Azadi Ka Amrit Mahotsav is the celebration of India 75 years Independence, hence this program will empower 75 women in Semoconductor Education Research and Development. IBM and Open Power foundation are collaborating in chip solutions in open.





To develop the, participants were asked to write individually what they would do to ensure that what they had learned would be implemented in Chip to Solutions. The following are the consolidated lists of 10 sentiments each participant committed to any topic of discussion

Report Conclusion:

One of the workshop's main goals was to allow interdisciplinary teams to collaborate on developing ideas and solutions that contribute to Chip to Solutions. This goal was met thanks to the workshop's extremely positive response to the progress of their projects and the overall excellent feedback. The session was rated favourable or better by all 75 women in semiconductor education research and development, as was the experience.

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REGISTRAR
J.N.T.U. Anantapur
ANANTAPURAMU-515002