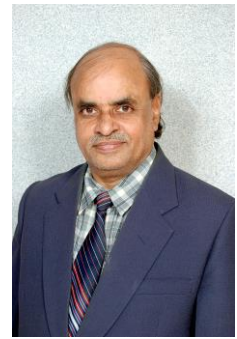


Dr. K. Lal Kishore



Registrar &

Professor of Electronics & Commn. Engg.,

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Date of birth: 17.03.1952

Academic Qualifications:

- Ph.D. from Indian Institute of Science (I.I.Sc.), Bangalore in 1986
- M.Tech. I Class with Distinction from Indian Institute of Science (I.I.Sc.), Bangalore in 1976
- B.E. I Class with Distinction (Rank Holder) from Osmania University, 1974

Professional Experience:

Teaching Experience:

- More than **34 years**, in different areas of Electronics & Communication Engineering and VLSI in the cadres of Assistant Professor, Associate Professor and Professor.
- Taught various subjects for B.Tech. and M.Tech. Courses.
- **Professor since 1990 (since 21 years)**
- Associate Professor from. October 1986 to 1990
- Assistant Professor July 1977 to 1986.

Positions Held:

Administrative Experience:

- Presently working as **Registrar since 21st November 2011.**
- **Director, Research & Development Cell** from July 2010 to November 2011

- **Director, School of Information Technology** from March to July 2010
- **Rector, JNT University, Hyderabad** from August 2008 to March 2010
- **Registrar, JNT University, Hyderabad** from September 2005 to 2008
- **Director, UGC-Academic Staff College - 2005**
- **Director, Academic and Planning, JNT University, Hyderabad** from June 2004 to June 2005
- **Principal, JNTU College of Engineering, Kukatpally, Hyderabad** from June.2002 to June 2004
- **Director, School of Continuing and Distance Education (SCDE)**, from November 2001 to June 2002.
- Professor and **Head at Defence Engineering College, Ethiopia**, on lien and established Laboratories and framed syllabi for the course work from 1997 to 1999.
- **Chairman, Board of Studies** for Electronics and Communication Engineering, Jawaharlal Nehru Technological University from 2002 to 2003.
- **Convener, ECET (FDH) 2000, 2001 and 2005** and successfully completed the assignment
- **VISITING PROFESSOR, ASIANINSTITUTE OF TECHNOLOGY (AIT), BANGKOK, DURING AUGUST-DEC. 2010**

Special Awards:

- **Best Teacher Award for the year 2004 - Awarded by Government of Andhra Pradesh to the University Teachers.**
- **SIR MOKSHAGUNDAM VISWESWARAYA AWARD** from Govt. of Andhra Pradesh and Institution of Engineers, Hyderabad chapter, in SEPTEMBER 2010
- Received **11th IETE – Prof. SVC Aiya Memorial Award in 2007** for recognition of Exemplary contribution in guiding students, researchers and teachers for research work in the field of Electronics & Telecommunications Engineering during the last five years
- Presented with **Distinguished Indian Award** for achievements in Academic, Research and Development at the International Conference ICSCI 2007, on January 03 – 07, 2007.
- Won **First Bapu Seetharam Memorial Award** for Outstanding Contribution in the field of Electronic Components in 1986.
- Certificate of Recognition from DEC, Debrezeit

Text Books Written:

1. VLSI Design : IK Publishers 2009 (Co Author: V.S.V. Prabhakar)
2. Operational Amplifiers And Linear Integrated Circuits : Pearson Education
3. Electronic Measurements And Instrumentation : Pearson Education
4. Electronic Circuit Analysis : BS Publications
5. Electronic Devices And Circuits : BS Publications
6. BASIC ELECTRONICS: BS Publications

Research:

Ph. Ds Produced:

Sl. No.	Name of thesis	Name of Ph.D. Scholar
1.	CASCADABLE ADIABATIC LOGIC CIRCUITS- A LOW POWER VLSI CIRCUIT FAMILY	N.SIVA SANKARA REDDY
2.	QUANTUM CELLULAR AUTOMATA CIRCUITS- DESIGN, MODELING AND SIMULATION	E.N.GANESH (Won Best Thesis Award in 2009)
3.	PERFORMANCE IMPROVEMENT OF CMOS DATA CONVERTERS	K.SHIVANI
4.	HIGH PERFORMANCE LOW POWER CMOS VLSI SYSTEM DESIGN	B.K.MADHAVI
5.	SPATIAL TECHNOLOGY FOR NAVIGATION AND STUDIES ON MODELING OF GPS ERRORS BASED ON KALMAN FILTER	B.L.MALLESWARI
6.	STUDIES ON VLSI ARCHITECTURE FOR NEURAL NETWORK BASED IMAGE COMPRESSION	K.VENKATA RAMANAIAH
7.	ATMOSPHERIC RADAR SIGNAL PROCESSING USING NEW ALGORITHMS AND ADVANCED TECHNIQUES	D.B.V. JAGANNADHAM
8.	STUDIES ON THE FEASIBILITY OF DEVELOPING ELECTO-OPTICAL HYBRID LOGIC CIRCUITS	E. SREENIVASARAO (SUBMITTED)

9.	EFFICIENT METHODS AND NOVEL APPROACHES FOR ANALOG ELECTRONIC CIRCUIT FAULT DIAGNOSIS USING SIMULATION BEFORE TEST APPROACH	S.P. VENUMADHAVA RAO (SUBMITTED)
10.	DYNAMIC FREQUENCY ALLOCATION FOR UNINTERRUPTED COMMUNICATION IN A COGNITIVE RADIO ENVIRONMENT	R. HEMALATHA (SUBMITTED)
11.	REDUCTION OF NOISE IN TOMOGRAPHIC IMAGES USING MULTI-WAVELET TECHNIQUES	MR. SYED AMHED ALI (SUBMITTED)
12.	FACE RECOGNITION TECHNIQUES BASED ON EIGEN FEATURES OF MULTI-SCALE FACE COMPONENTS AND ARTIFICIAL NEURAL NETWORKS	MR. K. RAMA LINGA REDDY (SUBMITTED)

Projects Implemented:

- i. Implemented the institutional networking scheme of Government of India, in collaboration with Indian Institute of Science (IISc) to establish microprocessor development systems laboratory.
- ii. Another scheme proposed to develop microprocessor based instrumentation systems laboratory in collaboration with IISc had been implemented.
- iii. Research project entitled “Development of functional components through Composites” has been sanctioned by AICTE and is being implemented.
- iv. Submitted a research project proposal to the Department of Science and Technology on “Design and Development of Functional Devices through Hybrid Micro Circuits”, involving Technology Development and Manpower Training.
- v. Member of the Research Group constituted in the University on Super Conductivity.
- vi. Worked on the Educational TV Project of the School for Continuing and Distance Education of JNTU Hyderabad.
- vii. Establishing a Centre for Micro Electronics and VLSI Design in the Department.

Projects Guided

- i. Multi Channel Multi Phase Direct Digital Frequency Synthesizer: ASIC Design.
- ii. Design of Floating point Multiplier – Accumulator Chip.
- iii. Intel 8031 based Single Channel Stationery and Mobile Target Controller.
- iv. Analog and Digital Circuit Simulation using SPICE.
- v. Multi-channel Process Parameter Monitoring and Control.

- vi. Functional Approach aspects in Circuit Design.
- vii. Micro Computer based Temperature Monitoring and Controlling System.
- viii. Digital Time constant Meter.

Publications:

- Published about 145 research papers in International and National Journals and Conferences.

Journals	(International	(44)	National	(16)
Conferences	(International	(34)	National	(51)

International Journals (44):

1.	Prof. K. Lal Kishore Prof. M. Satyam Prof. K. Rama Kumar	1986.	Solid State Varistors with +ve Voltage Coefficient of resistance - Journal of Physics, "D" Applied Physics, Vol. 19, 1986	L61-62	IJ
2.	Prof. K. Lal Kishore Prof. M. Satyam Prof. K. Rama Kumar	January, 1987.	Varistors based on Barium Titanate-Graphite-Rubber Composites - Journal of Applied Physics	January, 1987.	Pages 387 – 403
3.	Prof. K. Lal Kishore	Simulation Journal	Modeling of particulate composite Varistors exhibiting Positive Voltage coefficient of Resistance Simulation Journal	Simulation Journal	IJ
4.	Siva Sankar Reddy Prof. M. Satyam Prof. K. Lal Kishore		Glitch Free And Cascadable Adiabatic Logic For Low Power Applications	AJSR, Vol.1 No.4, 2008	Pages 464-469
5.	Siva Sankar Reddy Prof. M. Satyam Prof. K. Lal Kishore	January 2009	Adiabatic universal gates for hierarchical circuits,	IE(I) journal-ET, Vol 89	Pages 3-7
6.	EN Ganesh Dr. Lal Kishore Dr. MJS Rangachar	December 2007	StStudy and simulation of QCA devices using Neural Networks	IEEEICIMCA, VOL-1	Pages 172-176
7.	EN Ganesh Dr. Lal Kishore Dr. MJS Rangachar	December 2007	Reliability analysis of Quantum cellular automata circuits using Bayesian networks	Procd of IEEE ICIMCA Vol-1	Pages 167- 171
8.	EN Ganesh Dr. Lal Kishore Dr. MJS Rangachar	JUNE 2008	Analysis of Energy dissipation and point effects in Quantum Cellular Automata Circuits	VOL 2 No1	Pages 89-106
9.	EN Ganesh Dr. Lal Kishore Dr. MJS Rangachar	AUGUST 2007	Simulation of QCA Logic devices for quantum logic circuits	VOL 2 Issue 1	Pages 1-24
10.	EN Ganesh Dr. Lal Kishore Dr. MJS Rangachar	12 th – 14 th December 2007	Energy analysis of Quantum cellular automata circuits	VOLUME-2	Pages 476-485
11.	EN Ganesh Dr. Lal Kishore Dr. MJS Rangachar	March 2008	Study of complex Gate structures in Quantum Cellular Automata Technology	ISSN 0973-4562 VOL 3, No.3	Pages 305-319
12.	Malleswari, Dr. K. Lal Kishore, Dr. IV Murali Krishna	Accepted for publication	KALMAN FILTER FOR GPS DATA Preprocessing - International Journal of Geoinformatics	Accepted for publication	IJ
13.	BL Malleswari, M. Seeta,	9 th June 2007 – (Accepted for	Performance of KALMAN Filter for GPS Datum Conversion - ITC Netherlands	9 th June 2007	IJ

	Dr. K. Lal Kishore, Dr. IV Murali Krishna, Nagaratna P	publication)			
14.	Dr. N. Sarat Chandra Babu SP Venu Madhava Rao & Dr. K. Lal Kishore		Multi Stimulus and MultiFrequency measurements in Analog Fault Diagnosis		IJ
15.	Dr. BK Madhavi Dr. K. Lal Kishore Dr. T. Subba Ra	IE(I) Journal – ET	Performance Evaluation of Low Power high Speed Ring Oscillator and Design of 2.5 GHz Ring Oscillator for Wireless Network application	IE(I) Journal – ET	Pages 30 – 33
16.	Dr. N. Sarat Chandra Babu SP Venu Madhava Rao & Dr. K. Lal Kishore		Multi Stimulus and Multi Frequency measurements in Analog Fault Diagnosis		IJ
17.	Dr. BK Madhavi Dr. K. Lal Kishore Dr. T. Subba Ra	IE(I) Journal – ET	Performance Evaluation of Low Power high Speed Ring Oscillator and Design of 2.5 GHz Ring Oscillator for Wireless Network application	IE(I) Journal – ET	Pages 30 – 33
18.	E. Sreenivasa Rao M. Satyam Dr. K. Lal Kishore	Quantum Electronics & Optoelectronic s	Electro-optical hybrid logic gates	Vol. 10 N1	P 72-76
19.	E. Sreenivasa Rao M. Satyam Dr. K. Lal Kishore		Universal Hybrid Optoelectronic Logic Gates		IJ
20.	Prof. K. Venkata Ramanaiah Dr. K. Lal Kishore Dr. P. Gopal Reddy		Power Efficient Multilayer Neural Network for Image Compression	ISSN. No. 1812-5638	IJ
21.	CV Krishna Reddy Dr. K. Lal Kishore	Communicated	Power Optimization in Flash ADCs – Far East Journal EC (Int. Journal)		IJ
22.	Prof. K. Venkata Ramanaiah Dr. K. Lal Kishore Dr. P. Gopal Reddy		Power Efficient Multilayer Neural Network for Image Compression	ISSN. No. 1812-5638	IJ
23.	Prof. BK Mdhavi Dr. K. Lal Kishore Dr. T. Subba Ra	Journal of Solid State Electronics	Low Power High Performance CMOS VLSI Shift Register		IJ
24.	Prof. BK Mdhavi Dr. K. Lal Kishore Dr. T. Subba Ra	Journal of Solid State Electronics	Low Power High Performance 8 : 1 Deep submicron – CMOS Multiplier		IJ
25.	Dr. B L Deekshatulu Dr. K Lal Kishore Y Rakesh Kumar	JATIT Journal of Theoretical and applied IT	“Novel impulse detection technique for image Denoising” ID: 647-JATIT-2K8	647-JATIT-2K8	IJ
26.	N.Sarat Chandra Babu, V.C. Prasad, S.P. Venu Madhava Rao and K.L. Kishore	Journal of CIRCUITS, SYSTEMS AND COMPUTE-RS	Multi-Frequency Approach to Fault Dictionary of Linear Analog Fault Diagnosis	Vol.17,No.5(200 8) 905-928	IJ
27.	G.Laxminarayana, Dr.N.S.Murthy Sharma, Dr.K.Lal Kishore	Journal of COMPUTER SCIENCE AND NETWORK	High Precision Navigation Control With Integrated INS/GPS System	Vol.9,No.9,Septe mber 2009	IJ

		SECURITY			
28.	K. Rama Linga Reddy, G.R. Babu, K. Lal Kishore	International Journal for Signal Processing, Image Processing and Pattern Recognition (IJSIP-SERSC)	Face Recognition based on Eigen Features of Low Resolution Face Components and Artificial Neural Network		IJ
29.	K. Rama Linga Reddy, G.R. Babu, K. Lal Kishore	International Journal of Computational Intelligence and Research (IJCIR)	Face Recognition based on Eigen Features of Multi Scaled Face Components and Artificial Neural Network	Vol-6, issue no:4	Page No: 600-618
30.	K. Rama Linga Reddy, G.R. Babu, K. Lal Kishore, "Partial Face Recognition	IFCAI, Journal of Electrical Electronic Engineering	Partial Face Recognition	Vol No.3, issue 4,	pages: 1-18.
31.	K. Rama Linga Reddy, G.R. Babu, K. Lal Kishore	Technology Spectrum JNTU, 2008	Partial Face Recognition using Multi Scale Feature and Artificial Neural Network	vol:2, Issue:2,	Pages: 21-27.
32.	K. Rama Linga Reddy, G.R. Babu, K. Lal Kishore	International Journal of Computer Science and Network security	Face Recognition based on Multi Scale Low Resolution Feature Extraction and Single Neural Network	vol:8 Issue:6, June 2008,	Pages: 279-283.
33.	K. Rama Linga Reddy, G.R. Babu, K. Lal Kishore	Journal of Theoretical and Applied Information Technology	Multiscale feature and single neural network based face recognition	2005-2008 JATIT	
34.	Prof. Syed Amjad Ali, Dr. Srinivasan Vathsal, Dr. K. Lal Kishore	International Journal of Soft Computing Applications (IJSCA)	An efficient denoising technique for CT images using Window- based Multiwavelet transformation and thresholding		
35.	Prof. Syed Amjad Ali, Dr. Srinivasan Vathsal, Dr. K. Lal Kishore	International Journal of Computer Science and Information Security (IJCSIS)	A GA based window selection Methodology to Enhance window-based Multi-wavelet transformation and thresholding aided CT image denoising technique	Vol.7, 2 nd Feb.2010, ISSN: 1947-5500	
36.	Prof. Syed Amjad Ali, Dr. Srinivasan Vathsal, Dr. K. Lal Kishore	International Journal of Digital Content Technology and its Applications	CT Image denoising Technique using GA aided window-based Multiwavelet Transformation and Thresholding with the Incorporation of an Effective Quality Enhancement Method	Volume 4, Number 4, July 2010.	
37.	ND Bobby Dr. SK Srivatsa and Dr. K. Lal	European Journal of	Low Power Adaptive Viterbi Decoder with FASCU	ISSN 1450-216X Vol. 60 No. 4	

	Kishore	Science and Research		(2011) Pg 481 – 487
38.	Prof.Syed Amjad Ali, Dr.Srinivasan Vathsal, Dr.K.Lal Kishore	<i>European Journal of Scientific Research</i>	An efficient denoising technique for CT images using window-based multiwavelet transformation and thresholding	vol. 48, no.2, 2010, pp.315-325
39.	Prof.Syed Amjad Ali, Dr.Srinivasan Vathsal, Dr.K.Lal Kishore	<i>International Journal of Computer Science and Information Security(IJCSIS)</i>	A GA-based window selection methodology to enhance window-based multiwavelet transformation and thresholding aided CT image denoising technique	vol.7, no.2, February 2010, pp.280-288
40.	Prof.Syed Amjad Ali, Dr.Srinivasan Vathsal, Dr.K.Lal Kishore	<i>International Journal of Digital Content Technology and its Applications</i>	CT image denoising technique using GA aided window-based multiwavelet transformation and thresholding with the incorporation of an effective quality enhancement method	Volume 4, number 4, July 2010, pp.75-87
41.	Rajendra Prasad S, Dr. BK Madhavi and Dr. K. Lal Kishore	<i>International Journal of VLSI Design & Communication Systems (VLSICS)</i>	Design of Low Write-Power Consumption SRAM Cell based on CNTFET at 32 nm Technology	Vol. 2 No. 4 Dec. 2011
42.	Rajendra Prasad S, Dr. BK Madhavi and Dr. K. Lal Kishore	<i>International Journal of Computer Science and Information Technologies</i>	A New Low Power 9T SRTAM Cell based on CNTFET at 32nm Technology	Vol. 2 No. 6 pp 2761-2764 Nov. Dec. 2011
43.	Rajendra Prasad S, Dr. BK Madhavi and Dr. K. Lal Kishore	<i>International Journal of Advances in Science and Technology</i>	High-performance Memory Cell Design at 32 nm Technology based on CNTFET for Low-Power Embedded Systems	Vol.3, No. 4, PP 46-52 Oct. 2011
44.	Rajendra Prasad S, Dr. BK Madhavi Dr. K. Lal Kishore and Rangaiah L,	<i>International Journal of Electrical, Electronics & Computing Technology</i>	Reduction of Delay and Cross Talk using Buffer Insertion Method	Vol. 1 (2), pp 34-40 Jan. Apr. 2011

National Journals (16):

1.	Dr. Kama Raju; Tilak, Dr. K. Lal Kishore		Novel Adder Circuits using Reversible Logic gates		NJ
2.	Siva Sankar Reddy Prof. M. Satyam Prof. K. Lal Kishore		An Adiabatic Inverter for hierarchical circuits - Communicated to JI of IETE	Procd of Conference	NJ

3.	Siva Sankar Reddy Prof. M. Satyam Prof. K. Lal Kishore	July 2008	Realization of Adiabatic four bit ripple counter using Glitch Free And Cascadable Adiabatic Logic, Technology Spectrum	Vol 2,No.2	Pages 15 - 20
4.	EN Ganesh Dr. Lal Kishore Dr. MJS Rangachar	January 2007	Nanotechnology research and education in Indian universities	IETE Volume-24	Pages 37-42
5.	Smt. BK Madhavi Prof. K. Lal Kishore Prof. Subba Rao		High Performance Low Power CMOS BCD ADDER	Vol -06, No 02, June 2006	Pages 149-154
6.	Smt. BK Madhavi Prof. K. Lal Kishore Prof. Subba Rao		DESIGN IMPLEMENTATION AND SIMULATION OF LOW POWER, HIGH PERFORMANCE SHIFT	Vol -06, No-02	Pages 155-162
7.	Smt. BK Madhavi Prof. K. Lal Kishore Prof. Subba Rao		PERFORMANCE EVALUATION OF LOW POWER, HIGH SPEED RING OSCILLATOR AND DESIGN OF 2.5G.Hz RING OSCILLATOR FOR WIRELESS NETWORK	Vol 88, Jan 28 th 2008	Pages 30-33
8.	Smt. BK Madhavi Prof. K. Lal Kishore Prof. Subba Rao		Power Optimization And High Performance CMOS 4 X MULTIPLIER FOR DSP PROCESSOR		NJ
9.	Dr. EVLN Rangacharyulu, Prof. K. Lal Kishore et.al	15-16 July 2006, Hyderabad	Design of WAE Gateway encoder - All India Seminar on Role of Telecommunication for betterment of Society, Institute of Engineers	Proceedings	Pages 3.9 – 3.11
10.	Smt. BK Madhavi Prof. K. Lal Kishore Prof. Subba Rao T Dr. K. Ragavendra Rao		High performance Low Power CS BCD Adder	Vol. 6 No. 2	149 – 162
11.	EN Ganesh Dr. Lal Kishore Dr. MJS Rangachar	Technology Spectrum	Simulation of Quantum Cellular Automata majority gate using Genetic algorithm	Vol. 1 No.3, Jan 2008	Pages 26-81
12.	Dr. K. Lal Kishore	IETE Technical Review	Trends in VLSI Technology – Rural Applications Perspective	Vol. 24 No. 4	P No. 243 -248
13.	BK Mdhavi Dr. K. Lal Kishore Dr. T. Subba Rao		Power Optimization and High performance CMOS 4 X 4 Multiplier for DSP Processor	Proceedings	NJ
14.	DBV Jagannadham Dr. K. Lal Kishore	Technology Spectrum	Serilizer and deserilizer based modulation and demoderalation for high speed inter-sattelite link	Vol. I Nos. 2 July 2007	NJ
15.	CV Krishna Reddy Dr. K. Lal Kishore	July 2007 – Technology Spectrum	Design and Simulation of CMOS Voltage to Current Convertor	Journal Vol. 1 No. 2	NJ
16.	DBV Jagannadhom, V.K. Anandam, G.V.S. Raju Dr. K. Lal Kishore	Technology Spectrum JNTU	Analysis of Atmospheric Radar Signals Using Empirical Mode of Decomposition and the Hilbert Transform	Vol. 2 No. 3	P. No 82 – 84

International Conference (34):

1.	Prof. K. Lal Kishore Prof. M. Satyam Prof. K. Rama Kumar	September, 1982.	Ceramic Composites in Electronics - International Conference of BHEL, Bangalore	Procd 209.1-3	IC
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2.	Prof. K. Lal Kishore Prof. M. Satyam Prof. K. Rama Kumar	December, 1984	Varistors based on Conductor Piezoelectric Composites - International Conference of ISSM held at New Delhi	December, 1984	Pages 535-536
3.	Prof. K. Lal Kishore Prof. M. Satyam Prof. K. Rama Kumar	September, 1985	Varistors with PVCRC based on Sintered Materials - International Conference held at Yugoslavia	September, 1985	IC
4.	Prof. K. Lal Kishore	7 th November 1992	Quality in Technical Education: IEEE Seminar - Hyderabad, India.	Procd IEEE	IC
5.	Prof. K. Lal Kishore Prof. M. Satyam Prof. K. Rama Kumar	December, 1984	Varistors based on Conductor Piezoelectric Composites - International Conference of ISSM held at New Delhi	December, 1984	Pages 535-536
6.	Prof. K. Lal Kishore Prof. M. Satyam Prof. K. Rama Kumar	September, 1985	Varistors with PVCRC based on Sintered Materials - International Conference held at Yugoslavia	September, 1985	IC
7.	Prof. K. Lal Kishore	November 21- 23, 1994,	University Industry Interaction Continuing Education Aspects - International Conference on Engineering Education – An Indian perspective. (UNESCO sponsored), Andhra University, Vishakapatnam.	Procd of InH Conference	IC
8.	Prof. K. Lal Kishore	2-5 th July 1996.	Continuing Education Aspects – Global perspective - Proceedings of World Congress of Engineering Deans and Industry Leaders, UNESCO, Paris,	Procd of Conference	IC
9.	Siva Sankar Reddy Prof. M. Satyam Prof. K. Lal Kishore		MMinimization of energy dissipation in Glitch free cascaded adiabatic logic circuits	TENCON 2008-IEEE Reg 10 conference	Pages 1-5
10.	Dr. EVLN Rangacharyulu, Prof. K. Lal Kishore et.al	11 – 13 December 2004	Gate optimization and yield analysis of radiation tolerant programmable system on chip (SOC) with 10 million transistors.- International Conference on recent trends in non-linear analysis and its applications, IIT, Powai, Mumbai	Proceedings	Pages 21 - 22
11.	Sri EN Ganesh Prof. K. Lal Kishore	Dec. 18 th – 20 th 2004	QCA Logic gates design “Combinational and sequential circuits” - First International Conference on Lateral Computing IISC	CD of Conference Vol 2 No1 June 2008	Pages 89-106
12.	EN Ganesh Dr. Lal Kishore Dr. MJS Rangachar	December 18 th – 19 th	Quantum computation and simulation of Combinational and sequential logic circuits	WCLC-2004 ISSC- Banglore	IC
13.	EN Ganesh Dr. Lal Kishore Dr. MJS Rangachar	Dec 20-22 2007	Study of complex gate structure of QCA circuits for FPGA Applications	ICTES 2007 VOL 2	Pages 789-794
14.	EN Ganesh Dr. Lal Kishore Dr. MJS Rangachar	12 th – 14 th December 2007	Implementation of Quantum cellular automata circuits using Tile based design methodology	Procd TIES2007, VOL-2	Pages 476-485
15.	Malleswari, Dr. K. Lal Kishore, Dr. IV Murali Krishna	5-8 June 2005	Networking of Embedded Systems through GPRS - International Conference on Remote Sensing & Geo-information	June 2005	IC
16.	Sri S. Srinivasa Rao Prof. Rajan Prof. K. Lal Kishore	Jan 3-7; 2007 Hyderabad.	Merits and Demerits of using RFID tags - International Conference ICSCI 07 Systems, Cybermatics and Informatics	Jan 2007	IC
17.	Dr. DBV Jagannadham, A. Appa Rao, M. Mukunda Rao and Dr. K. Lal Kishore	Jan 3-7, 2007 Hyderabad	Optical Sensor System for Non-Invasive Medical Diagnostics - International Conference ICSCI 07 Systems, Cybermatics and Informatics	Jan 2007	IC

18.	Malleswari, Dr. K. Lal Kishore, Dr. IV Murali Krishna	22-25, Jan 2007	KALMAN FILTER FOR GPS DATA Conversion Global Meet Map World Forum - ICORG 2007	Jan 2007	Page 11
19.	Sri CV Krishna Reddy Prof. K. Lal Kishore	Feb. 2007 Visakhapatnam	Design and simulation of Voltage to current converter - IEEE Zonal Seminar	Feb. 2007	IC
20.	CV Krishna Reddy Dr. K. Lal Kishore	August 2007	Design of CMOS differential Mode Transconductor – International Conference on Modeling and Simulation – Coimbatore Institute of Technology, Coimbatore	Proceedings	IC
21.	CV Krishna Reddy Dr. K. Lal Kishore	August 2007	Design of 8-bit current mode segmented DAC - International Conference on Modeling and Simulation – Coimbatore Institute of Technology, Coimbatore	Proceedings	IC
22.	M.Kamaraju, K.Lal Kishore, A.V.N.Tilak	Proceedings of an conference on optoelectronics, ICT-(ICOICT 2009)	Low Power Programmable Embedded Controller(LP ² EC) Design	ICOICT-2009 Pages 397-399	IC
23.	M.Kamaraju, K.Lal Kishore, A.V.N.Tilak	Proceedings CITICOMS-2007 Coimbatore	MAT LAB Implementation of AES 128 Algorithm for Secure Image Transfer. International Conference on modeling and simulation (CITICOMS- 2007), CIT, Coimbatore.	Aug 2007	IC
24.	M.Kamaraju, K.Lal Kishore, A.V.N.Tilak	Proceedings ICOICT 2009 Kerala	Remote Control Observation System for Military Survilience. International Conference(ICOICT 2009),Kerala,26-27February 2009	2009	IC
25.	M.Kamaraju, K.Lal Kishore, A.V.N.Tilak	Proceedings ICOICT 2009 Kerala	Low Power Programmable Embedded Controller Design Int. Conference(ICOICT 2009),Kerala,26-27 February 2009	2009	IC
26.	Mr. N. Venkateswara Rao Mr. T. Madhu Dr. K. Lal Kishore	October 7-9,2009	“Geomagnetic Strom Effect on GPS Application in India”,Indo-German Conference on PDE,Scientific Computing and Optimization in applications”,Dept.of Mathematics,IIT,Kanpur, October 7-9,2009	October,2009	IC
27.	K. Rama Linga Reddy, G.R. Babu, K. Lal Kishore		Face Recognition based on Eigen Features of Multi Scaled Face Components and an Artificial Neural Network, International conference for Biometric Technology-2,3, at PSG college, coimbatore	September-2010	IC
28.	Prof. Syed Amjad Ali, Dr. Srinivasan Vathsal, Dr. K. Lal Kishore	, 11 th – 13 th June 2010	Denosing and Quality Enhancement of a CT Image using Multiwavelets and Multivariate thresholding techniques. First International Conference on Emerging Trends in Signal Processing & VLSI design (SPVL-2010) in Guru Nanak Engineering College		

29.	N.D.Bobby, Dr S.K.Srivatsa, Dr Lal Kishore,	Nov 28 to Nov 30 2011	Implementation of Radix 2 ACS in Adaptive Viterbi Decoder International Conference on Nano Science, Engineering and Technology ICONSET 2011 organised by Sathyabama University in association with Indira Gandhi Centre for Atomic Research (Ieee Explorer)		
30.	N.D.Bobby, Dr S.K.Srivatsa, Dr Lal Kishore,		Comparison of Fast ACS with Radix 2 ACS in Adaptive Viterbi Decoder International Conference on Nano Science, JNTU Technology Spectrum (processing)		
31.	N.D.Bobby, Dr S.K.Srivatsa, Dr Lal Kishore,		Design and Analysis of Viterbi and Adaptive Viterbi Decoder International Journal of Engg Research and Industrial Applications	ISSN 0974-1518, VOL3, NO.3 August 2010.	
32.	Prof.Syed Amjad Ali, Dr.Srinivasan Vathsal, Dr.K.Lal Kishore	11 th -13 th June, 2010	Denoising and quality enhancement of a CT image using multiwavelets and multivariate thresholding techniques <i>International Conference on "Emerging Trends in Signal Processing & VLSI design-SPVL-2010,(IEEE)</i>	Gurunanak Engineering College	
33.	Rajendra Prasad S, Dr. BK Madhavi and Dr. K. Lal Kishore	March 15-16, 2012	Design of a 32 nm 7T SRAM Cell based on CNTFET for Low Power Operation		
34.	P. Prasada Rao and Dr. K. Lal Kishore	Feb. 19-21, 2012	Optimizing the number of Bits/Stage in 10-bit, 50Ms/Sec Pipelined ADC Considering Area, Speed, Power and Linearity World Academy of Science, Engineering and Technology	ICCSCC 2012 KL Penang Malaysia	

National Conference (51):

1.	Prof. K. Lal Kishore	December, 1982	Role of ISTE: Proceedings of ISTE Annual Convention 1982 - Indore, India.	ISTE Procd	NC
2.	Prof. K. Lal Kishore	Feb, 28, 1993,.	"High Speed Testing and Design and Design for Testability": Seminar on Modern Trends in Digital System Design and Fault diagnosis, Hyderabad	IETE Procd Conference	NC
3.	Prof. K. Lal Kishore	December 26-28, 1993.	Continuing Education in Engineering – Necessity – Influencing Factors - XXIII Annual Convention of ISTE Mangalore	Procd ISTE	NC
4.	Prof. K. Lal Kishore	December 17-19, 1994	Need for training and retaining of Professionals to meet Technological Developments - 24 th ISTE National Convention, Regional Engineering College, Calicut	Procd ISTE	NC
5.	Prof. K. Lal Kishore	December 29-31, 1995.	Privatization of Technical Education – Implications and Global Perspective - 25 Annual Convention of ISTE, Nagpur,	Procd ISTE Conference	NC
6.	Dr. EVLN Rangacharyulu, Prof. K. Lal Kishore, Dr. Ch.DVP Paradesi	Dec. 9 – 11, 2002	Case Study in Design of Short Message Communications in Mobile Networks using Nokia/Ericson switches - National Symposium on Antennas and Propagation	CD of Conference	NC

	Rao, Dr. HSN Murthy and Dr. GR Babu				
7.	Dr. EVLN Rangacharyulu, Prof. K. Lal Kishore, Dr. Ch.DVP Paradesi Rao, Dr. PB Venkatesh, Dr. HSN Murthy and Dr. GR Babu	Dec. 19 2002	Design of MAP User Layer in SST - National Conference at NETRU, OU	Proceedings, NETRU 021 P14-15	NC
8.	Dr. EVLN Rangacharyulu, Rangacharyulu; PB Venkatesh; Prof. K. Lal Kishore; Dr. HSN Murthy	19 th December 2002.	Design of Map User Layer in SS7 – National Conference at Nehru Osmania University, ECE Department –	CD of Conference	NC
9.	Dr. EVLN Rangacharyulu, Prof. K. Lal Kishore, Dr. Ch.DVP Paradesi Rao, Dr. GR Babu Dr. PB Venkatesh, Dr. HSN Murthy and	2002 Hyderabad.	Design of voice and Data Racket Transmitter and Receiver - National Seminar of Broadcasting Engineers India 2002	Proceedings	Pages 180 – 183
10.	Dr. EVLN Rangacharyulu Prof. K. Lal Kishore et.al	July 12-13 2003, Hyderabad	Design of TDAPI in SMS - IETE Golden Jubilee Symposium	Proceedings	Page es 64 – 66
11.	Dr. EVLN Rangacharyulu Prof. K. Lal Kishore et.al	July 12-13 2003, Hyderabad	Design of Computer Interface Message distribution - IETE Golden Jubilee Symposium	CD of Conference	NC
12.	Prof. K. Lal Kishore, Paul Russel	Dec. 15-16, 2003 Hyderabad.	VLSI Design of Integrated Global Positioning System Receiver with the Emergency Locator Transmitter (ELT) of Air Craft - National Conference on “Aerospace Technology Challenges in the Millennium”	Dec, 2003	NC
13.	Sri EN Ganesh Prof. K. Lal Kishore	Feb. 11- 14 th 2005	“Quantum computing using QCE Tool” - National Conference on advanced computing at Anna University	Procd of Conference	NC
14.	Sri EN Ganesh Prof. K. Lal Kishore	June 1 – 3 2005	“Comparison of QCA and CMOS Technology” - National Conference on Signal System and communication – Anna University	Procd of Conference	NC
15.	Malleswari, Dr. K. Lal Kishore, Dr. IV Murali Krishna	21-23 June 2005	Multi-user, detection in CDMA - National Conference RACE 05 at SRKR College of Engg., Bhimavaram	June 2005	NC
16.	Smt. BK Madhavi Prof. K. Lal Kishore Prof. Subba Rao Prof. KES Murthy	3 – 4 March 2006	Low Power Techniques for CMOS Designs and design methodologies - Second National Conference on Emerging Trends in VLSI Design, PSG College of Tech., Coimbatore	Procd. NCVD 2006 , 3-4 March,	Page No. 15

17.	Smt. BK Madhavi Prof. K. Lal Kishore Prof. Subba Rao Prof. KES Murthy	3 – 4 March 2006	Design, Simulation and performance evaluation of low power, high speed ring oscillator and 2.5 GHz Ring oscillator for wireless network applications - Second National Conference on Emerging Trends in VLSI Design, PSG College of Tech., Coimbatore	Procd. NCVD 2006 ,	Page No. 20
18.	Smt. BK Madhavi Prof. K. Lal Kishore Prof. Subba Rao Prof. KES Murthy	10 – 11 March 2006 Hyderabad.	Implementation and simulation of low power high performance 8:1 submicron CMOS multiplexor for mobile communications - National Conference on wireless and mobile communications	NCWMC, 10-11 March, 2006	Page No.59
19.	Smt. BK Madhavi Prof. K. Lal Kishore Prof. Subba Rao	10 – 11 March 2006 Hyderabad.	Design and Implementation of Low Power 32 bit Shift Register using parallel procession technique	GNITS Hyd procd.	Pages 206 - 215
20.	Smt. BK Madhavi Prof. K. Lal Kishore Prof. Subba Rao T Prof. KES Murthy	27 th – 29 th March 2006 Kurnool	Performance evaluation with integrated circuit scale down - National Conference on ceramics & advanced materials organized by	NCCAM-06 27 th to 29 th March 2006	Page No.17
21.	Dr. EVLN Rangacharyulu, Prof. K. Lal Kishore et.al	24 th – 25 th June 2005	Effect of noise on Gate Optimization, power & speed with 10 million Transistors - RACE 05, SRKR Engg. College, Bhimavaram	CD	NC
22.	Sri EN Ganesh Prof. K. Lal Kishore	27 – 30 August 2006	“Nano materials for Silicon nanotechnology” - International Conference on material manufacturing and technology, Anna University	Procd of Conference	NC
23.	Dr. OBV Jagannadham, Vhishnu Charean, M. Mukunda Rao and Dr. K. Lal Kishore	Jan 3-7, 2007 Hyderabad	Ser Des Based Modulation and Demodulation for high speed inter-satellite Laser Link - International Conference ICSCI 07 Systems, Cybermatics and Informatics	Jan 2007	NC
24.	Sri CV Krishna Reddy Prof. K. Lal Kishore	February 2003	Techniques to reduce power consumption in memories - National level symposium at Bhoj Reddy College of Engg., Hyderabad	February 2003	NC
25.	Sri CV Krishna Reddy Prof. K. Lal Kishore	March 2003	Advanced techniques in Mobile communication - DIET National Conference conducted by Institution of Engineers at Warangal	March 2003	NC
26.	Sri CV Krishna Reddy Prof. K. Lal Kishore	March 2004	Performance review of CMOS, ADCs, - National Conference Gwalior	March 2004	NC
27.	Sri CV Krishna Reddy Prof. K. Lal Kishore	Dec. 2005	Technique to reduce power consumption in Flash ADCs, NCAC - PSG College of Technology, Coimbatore	Dec. 2005	NC
28.	Mr. DBV Jagannadham Dr. VK Anandan Dr. K. Lal Kishore		Analysis of Atmospheric Radar Signals Using Empirical Mode of Decomposition and the Hilbert Transform		NC
29.	K. Silvani CVK Reddy Prof. Lal Kishore		Design of 8-bit current mode high seed low power segmented DAC		NC
30.	Prof. M. Karaju Dr. AVN Tilak Dr. K. Lal Kishore K. Babu Rao	MASCOT 07	VHDL Implementation and Verification of ARINC – 429 Core – National Conference on Modeling Analysis and Simulation of Computers and Telecommunication Systems	Proceedings	P 425 – 431
31.	Prof. M. Karaju Dr. AVN Tilak Dr. K. Lal Kishore	Nce-merge-07	Programmable Digital Clock Generator (PDCG) for Low Power Microcontroller – National Conference on emerging trends in ECE	Proceedings	NC

32.	Prof. K. Venkata Ramanaiyah Dr. K. Lal Kishore Dr. P. Gopal Reddy		Hybrid Techniques For Image Compression On Neural Networks And Discrete Wavelet Transforms	(NICEJET) ISSN. No. 0973-2993	NC
33.	Mr. N. Venkateswara Rao Mr. T. Madhu Mr. GVS Padma Rao Dr. K. Lal Kishore	GNSS – 2007	Storm Time Variations of TEC data over a low latitude station – Silver National Conference on Global Navigation Satellite System and its Applications	Proceedings	NC
34.	BK Mdhavi Dr. K. Lal Kishore Dr. T. Subba Ra		Design, Simulation and Performance Evaluation of Low Power, High Speed Ring Oscillator and 2.5 GHz Ring Oscillator for Wireless Network Applications	Proceedings	NC
35.	P. Lachi Reddy Dr. K. Lal Kishore		Low Power VLSI CMOS Design an Image Processing Chip for RGB	Proceedings	NC
36.	Dr. K. Lal Kishore		Power Efficient Multilayer Neural Network for Image Compression	Proceedings	NC
37.	Dr. K. Lal Kishore		Hybrid Techniques for Image Compression Based on Neural Networks and Discrete Wavelet Transforms	Proceedings	NC
38.	DBV Jagannadham Dr. K. Lal Kishore	Technology Spectrum	Serilizer and deserilizer based modulation and demoderalation for high speed inter-sattelite link	Vol. I Nos. 2 July 2007	NC
39.	M.Kamaraju, K.Lal Kishore, A.V.N.Tilak	Proceedings ACT- 06 Vishkapatnam	Low Power ALU for High Performance Systems National Conference, ACT- 06, Andhra university, and Vishkapatnam.	8-9 December 2006	NC
40.	M.Kamaraju, K.Lal Kishore, A.V.N.Tilak	Proceedings (SECT-07) Vishakapatnam	Embedded System for Remote Control Observation Zonal Seminar on Emerging and Converging Communication Technologies(SECT-07), A.U. College of Engineering, Vishakapatnam.	10 th & 11 th February 2007	NC
41.	M.Kamaraju, K.Lal Kishore, A.V.N.Tilak	Proceedings MASCOT-07 Salem	VHDL Implementation and Verification of ARINC 429 National Conference MASCOT-07, Govt College of Engineering, Salem	1-2 March 2007	NC
42.	M.Kamaraju, K.Lal Kishore, A.V.N.Tilak	Proceedings (LPEC 16) Gudlavalleru	Low Power Embeded Chip(LPEC 16) for multiple sensor applications Natiaonal conference 2008,GEC Gudlavalleru	Nov 2008	NC
43.	M.Kamaraju, K.Lal Kishore, A.V.N.Tilak	Proceedings Natiaonal conference 2008	Embedded System for Electrical substation Natiaonal conference 2008,GEC Gudlavalleru	Nov 2008	NC
44.	Mr. N. Venkateswara Rao Mr. T. Madhu Mr. GVS Padma Rao Dr. K. Lal Kishore	AVIT,Chennai,8-9, April 2009	.Estimation of VTEC over a Low latitude station (Hyderabad) during October 2003 Strom, Second National Conference on VLSI, Embedded System , Signal Processing andCommunicationTechnologies (NCVESCO),AVIT,Chennai,8-9, April 09	April 2009	NC
45.	Mr. N. Venkateswara Rao Mr. T. Madhu	K.L.N.College of Engineering, Pottapalayam-	Estimation of VTEC over a Low latitude station (Hyderabad) during October 2003 Strom, Second National Conference on wireless Information and	April 2009	NC

	Dr. K. Lal Kishore	6306111, April 16 th -17 th 2009	Networking in Global Systems(WINGS 2009), K.L.N.College of Engineering, Pottapalayam-6306111, April 16 th -17 th 2009		
46.	Prof. Syed Amjad Ali, Dr. Srinivasan Vathsal, Dr. K. Lal Kishore	19 th – 21 st March '09, Deccan College of Engineering and Technology, Hyderabad	Construction of Multifilter coefficients and its implementation in pre-filter and MW transforms. National Conference on Futuristic Advancements on Computing & Electronics (FACE-09)		
47.	Prof. Syed Amjad Ali, Dr. Srinivasan Vathsal, Dr. K. Lal Kishore	Organized by Department of Telecommunication, May 14-15, 200	Design and Development of a Univariate and Multivariate thresholding techniques National Conference on Recent Trends in Communication Electronics and Information Technology, NACTEIT'09		
48.	N.D.Bobby, Dr S.K.Srivatsa, Dr Lalkishore		Design and Analysis of Viterbi and Adaptive Viterbi Decoder 12 th ISTE Annual convection organised by Tamilnadu and Pondicherry section at Vinayaka Missions Deemed University		
49.	Prof.Syed Amjad Ali, Dr.Srinivasan Vathsal, Dr.K.Lal Kishore	19 th -21 st May 2009	Construction of multifilter coefficients and its implementation in prefilter and MW transforms,” National Conference on Futuristic Advancements on Computing & Electronics(FACE-09)	DCET, Hyderabad, A.P, India	
50.	Prof.Syed Amjad Ali, Dr.Srinivasan Vathsal, Dr.K.Lal Kishore	14 th & 15 th May, 2009	Design and Development of a Univariate and Multivariate thresholding techniques National Conference CMRIT-09,	Bengaluru, India	
51.	Rajendra Prasad S, Dr. BK Madhavi and Dr. K. Lal Kishore	Jan, 2012	Design and Analysis of Conventional SRTAM Cell at 32 nm Technology Node for Low-Power Embedded Systems		

Membership in Professional bodies:

- Fellow of Institute of Engineers (**FIE**)
- Member of Institute of Electrical & Electronics Engineering, (**MIEEE**)
- Fellow of Institute of Electronics & Telecommunication Engineers, INDIA (**FIETE**)
- Member of India Society for Technical Education, New Delhi (**MISTE**)
- Member of International Society of Hybrid Micro Electronics (**MISHM**)
- Chairman, IETE Centre, Hyderabad News (2011)

Research Centers/Universities visited abroad:

- University of Regis (France);
- University of Hong Kong (Hong Kong);

- Defence Engineering College, University of Addis Ababa (Ethiopia)
- University of West Minister, University of Northumbria (UK)

Short Term Courses Attended:

- i. Advanced Course in Vacuum Science and Engineering – December 5 – 17, 1977, IIT, Bombay.
- ii. Advanced Instrumentation – May 29th - June 24th 1978, MIT, Madras, Chennai.
- iii. Microprocessor based System Design May 20th – 31st, IIT, Kharagpur.
- iv. International School of Sintered Materials December 6th – 9th 1983, New Delhi.
- v. Functional Approach in electronics, January 13th – 24th 1986 IISc, Bangalore.
- vi. 16-Bit, Microprocessor Systems, August 10th – 22nd 1987, CEDT, IISc, Bangalore.
- vii. Reliability Engineering, December 22nd – 28th, 1986, Jawaharlal Nehru Technological University, Hyderabad.
- viii. Design Installation and Maintenance aspects of Instrumentation in Process Industries June 7th – 17th 1989, CSFC, Training Institute, Vadodara.
- ix. Computer Control of Industrial Processes, March 30th – 31st 1990, JNTU School of P.G. Studies and Research, Hyderabad.
- x. Awareness Programme for utilization of Electronic Television facilities.
- xi. Digital Communications July 5-6 1990, JNTU College of Engineering, Hyderabad – 1990.
- xii. Automation in Industry June 14th – 19th 1993, the TATA Iron and Steel Company Ltd, Jamshedpur.
- xiii. Engineering Trends in Information Technology, July 4th – 5th 1997 IETE- CASET and IEEE, Hyderabad.
- xiv. Attended **Indo – Israeli** Conference on Information Technology, June 23-27 **1996, Jerusalem, Israel** and presented a research paper.

Other details:

1. More than **25 years teaching experience** at Graduate and Post Graduate levels.
2. Name included in the International Directory of Distinguished Leadership published by American Biographies Inc., USA.
3. Distinguished Academic Record.
4. **University Ranker in B.E.**
5. **National Merit Scholarship Holder** in B.E.

6. Coordinating the work on the establishment of Centre for Micro Electronics and **VLSI Design** in the Department.
7. Implemented the Network Scheme sanctioned by Ministry of Education, Government of India to the Department of Electronics and Communication Engineering, JNTU College of Engineering, Hyderabad.
8. Submitted proposals for Research Scheme to DOE and UGC.
9. Prepared and submitted for a proposal for starting M.Tech. Course in Micro Electronics.
10. Receiving Abstracts of Articles from National Centre for Science Information, IISC, Bangalore.
11. Prepared Experimental Boards and Procedure Sheets for Lab Experiments.
12. Conducted **Refresher Courses on VLSI Engineering** for Teachers and Engineers thrice in 2001, 2002 & 2003 at Jawaharlal Nehru Technological University, Hyderabad.
13. Worked as **Principal**, JNTU College of Engineering, Kukatpally, Hyderabad during June 2002 to 30th June 2004. The College has **won KONGU Engineering College Trophy from ISTE**, at the Sixth Annual Convention of ISTE students held during October 10th – 11th at Hyderabad under his leadership.
14. The College identified as **Lead Institute** under World Bank assistant Technical Education Quality Improvement Programme (**TEQIP**), under his stewardship. Development Plan worth about Rs. 60 Crores is proposed under this scheme.
15. Organized 6th National Convention of **ISTE Students** during October 9th –11, 2003, as **Convener**. The College was given **Certificate of Appreciation** and Medallion by ISTE at the **31st National Convention**, held at Coimbatore in December 2003.
16. Expert Committee member in many National level and state level boards and University Examinations Boards like, AICTE, DTE, UPSC etc.
17. Improved Infrastructure facilities at JNTU College of Engineering, Kukatpally, Hyderabad significantly. The College is identified as one among the top-level technical institutions, by the Expert Committee constituted by the Andhra Pradesh State Council for Higher Education, Hyderabad.
18. Construction works worth about Rs. 10.00 Crores are sanctioned to the College under his tenure.
20. Subjects handled:
 - a) VLSI Technology
 - b) Micro Electronics

- c) Advanced Instrumentation
- d) Solid State Devices
- e) Pulse and Digital Circuits
- f) Electronic Circuits
- g) Logic and Switching Circuits
- h) Power and Industrial Electronics
- i) Electronic Measurements
- j) I.C. Applications

(Dr. K. Lal Kishore)