

Format of Application
Electronics and ICT Academy
Faculty Development Program on
EMBEDDED REAL TIME SYSTEMS
(1st – 10th February 2017)

1. Name:
2. Designation:
3. Institution:
4. E-mail:
Mobile:
5. DD No: Bank: Date:
6. Address for Correspondence:
7. Educational Qualifications with specialization:
8. Subjects taught so far:
9. No. of refresher courses/workshops attended:
10. Experience (in years) :
Teaching: Research: Industry:
11. Do you belong to SC/ST (YES/NO):
If yes, enclose certificate

Declaration :

The information provided is true to the best of my knowledge. If selected, I agree to abide by the rules and regulations of the FDP and shall attend the course for the entire duration. I also undertake the responsibility to inform the Coordinator in case, I am unable to attend the course.

Place:
Date:

Signature of the applicant

Sponsorship Certificate :

Dr./Mr./Ms.....is an employee of our Institute/Organization and is hereby sponsored to participate in the FDP on Embedded Real Time Systems sponsored by Electronics & ICT Academy during 1st – 10th February 2017 at Department of ECE, JNTUA College of Engineering, Ananthapuramu – 515002(A.P).

Place:

Date:

Signature of the Principal/ Head
of Institution (With seal).

Patron : Prof. K. Prahlada Rao
Principal,
JNTUACEA

Co-Patron : Prof. M.L.S. Deva Kumar
Vice Principal,
JNTUACEA

Convenor : Prof. V. Sumalatha
Head, Department of ECE,
JNTUACEA

Co-convenor : Prof. M.N.GiriPrasad
Professor, Department of ECE,
JNTUACEA

Coordinators : Dr. T. Kishore Kumar
Head, Dept. of ECE
NIT-Warangal
Dr. G. Mamatha
Assistant Professor
JNTUACEA,
Ananthapuramu

Organizing Committee :

Dr. K. Rama Naidu, Professor
Dr. P. Ramana Reddy, Professor
Dr. S. ArunaMastani, Asst.Professor
Smt. D. Lalitha Kumari, Asst.Professor



**FACULTY DEVELOPMENT
PROGRAMME (FDP)**

On
EMBEDDED REAL TIME SYSTEMS
(1st – 10th February 2017)

Organized by
Department of ECE
JNTUA College of Engineering
Ananthapuramu
in
association with
Electronics & ICT Academy, NIT Warangal,
Telangana State
DeitY, HRD division, Ministry of ICT
Govt. of India Sponsored



JNTUA College of Engineering,
Sir Mokshagundam Vishveshwaraiah Road,
Ananthapuramu

Preamble :

“Electronics & ICT Academy” is being set up at NIT Warangal with financial assistance from DeitY, MCIT, GOI. This academy role is to offer faculty development programs in standardized courses and emerging areas of Electronics, Information, Communication Technologies; training & consultancy services for Industry; Curriculum development for Industry; CEP for working professionals; Advice and support for technical incubation and entrepreneurial activities. The jurisdiction of this academy is Telangana, Andhra Pradesh, Karnataka States and Pondicherry, Andaman & Nicobar Islands and Lakshadweep UTs.

About NIT Warangal :

National Institute of Technology, (formerly Regional Engineering College), Warangal is an Institute of National Importance and fully funded by MHRD, Govt. of India. It offers various programmes, B.Tech in eight branches, PG and M. Tech Programmes and also Ph.D. programmes.

About JNTUA CEA, Ananthapuramu:

JNTUA College of Engineering, established in the year 1946 is one of the prestigious institutions in India. The institute has a rich history and dedication to the pursuit of excellence. Over the years, there has been dynamic progress in all academic and research activities, and a parallel improvement in facilities and infrastructure, to keep it on par with the best institutions in the country.

About the host department :

The Electronics and Communication Engineering department is established in the year 1974. At present department is offering UG program in ECE and four full time PG programs in Digital Systems & Computer Electronics (DSCE), Digital Electronics & Communication Systems (DECS), VLSI System design and Embedded Systems specializations besides Ph.D. degree program in emerging areas of relevant technology. The Department has Qualified and Experienced Faculty Members with 6 Ph.Ds.

Major Course Contents :

- Introduction to Embedded Systems and its applications
- Hardware and Software Co-design
- System on chip
- Introduction to Internet of things (IoT)
- Practical Live Examples
- Getting started with Raspberry Pi
- Raspberry Pi Architecture Specifications
- Interfacing and Programming
- Introduction to IT cloud
- E-mail controlled Home Automation
- MQTT protocol

Eligibility :

The programme is open to the teachers of Engineering colleges, MCA colleges and other allied disciplines in Telangana, Andhra Pradesh, Karnataka states and Pondicherry, Andaman and Nicobar Islands, Lakshadweep UTs. Scientists, engineers and other working Professionals are also eligible.

Registration Fee Particulars :

Faculty members: Rs. 2000/-
Industry participants: Rs. 6000/-
There is no registration fee for SC/ST faculty
The participants need to send a crossed demand draft (DD) drawn in favour of “Director, NIT Warangal” and payable at SBH, NIT Warangal branch.

Accommodation :

No TA/DA will be paid to the participants. Accommodation will be provided on prior request. Working Lunch, Tea & Snacks would be provided during the training in the campus.

How to apply :

A filled-in form of application in the prescribed format duly signed and sponsored by appropriate authorities (along with demand draft) should reach the coordinator on or before the last date by post/courier. Participants are requested to write their name and contact number backside of demand draft. It is also mandatory to send scanned application form and demand draft through e-mail to gmatha.ece@jntua.ac.in as selection will be intimated only through e-mail.

Selection Criteria :

Selection will be done based on first-cum-first-serve basis and the confirmed candidates will be notified by email. The maximum number of participants will be 50 (fifty). Additionally 10 participants from industry are allowed to participate. The list of selected participants will be intimated through e-mail. In case a candidate is not selected, the demand draft will be sent back. Candidates will be issued certificates on successful completion of the course along with grade. Reservations of seats are followed for selecting candidates as per GOI norms.

Important dates :

Last date to receive application & Demand Draft :
20- 01-2017

Selection-list intimation by email : **25-01-2017**

Duration of Program : **1st – 10th February 2017**

Coordinators

Dr. T. Kishore Kumar	Dr. G. Mamatha
Head, Dept. of ECE	Assistant Professor
NIT-Warangal	JNTUACE, Ananthapuramu
kishorefr@gmail.com	gmatha.ece@jntua.ac.in

Address for correspondence :

Post your application form along with DD to
Dr. G. Mamatha,
Asst.Professor,
Dept. of ECE, JNTUA CEA,
Contact No: 09490446399

E-mail the scanned copies of filled-in and duly signed application form along with DD to
kishorefr@gmail.com,
gmatha.ece@jntua.ac.in
For more details about
Electronics and ICT academy please visit:
<http://nitw.ac.in/eict/>