JAWAHARLAL NEHRU

TECHNOLOGICAL UNIVERSITY ANANTAPUR

ANANTAPUR – 515 002 (A.P) INDIA

ACADEMIC REGULATIONS

COURSE STRUCTURE

AND

DETAILED SYLLABI

ELECTRONICS AND COMPUTER ENGINEERING

B.Tech. Regular Four Year Degree Course

(Applicable for the batches admitted from 2009-2010)

&

B.Tech. (LES) (for the batches admitted from 2010–11)

Academic Regulations 2009 for B. Tech (Regular)

(Effective for the students admitted into I year
from the Academic Year 2009-2010 onwards)

1. Award of B.Tech. Degree

A student will be declared eligible for the award of the B.Tech. Degree if he fulfils the following academic regulations:

i. Pursue a course of study for not less than four academic years and in not more than eight academic years.

ii. Register for 220 credits and secure all 220credits

2. Students, who fail to fulfil all the academic requirements for the award of the degree within eight academic years from the year of their admission, shall forfeit their seat in B.Tech course and their admission is cancelled.

3. Courses of study

The courses of study are offered at present for specialization for the B. Tech. Course:

S.No.	Branch
1.	Aeronautical Engineering.
2.	Biotechnology.
3.	Civil Engineering.
4.	Computer Science and Engineering.
5.	Computer Science and System Engineering.
6.	Electrical and Electronics Engineering.
7.	Electronics and Communication Engineering.
8.	Electronics and Computer Engineering.
9.	Electronics and Control Engineering.
10.	Electronics and Instrumentation Engineering.
11.	Information Technology.
12.	Mechanical Engineering.

and any other course as approved by the authorities of the University from time to time.

4. Credits

	I Year		Semester	
	Periods / Week	Credits	Periods / Week	Credits
Theory	03	06	03	04
	02	04	--	--
Practical	03	04	03	02
Drawing	06	06	03 06	02 04
Seminar	--	--	6	02
Project	--	--	15	10

5. Distribution and Weightage of Marks

i. The performance of a student in each semester / I year shall be evaluated subject –wise with a maximum of 100 marks for theory and 75 marks for practical subject. In addition seminar and project work shall be evaluated for 50 and 200 marks respectively.

ii. For theory subjects the distribution shall be 30 marks for Internal Evaluation and 70 marks for the End-Examination.

iii. For theory subjects, during the semester there shall be Two midterm examinations. Each mid term examination consists of objective paper for 10 marks and subjective paper for 20 marks with duration of 1hour 50 minutes (20 minutes for objective and 90 minutes for subjective paper).

Objective paper is set for 20 bits for 10 marks. Subjective paper shall contain 5 questions of which student has to answer 3 questions evaluated***** for 20 marks. First mid term examination shall be conducted for I-IV units of syllabus and second mid term examination shall be conducted for V -VIII units. The total marks secured by the student in each mid term examination for 30 marks is considered and the better of the two mid term examinations shall be taken as the final sessional marks secured by each candidate in the subject.

However for first year, there shall be Three midterm examinations as in the above pattern and the average marks of the best two midterm examinations secured in each subject shall be considered as final marks for sessionals.

*****Note 1: The subjective paper shall contain 5 questions of equal weightage of 10 marks and the marks obtained for 3questions shall be condensed to 20 marks, any fraction rounded off to the next higher mark

*****Note 2: The mid term examination shall be conducted first by distribution of the Objective paper simultaneously marking the attendance, after 20minutes the answered objective paper is collected back. The student is not allowed to leave the examination hall. Then the descriptive question paper and the answer booklet are distributed. After 90minutes the answered booklets are collected back.

iv. For practical subjects there shall be a continuous evaluation during the semester for 25 sessional marks and 50 end examination marks. Day-to-day work in the laboratory shall be evaluated for 25 marks by the concerned laboratory teacher based on the report of experiments/jobs. The end examination shall be conducted by the laboratory teacher and another examiner.

v. For the subject having design and / or drawing, such as Engineering Drawing, Machine Drawing and estimation, the distribution shall be 30 marks for internal evaluation and 70 marks for end examination. The Internal evaluation for sessionals will be 15 marks for day-to-day work in the class that shall be evaluated by the concerned subject teacher based on the reports/submissions prepared in the class. And there shall be two midterm exams in a Semester for a duration of 2hrs each, evenly distributed over the syllabi for 15 marks and the better of the two shall be considered as internal test marks. The sum of day to day evaluation and the internal test marks will be the final sessionals for the subject. However in the I year

class, there shall be three midterm exams and the average of best two will be taken into consideration.

vi. There shall be a seminar presentation in IV year II Semester. For the seminar, the student shall collect the information on a specialized topic and prepare a technical report, showing his understanding over the topic, and submit to the department before presentation. The report and the presentation shall be evaluated by the Departmental committee consisting of Head of the department, seminar supervisor and a senior faculty member. The seminar shall be evaluated for 50 marks and marks shall be submitted to the University along with internal marks. There shall be no external examination for seminar.

vii. Out of a total of 200 marks for the project work, 60 marks shall be for Internal Evaluation and 140 marks for the End Semester Examination (Viva-voce). The viva-voce shall be conducted by a committee consisting of HOD, Project Supervisor and an External Examiner nominated by the University. The evaluation of project work shall be conducted at the end of the IV year. The Internal Evaluation shall be made by the departmental committee, on the basis of two seminars given by each student on the topic of his project.

viii. Laboratory marks and the sessional marks awarded by the College are not final. They are subject to scrutiny and scaling by the University wherever necessary. In such cases, the sessional and laboratory marks awarded by the College will be referred to a Committee. The Committee will arrive at a scaling factor and the marks will be scaled as per the scaling factor. The recommendations of the Committee are final and binding.

ix. The laboratory records and internal test papers shall be preserved in the respective institutions as per the University norms and shall be produced to the Committees of the University as and when the same are asked for.

6. Attendance Requirements:

i. A student shall be eligible to appear for University examinations if he acquires a minimum of 75% of attendance in aggregate of all the subjects in a semester/ I year.

ii. **Shortage of Attendance below 65% in aggregate shall in <u>NO</u> case be condoned**.

iii. Condonation of shortage of attendance in aggregate up to 10% (65% and above and below 75%) in each semester or I year may be granted by the College Academic Committee.

iv. Students whose shortage of attendance is not condoned in any semester / I year are not eligible to take their end examination of that class and their registration shall stand cancelled.

v. A student will not be promoted to the next semester unless he satisfies the attendance requirements of the present semester / I year, as applicable. They may seek readmission for that semester / I year when offered next.

vi. A stipulated fee shall be payable towards condonation of shortage of attendance to the University.

7. Minimum Academic Requirements:

The following academic requirements have to be satisfied in addition to the attendance requirements mentioned in item no.**6**

i. A student shall be deemed to have satisfied the minimum academic requirements and earned the credits allotted to each theory, practical, design, drawing subject or project if he secures not less than 35% of marks in the end examination and a minimum of 40% of marks in the sum total of the internal evaluation and end examination taken together. In the Seminar he should secure 40%.

ii. A student shall be promoted from II to III year only if he fulfils the academic requirement of securing **40** credits from

a. One regular and one supplementary examinations of I year.
b. One regular examination of II year I semester

irrespective of whether the candidate takes the end examination or not as per the normal course of study.

iii. A student shall be promoted from third year to fourth year only if he fulfils the academic requirements of securing **68 c**redits from the following examinations,

a. Two regular and two supplementary examinations of I year.

b. Two regular and one supplementary examinations of II year I semester.
c. One regular and one supplementary examinations of II year II semester.
d. One regular examination of III year I semester.

irrespective of whether the candidate takes the end examination or not as per the normal course of study.

And in case of getting detained for want of credits by sections ii and iii above, the student may make up the credits through supplementary exams of the above exams before the date of class work commencement of Third or Fourth year I semester respectively.

iv. A student shall register and put up minimum attendance in all 220 credits and earn all the 220 credits. Marks obtained in all 220 credits shall be considered for the calculation of percentage of marks obtained.

v. Students who fail to earn 220 credits as indicated in the course structure within eight academic years from the year of their admission shall forfeit their seat in B.Tech course and their admission shall stand cancelled.

8. Course pattern:

i. The entire course of study is of four academic years. The first year shall be on yearly pattern and the second, third and fourth years on semester pattern.
ii. A student eligible to appear for the end examination in a subject, but absent at it or has failed in the end examination may appear for that subject at the next supplementary examination offered.
iii. When a student is detained due to lack of credits / shortage of attendance he may be re-admitted when the semester is offered after fulfilment of academic regulations, whereas he continues to be in the academic regulations he was first admitted.

9. Transitory Regulations:

Candidates who have been detained for want of attendance or not fulfilled academic requirements or who have failed after having undergone the course in earlier regulations or have discontinued and wish to continue the course are eligible for admission into the unfinished semester from the date of commencement of class work with the same or equivalent subjects as and when subjects are offered, subject to Section **2**. and they continues to be in the academic regulations they were first admitted.

10. With–holding of results:

If the candidate has any dues not paid to the university or if any case of indiscipline or malpractice is pending against him, the result of the candidate shall be withheld and he will not be allowed / promoted into the next higher semester. The issue of degree is liable to be withheld in such cases.

11. Award of Class:

After a student has satisfied the requirements prescribed for the completion of the program and is eligible for the award of B. Tech. Degree he shall be placed in one of the following four classes:

Class Awarded	% of marks to be secured	
First Class with Distinction	70% and above	From the aggregate marks secured for the best 220 Credits.
First Class	Below 70% but not less than 60%	
Second Class	Below 60% but not less than 50%	
Pass Class	Below 50% but not less than 40%	

(The marks in internal evaluation and end examination shall be shown separately in the marks memorandum)

12. Minimum Instruction Days:

The minimum instruction days including exams for each semester / I year shall be 90/180 days respectively.

13. There shall be no branch transfers after the completion of admission process.

14. There shall be no place transfer within the Constituent Colleges.

15. General:

i. **The academic regulations should be read as a whole for purpose of any interpretation**.

ii. **Malpractices rules- nature and punishments is appended**

iii. **Where the words "he", "him", "his", occur in the regulations, they include "she", "her", "hers".**

iv. **In the case of any doubt or ambiguity in the interpretation of the above rules, the decision of the Vice-Chancellor is final.**

v. **The University may change or amend the academic regulations or syllabi at any time and the changes or amendments shall be made applicable to all the students on roles with effect from the dates notified by the University.**

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ACADEMIC REGULATIONS FOR B. TECH. (LATERAL ENTRY SCHEME)

(Effective for the students getting admitted into II year through Lateral Entry Scheme from the Academic Year 2010-2011 and onwards)

1. Award of B.Tech. Degree

A student admitted in LES will be declared eligible for the award of the B. Tech Degree if he fulfils the following academic regulations:

i. Pursue a course of study for not less than three academic years and in not more than six academic years.

ii. Register for 168 credits and secure all 168 credits from II to IV year of Regular B.Tech. program

2. Students, who fail to fulfil the requirement for the award of the degree in six consecutive academic years from the year of admission, shall forfeit their seat.

3. The regulations **3** to **6** are to be adopted as that of B. Tech. (Regular).

7. Minimum Academic Requirements :

The following academic requirements have to be satisfied in addition to the attendance requirements mentioned in item no.**6**

i. A student shall be deemed to have satisfied the minimum academic requirements and earned the credits allotted to each theory, practical, design, drawing subject or project if he secures not less than 35% of marks in the end examination and a minimum of 40% of marks in the sum total of the internal evaluation and end examination taken together. For the Seminar he should secure 40% in the internal evaluation.

ii. A student shall be promoted from third year to fourth year only if he fulfils the academic requirements of 42 credits from the following examinations.

a. Two regular and one supplementary examinations of II year I semester.

b. One regular and one supplementary examinations of II year II semester.

c. One regular examination of III year I semester.

irrespective of whether the candidate takes the end examination or not as per the normal course of study.

and in case of getting detained for want of credits the student may make up the credits through supplementary exams of the above exams before the date of class work commencement of Fourth year I semester.

8. Course Pattern

i. The entire course of study is three academic years on semester pattern.
ii. A student eligible to appear for the end examination in a subject, but absent at it or has failed in the end examination may appear for that subject at the next supplementary examination offered.
iii. When a student is detained due to lack of credits / shortage of attendance he may be re-admitted when the semester is offered after fulfilment of academic regulations, whereas he continues to be in the academic regulations he was first admitted.

9. The regulations **9** to **10** are to be adopted as that of B. Tech. (Regular).

11. Award of Class:

After a student has satisfied the requirements prescribed for the completion of the program and is eligible for the award of B. Tech. Degree he shall be placed in one of the following four classes:

First Class with Distinction	70% and above	From the aggregate marks secured for 168 Credits. (i.e. II year to IV year)
First Class	Below 70% but not less than 60%	
Second Class	Below 60% but not less than 50%	
Pass Class	Below 50% but not less than 40%	

(The marks in internal evaluation and end examination shall be shown separately in the marks memorandum)

12.The regulations **12** to **15** are to be adopted as that of B. Tech. (Regular). All other regulations as applicable for B. Tech. Four-year degree course (Regular) will hold good for B. Tech. (Lateral Entry Scheme)

RULES FOR DISCIPLINARY ACTION FOR MALPRACTICES / IMPROPER CONDUCT IN EXAMINATIONS

	Nature of Malpractices/Improper conduct	Punishment
	If the candidate:	
1. (a)	Possesses or keeps accessible in examination hall, any paper, note book, programmable calculators, Cell phones, pager, palm computers or any other form of material concerned with or related to the subject of the examination (theory or practical) in which he is appearing but has not made use of (material shall include any marks on the body of the candidate which can be used as an aid in the subject of the examination)	Expulsion from the examination hall and cancellation of the performance in that subject only.
(b)	Gives assistance or guidance or receives it from any other candidate orally or by any other body language methods or communicates through cell phones with any candidate or persons in or outside the exam hall in respect of any matter.	Expulsion from the examination hall and cancellation of the performance in that subject only of all the candidates involved. In case of an outsider, he will be handed over to the police and a case is registered against him.
2.	Has copied in the examination hall from any paper, book, programmable calculators, palm computers or any other form of material relevant to the subject of the examination (theory or practical) in which the candidate is appearing.	Expulsion from the examination hall and cancellation of the performance in that subject and all other subjects the candidate has already appeared including practical examinations and project work and shall not be permitted to appear for the

		remaining examinations of the subjects of that Semester/year. The Hall Ticket of the candidate is to be cancelled and sent to the University.
3.	Impersonates any other candidate in connection with the examination.	The candidate who has impersonated shall be expelled from examination hall. The candidate is also debarred and forfeits the seat. The performance of the original candidate who has been impersonated, shall be cancelled in all the subjects of the examination (including practicals and project work) already appeared and shall not be allowed to appear for examinations of the remaining subjects of that semester/year. The candidate is also debarred for two consecutive semesters from class work and all University examinations. The continuation of the course by the candidate is subject to the academic regulations in connection with forfeiture of seat. If the imposter is an outsider, he will be handed over to the police and a case is registered against him.
4.	Smuggles in the Answer book or additional sheet or takes out or arranges to send out the question paper during the examination or answer book or additional sheet,	Expulsion from the examination hall and cancellation of performance in that subject and all the other subjects the candidate has already appeared

	during or after the examination.	including practical examinations and project work and shall not be permitted for the remaining examinations of the subjects of that semester/year. The candidate is also debarred for two consecutive semesters from class work and all University examinations. The continuation of the course by the candidate is subject to the academic regulations in connection with forfeiture of seat.
5.	Uses objectionable, abusive or offensive language in the answer paper or in letters to the examiners or writes to the examiner requesting him to award pass marks.	Cancellation of the performance in that subject.
6.	Refuses to obey the orders of the Chief Superintendent/Assistant – Superintendent / any officer on duty or misbehaves or creates disturbance of any kind in and around the examination hall or organizes a walk out or instigates others to walk out, or threatens the officer-in charge or any person on duty in or outside the examination hall of any injury to his person or to any of his relations whether by words, either spoken or written or by signs or by visible representation, assaults the officer-in-charge, or any person on duty in or outside the examination hall or any of his	In case of students of the college, they shall be expelled from examination halls and cancellation of their performance in that subject and all other subjects the candidate(s) has (have) already appeared and shall not be permitted to appear for the remaining examinations of the subjects of that semester/year. The candidates also are debarred and forfeit their seats. In case of outsiders, they will be handed over to the police and a police case is registered against them.

	relations, or indulges in any other act of misconduct or mischief which result in damage to or destruction of property in the examination hall or any part of the College campus or engages in any other act which in the opinion of the officer on duty amounts to use of unfair means or misconduct or has the tendency to disrupt the orderly conduct of the examination.	
7.	Leaves the exam hall taking away answer script or intentionally tears of the script or any part thereof inside or outside the examination hall.	Expulsion from the examination hall and cancellation of performance in that subject and all the other subjects the candidate has already appeared including practical examinations and project work and shall not be permitted for the remaining examinations of the subjects of that semester/year. The candidate is also debarred for two consecutive semesters from class work and all University examinations. The continuation of the course by the candidate is subject to the academic regulations in connection with forfeiture of seat.
8.	Possess any lethal weapon or firearm in the examination hall.	Expulsion from the examination hall and cancellation of the performance in that subject and all other subjects the candidate has already appeared including practical examinations and project work and shall not be

		permitted for the remaining examinations of the subjects of that semester/year. The candidate is also debarred and forfeits the seat.
9.	If student of the college, who is not a candidate for the particular examination or any person not connected with the college indulges in any malpractice or improper conduct mentioned in clause 6 to 8.	Student of the colleges expulsion from the examination hall and cancellation of the performance in that subject and all other subjects the candidate has already appeared including practical examinations and project work and shall not be permitted for the remaining examinations of the subjects of that semester/year. The candidate is also debarred and forfeits the seat. Person(s) who do not belong to the College will be handed over to police and, a police case will be registered against them.
10.	Comes in a drunken condition to the examination hall.	Expulsion from the examination hall and cancellation of the performance in that subject and all other subjects the candidate has already appeared including practical examinations and project work and shall not be permitted for the remaining examinations of the subjects of that semester/year.
11.	Copying detected on the basis of internal evidence, such as, during	Cancellation of the performance in that subject and all other

	valuation or during special scrutiny.	subjects the candidate has appeared including practical examinations and project work of that semester/year examinations.
12.	If any malpractice is detected which is not covered in the above clauses 1 to 11 shall be reported to the University for further action to award suitable punishment.	

Malpractices identified by squad or special invigilators

1. Punishments to the candidates as per the above guidelines.
2. Punishment for institutions : (if the squad reports that the college is also involved in encouraging malpractices)

(i) A show cause notice shall be issued to the college.

(ii) Impose a suitable fine on the college.

Shifting the examination centre from the college to another college for a specific period of not less than one year.

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JAWAHARLAL NEHRU TECHNOLOGICAL UNIVERSITY ANANTAPUR

Course structure for B.Tech. (Regular) I year (2009-10) for affiliated Engineering Colleges.

ELECTRONICS AND COMPUTER ENGINEERING (E.C.M)
(Common for Branches: E.C.E., E.E.E., E.I.E., C.S.E., I.T., C.S.S.E., E.Cont.E., E.C.M.)

Sl. No	Course code	Subject	Th	Tu/Drg/Lab	Credits
1.	9ABS101	English	2		4
2.	9ABS102	Engineering Physics	2		4
3.	9ABS103	Engineering Chemistry	2		4
4.	9ABS104	Mathematics – I	3	1 - -	6
5.	9A05101	Programming in C and Data Structures	3	1 - -	6
6.	9A03101	Engineering Drawing *		- 6 -	6
7.	9ABS105	Mathematical Methods	3	1 - -	6
8.	9A05102	C Programming & Data Structures Lab		- - 3	4
9.	9A03102	Engineering & I.T. Workshop #		- - 3	4
10.	9ABS106	Engineering Physics and Engineering Chemistry Lab **		- - 3	4
11.	9ABS107	English Language & Communication Skills Lab		- - 3	4
		Contact periods/week	15	3 6 12	
			Total/week	36	
Total Credits (7 Theory + 4 Labs)					52

Th = Theory; Tu = Tutorial; Drg = Drawing & Lab = Laboratory:

* Engineering Drawing will have University External Exam.

** The Students attend the Physics lab and Chemistry lab in alternate weeks. The end exam shall be conducted separately and average of the two exams will be recorded by the University exam section.

Students attend Engineering and IT work shop as a single lab every week and the end exam is conducted as a single lab. Sharing the Maximum marks and time for one task each from Engineering workshop and IT workshop. The sum of the marks awarded will be recorded

JAWAHARLAL NEHRU TECHNOLOGICAL UNIVERSITY ANANTAPUR

B.Tech II - I Semester

Code	Subject	L	T	P	C
9ABS302	Mathematics - III	4	1	-	4
9A04301	Electronic Devices and Circuits	4	1	-	4
9A02305	Electrical Circuits	4	1	-	4
9A05302	Advanced Data Structures	4	1	-	4
9ABS303	Environmental Science	4	1	-	4
9A04304	Signals & Systems	4	1	-	4
9A19301	Advanced Data Structures Lab.	-	-	3	2
9A04302	Electronic Devices and Circuits Lab.	-	-	3	2
	contact periods/week	24	6	6	
		Total/Week		30	
Total Credits (6 Theory + 2 Labs)					28

JAWAHARLAL NEHRU TECHNOLOGICAL UNIVERSITY ANANTAPUR

B.Tech II - II Semester

Code	Subject	L	T	P	C
9A02401	Principles of Electrical Engineering	4	1	-	4
9A04303	Probability Theory and Stochastic Process	4	1	-	4
9A04401	Switching Theory & Logic Design	4	1	-	4
9A04404	Pulse and Digital Circuits	4	1	-	4
9A19401	Control Systems	4	1	-	4
9A05406	Computer Organization	4	1	-	4
9A02402	Electrical Engineering Lab.		-	3	2
9A19402	Pulse and Digital Circuits Lab.		-	3	2
	contact periods/week	24	6	6	
		Total/Week		30	
Total Credits (6 Theory + 2 Labs)					28

JAWAHARLAL NEHRU TECHNOLOGICAL UNIVERSITY ANANTAPUR

B.Tech III - I Semester

S. No.	Group	Subject	L	T	P	CP
01.	9AHS401	Managerial Economics and Financial Analysis	4	0	0	4
02.	9A05501	Principles Of Programming Languages	4	0	0	4
03	9A04602	Microprocessors and Microcontrollers	4	0	0	4
04.	9A10504	Linear and Digital IC Applications	4	0	0	4
05.	9A05503	Computer Graphics	4	0	0	4
06.	9A05505	Operating Systems	4	0	0	4
07.	9A04505	Linear and Digital IC Applications Lab	0	0	3	2
08.	9A19501	Microprocessors and Interfacing Lab	0	0	3	2
		contact periods / week	24	0	6	
			Total/Week 30			
Total Credits (6 Theory + 2 Labs)						28

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B.Tech III -II Semester

S. No.	Group	Subject	L	T	P	CP
01.	9AHS701	Management Science	4	0	0	4
02.	9A05506	Computer Networks	4	0	0	4
03	9A04701	Embedded Real Time Operating Systems	4	0	0	4
04.	9A04603	Digital Signal Processing	4	0	0	4
05.	9A05502	Software Engineering	4	0	0	4
06.	9A05403	Design and Analysis of Algorithms	4	0	0	4
07.	9AHS601	Advanced English Communication Skills Lab	0	0	3	2
08.	9A05507	Computer Networks and Operating Systems Lab	0	0	3	2
		contact periods /week	24	0	6	
			Total/Week 30			
Total Credits (6 Theory + 2 Labs)						28

JAWAHARLAL NEHRU TECHNOLOGICAL UNIVERSITY ANANTAPUR

B.Tech IV - I Semester

S. No.	Group	Subject	L	T	P	CP
01.	9A04604	Electronic Measurements and Instrumentation	4	0	0	4
02.	9A12601	LINUX Programming	4	0	0	4
03	9A05401	Database Management Systems	4	0	0	4
04.	9A12501	Automata and Compiler Design	4	0	0	4
05.	9A04802 9A05605 9A10505	**Elective – I** 1. Digital Image Processing 2. Artificial Intelligence 3. Principles of Communications	4	0	0	4
06.	9A04704 9A15801 9A19701	**Elective – II** 1. DSP Processors and Architectures 2. High Speed Networking 3. Mobile Computing	4	0	0	4
07.	9A19702	Digital Signal Processing Lab	0	0	3	2
08.	9A10706	Electronic Design and Automation Lab	0	0	3	2
		contact periods /week	24	0	6	
			Total/Week 30			
Total Credits (6 Theory + 2 Labs)						28

JAWAHARLAL NEHRU TECHNOLOGICAL UNIVERSITY ANANTAPUR

B.Tech IV - II Semester

S. No.	Group	Subject	L	T	P	CP
01.	9A05704	Advanced Computer Architecture	4	0	0	4
02.	9A04605	VLSI Design	4	0	0	4
03.	9A19801 9A04806 9A05709	**Elective – III** 1. Client Server Computing 2. Wireless Communications and Networks 3. Information Security	4	0	0	4
04.	9A04706 9A19802 9A05706	**Elective – IV** 1. Digital Design Through Verilog HDL 2. Fault Tolerant Systems 3. Data Warehousing and Data Mining	4	0	0	4
05.	9A19803	Seminar	0	0	0	2
06.	9A19804	Project Work	--	--	-	10
		contact periods / week	16	0	0	
			Total/Week 16			28
Total Credits (4Theory + Seminar + Project Work)						

JAWAHARLAL NEHRU TECHNOLOGICAL UNIVERSITY ANANTAPUR

Detailed Syllabus

B.Tech. I Year (E.C.M.) **T P C**
2 0 4

(9ABS101) ENGLISH

1. INTRODUCTION :

The sweeping changes in the world have elevated English to the status of a tool of global communication and transformed it into e-English. The syllabus has been drafted to improve the competence of students in communication in general and language skills in particular. The books prescribed serve as students' handbooks.

The teacher should focus on the skills of reading, writing, listening and speaking while using the prescribed text and exercises. The classes should be interactive. The students should be encouraged to participate in the classroom proceedings and also to write short paragraphs and essays. The main aim is to encourage two way communications in place of the one-sided lecture.

The text for non-detailed study is meant for extensive reading by the students. They may be encouraged to read some select topics on their own, which could lead into a classroom discussion. In addition to the exercises from the texts done in the class, the teacher can bring variety by using authentic materials such as newspaper articles, advertisements etc.

2. OBJECTIVES:

a. To improve the language proficiency of the students in English with an emphasis on LSRW skills.
b. To equip the students to study academic subjects with greater facility through theoretical and practical components of the syllabus.
c. To develop study skills as well as communication skills in formal and informal situations.

3. SYLLABUS :

Listening Skills:

Objectives

1. To enable students to develop their listening skills so that they may appreciate its role in the LSRW skills approach to language and improve their pronunciation
2. To equip students with necessary training in listening so that they can comprehend the speech of people of different backgrounds and dialects.

Students should be given practice in listening and identifying the sounds of English language and to mark stress , right intonation in connected speech.

- Listening for general content
- Listening to fill up information
- Intensive listening
- Listening for specific information

Speaking Skills :

Objectives

1. To make students aware of the role of ability to speak fluent English and its contribution totheir success.
2. To enable students to express themselves fluently and appropriately in social and professional contexts.

- Oral practice
- Describing objects/situations/people
- Role play – Individual/Group activities
- Just A Minute (JAM) Sessions.

(Using exercises from all units of the prescribed text)

Reading Skills:

Objectives

1. To develop an awareness in the students about the significance of silent reading and comprehension.
2. To develop the ability to guess the meanings of words from context and grasp the overall message of the text, draw inferences etc.

- Skimming the text
- Understanding the gist of an argument
- Identifying the topic sentence

- Inferring lexical and contextual meaning
- Understanding discourse features
- Recognizing coherence/sequencing of sentences

The students shall be trained in reading skills using the prescribed text for detailed study. They shalll be examined in reading and answering questions using 'unseen' passages which may be taken from the non-detailed text or other authentic texts, such as articles from magazines/newspapers

Writing Skills:

Objectives

1. To develop an awareness in the students the skill to write exact and formal writing
2. To equip them with the components of different forms of writing.
   - Writing sentences
   - Use of appropriate vocabulary
   - Paragraph writing
   - Coherence and cohesiveness
   - Narration / description
   - Note Making
   - Formal and informal letter writing
   - Editing a passage

4. TEXTBOOKS PRESCRIBED:

In order to improve the proficiency of the student in the acquisition of the four skills mentioned above, the following texts and course content are prescribed and divided into Eight Units:

For Detailed study: **ENJOYING EVERYDAY ENGLISH**,
Sangam Books (India) Pvt Ltd, Hyderabad, 2009
For Non-detailed study: **INSPIRING LIVES**,
Maruti Publications, Guntur, 2009

UNIT -I

a. Heaven's Gate from **ENJOYING EVERYDAY ENGLISH**
b. Mokshagundam Visvesaraya from **INSPIRING LIVES**

UNIT -II

a. Sir C.V.Raman from **ENJOYING EVERYDAY ENGLISH**

b. Mother Teresa from **INSPIRING LIVES**

UNIT -III

a. The Connoisseur from **ENJOYING EVERYDAY ENGLISH**

b. Dr. Amartya Kumar Sen from **INSPIRING LIVES**

UNIT -IV

a. The Cuddalore Experience from **ENJOYING EVERYDAY ENGLISH**

b. Gertrude Elion from **INSPIRING LIVES**

UNIT -V

a. Bubbling Well Road from **ENJOYING EVERYDAY ENGLISH**

b. Vishwanathan Anand from **INSPIRING LIVES**

UNIT-VI

a. Odds Against Us from **ENJOYING EVERYDAY ENGLISH**

b. Charlie Chaplin from **INSPIRING LIVES**

UNIT – VII

Exercises on

Reading and Writing Skills
Reading Comprehension
Letter writing
Report writing

UNIT – VIII

Exercises on

Remedial Grammar covering Common errors in English, Subject-Verb agreement,

Use of Articles and Prepositions, Active/Passive Voice, Reported speech, Tenses

Vocabulary development covering Synonyms & Antonyms, one-word substitutes, prefixes & suffixes, Idioms & phrases, words often confused.

Evaluation: The question paper shall contain two parts, Part A containing questions from Units I- VI and Part B containing questions from units VII & VIII. The student is required to answer five full questions choosing at least one from Part B.

REFERENCES:

1. Technical Communication , Principle and Practice, Meenakshi Raman and Sangita Sharma, OUP, 2009
2. Essential Grammar in Use, (with CD) 3/e, Cambridge University Press, 2009
3. Resumes and Interviews, M.Ashraf Rizvi, Tata – McGraw Hill, 2009
4. Everyday Dialogues in English by Robert J. Dixson, Prentice-Hall of India Ltd., 2006.
5. Communication Skills for Technical Students, Farhathullah, T.M., Orient Blackswan, 2008
6. Developing Communication Skills, 2/e. by Krishna Mohan & Meera Banerji , Macmillan, 2009
7. English for Technical Communication, Vol. 1 & 2, by K. R. Lakshmi Narayanan, Sci tech. Publications.
8. Basic Communication Skills For Technology, Andrea J Ruthurford, Pearson Education , Asia.
9. Longman Dictionary of Contemporary English with DVD, Pearson Longman

JAWAHARLAL NEHRU TECHNOLOGICAL UNIVERSITY ANANTAPUR

B.Tech. I Year (E.C.M.)	**T**	**P**	**C**
	2	**0**	**4**

(9ABS102) ENGINEERING PHYSICS

UNIT I- OPTICS: Interference - Interference in thin films by reflection - Newton's rings - Diffraction - Fraunhofer diffraction at a single slit - Fraunhofer diffraction at a double slit - Diffraction grating - Grating spectrum - polarization - Nicol prism -Theory of circular and elliptical polarized light - Quarter and half wave plates.

UNIT II- CRYSTAL STRUCTURES AND X-RAY DIFFRACTION: Introduction -Space lattice - Basis - Unit cell - Lattice parameter - Bravais lattices - Crystal systems - Structure Simple cubic - Body Centered Cubic – Face Centered Cubic crystals - Miller indices of planes and directions in crystals – Separation between successive (h k l) planes - X-ray diffraction by crystal planes - Bragg's law – Laue and Powder methods.

UNIT III- PRINCIPLES OF QUANTUM MECHANICS & ELECTRON THEORY: Waves and Particles - de- Broglie's hypothesis – Heisenberg's uncertainty principle - Schroedinger's one dimensional wave equation (Time Independent) - Particle in a one dimensional potential box – Energy levels - Fermi-Dirac distribution and effect of Temperature (qualitative treatment only) – Scattering - Source of electrical resistance - Kronig-Penney model (qualitative treatment only) - energy bands – metals, semi conductors & insulators.

UNIT IV- SEMICONDUCTORS: Intrinsic and extrinsic semiconductors – Law of mass action – Continuity equation – Drift & diffusion - Einstein's relation – Hall effect – Direct & indirect band gap semiconductors – p-n junction - Band diagram of p-n junction diode – Diode Equation-LED, LCD & Photo diode.

UNIT V- MAGNETIC PROPERTIES: Introduction - Origin of magnetic moment – Classification of magnetic materials - Dia, Para , Ferro, anti-Ferro and Ferri magnetism - Hysteresis - Soft and hard magnetic materials – Magnetic bubbles memory.

DIELECTRIC PROPERTIES: Introduction - Dielectric constant - Electronic, Ionic and Orientation polarizations (qualitative treatment only) - Local field - Clausius-Mossotti equation –Frequency dependence of polarisability (qualitative treatment only) – Ferro electricity- $BaTio_3$.

UNIT VI- SUPERCONDUCTIVITY: General properties - Meissner effect - Penetration depth - Type I and Type II superconductors - Flux quantization – Josephson effects – BCS theory - Applications of superconductors.

LASERS: Introduction – Characteristics of laser - Spontaneous and stimulated emission of radiation - Einstein's coefficients - Population inversion - Ruby Laser - Helium-Neon Laser – GaAs Laser - Applications of Lasers in Industry, Scientific and Medical fields.

UNIT VII- FIBER OPTICS: Introduction - Principle of optical fiber - Acceptance angle and Acceptance cone - Numerical aperture – Types of Optical fibers and refractive index profiles – Optical fiber communication systems - Application of optical fibers.

UNIT VIII- NANOMATERIALS : Introduction - Basic principles of nano materials – Fabrication of nano materials - ball milling –plasma arching – Chemical vapour deposition method – sol-gel methods – properties of nano materials – carbon nanotubes – properties and applications of carbon nano tubes - Applications of nano materials.

TEXT BOOKS:

1. Engineering Physics by P.K.Palanisamy, Scitech Publications
2. Engineering Physics by V. Rajendran & K.Thyagarajan, Tata McGraw-Hill Publishing Co. Ltd.
3. Engineering Physics by M.R.Srinivasan New Age Publications

REFERENCES:

1. Physics Volume 2, by Halliday, Resnick and Krane; John Wiley India
2. Solid State Physics by C.Kittel, Wiley India
3. Engineering Physics by Mittal, I.K.International
4. Introduction to Nanoscience & Nano Technology by K.K Chattopadhyay & A.N. Banarjee , Prentice – Hall of India Pvt. Ltd

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B.Tech. I Year (E.C.M.)	**T**	**P**	**C**
	2	**0**	**4**

(9ABS103) ENGINEERING CHEMISTRY

UNIT I: Water: Sources of Water, Types of impurities in Water, Hardness of Water – Temporary and Permanent hardness. Units. Estimation of hardness by EDTA Method. Analysis of Water - Dissolved Oxygen. Disadvantages of Hard Water. Problems on hardness of water. Methods of Treatment of Water for Domestic Purpose – Sterilisation: Chlorination, Ozonisation.

Water for Industrial purpose - Water for Steam Making, Boiler Troubles – Carry Over (Priming and Foaming), Boiler Corrosion, Scales and Sludge, Caustic Embrittlement. Water Treatment: - Internal Treatment – Colloidal, Phosphate, Calgon, Carbonate, Sodium aluminates Conditioning of Water. External Treatment - Ion- Exchange Process; Demineralization of Brakish Water – Reverse Osmosis.

UNIT II: **Science of Corrosion:** Definition, Types of corrosion: Dry Corrosion, (Direct Chemical attack), Wet Corrosion, Theories of Corrosion and Mechanism, Electro Chemical Theory of Corrosion. Galvanic Series, Galvanic Corrosion, Concentration Cell Corrosion, Oxygen absorption type. Factors Influencing Corrosion. Control of Corrosion – Cathodic Protection – Sacrificial anode and Impressed Current. Uses of Inhibitors. Electro Plating, and Electro less plating (copper and nickel)

UNIT III: **Polymers:** Polymerization Reactions – Basic concepts. Types of Polymerization – Addition and Condensation Polymerization. Plastics – Thermosetting and Thermoplastics. Composition, Properties and Engineering Uses of the Following: Teflon, Bakelite, Nylon. Rubber – Processing of Natural Rubber and Compounding. Elastomers – Buna S, Buna N, Polyurethane Rubber; Silicone Rubber. Conducting Polymers, Synthesis and applications of Polyacetylene and Poly aniline

Liquid Crystals definition, properties, suitable examples and Engineering Applications

UNIT IV: **Chemistry of nano materials:** Nano materials definition, properties and applications;

Explosives and Propellants: Explosives, Classification, precautions during storage, blasting fuses, important explosives. Rocket propellants, classification of propellants**.**

Lubricants : Principles and function of lubricants - Classification and properties of lubricants – Viscosity, flash and fire points, cloud and pour points, aniline point, Neutralisation Number and Mechanical Strength.

UNIT V: Electro Chemistry: Conductance – Equivalent Conductance – Molecular Conductance, Conductometric Titrations – Applications of Conductivity Measurements.

Electrochemical Cells: Measurement of EMF, Standard electrode potential, concentration cells, batteries (Ni–Cd cell), Lithium batteries. Fuel cell: hydrogen oxygen fuel cell and methanol fuel cell

Insulators – Definition, Properties and Characteristics of Insulating Materials; Engineering Applications.

UNIT VI: Phase rule: Definition, Terms involved in Phase Rule and Phase rule equation. Phase diagrams – one component system (water system), two component system (lead- silver system) Eutectics, heat treatment based on iron-carbon phase diagram, hardening, annealing.

UNIT VII: Fuels and Combustion: Definition and Classification of fuels. Solid, liquid & gaseous fuels, Characteristics of a good fuel. Metallurgical Coke – Characteristics & Manufacture (Otto-Halfmann). Petroleum – Refining – Synthetic Petrol. Calorific Value & its determination (Bomb Calorimeter – Junker's Gas Calorimeter). Combustion: Flue gas analysis by Orsat's apparatus.

UNIT VIII: Building Materials: Cement: composition of Portland cement, analysis, setting and hardening of cement (reactions).

Refractories : Definition, Classification With Examples; Criteria of a Good Refractory Material; Causes for the failure of a Refractory Material

TEXT BOOKS:

1. Engineering Chemistry Prof. K.N.Jayaveera, Dr.G.V.Subba Reddy and Dr.C. Ramachandraiah, McGraw Hill Higher Education Hyd., 2009
2. A text book of Engineering Chemistry by S.S. Dara, S.Chand & Co, New Delhi (2008)
3. Text book of Engineering Chemistry by Jain & Jain, Dhanpat Rai Publishing Company, 15th edition New Delhi (2008).

REFERENCE:

1. Engineering Chemistry Dr. K. B. Chandrasekhar, Dr. U.N. Dash, Dr. Sujatha Mishra, Scitech Publications(India) Pvt. Limted, Hyderabad. 2009
2. Fuel Cells principles and applications by B.Viswanath, M.Aulice Scibioh-Universities press
3. Chemistry of Engineering Materials by C.V. Agarwal, Tara Publication, Varanasi.20084. Physical Chemistry - Glasston & Lewis.
4. Engineering Chemistry (Vol.1&2) by J C Kuriacose and J. Rajaram, Tata McGraw-Hill Co, New Delhi (2004)
5. Applied Chemistry: A Text Book for chemistry for Engineers & Technologists, G.D. Gesser, Springer, 2000

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B.Tech. I Year (E.C.M.)	**Th**	**Tu**	**C**
	3	**1**	**6**

(9ABS104) MATHEMATICS – I

UNIT I– Differential equations of first order and first degree – Exact, linear and Bernoulli equations. Applications: to Newton's law of cooling, law of natural growth and decay, orthogonal trajectories.

UNIT II– Non-homogeneous linear differential equations of second and higher order with constant coefficients with RHS term of the type e^{ax}, Sin ax, cos ax, polynomials in x, e^{ax} V(x), xV(x), method of variation of parameters.

UNIT III– Rolle's Theorem – Lagrange's Mean Value Theorem – (excluding proof). Simple examples of Taylor's and Maclaurin's Series - Functions of several variables – Jacobian – Maxima and Minima of functions of two variables, Lagrangian method of Multipliers with three variables only.

UNIT – IV

Raidus of Curvature – Curve tracing – Cartesian, polar and parametric curves. Applications of integration to lengths, volume and surface area of solids of revolution in Cartesian and polar coordinates

UNIT V– Multiple integral: – Double and triple integrals – Change of Variables – Change of order of integration.

UNIT VI– Laplace transform of standard functions – Inverse transform – First shifting Theorem, Transforms of derivatives and integrals – Unit step function – Second shifting theorem – Dirac's delta function – Convolution theorem – Laplace transform of Periodic function.

UNIT VII– Differentiation and integration of Laplace transform – Application of Laplace transforms to ordinary differential equations of first and second order.

UNIT VIII– Vector Calculus: Gradient – Divergence – Curl and Their properties; Vector integration – Line integral - Potential function – Area , Surface and volume integrals. Vector integral theorems: Green's theorem – Stoke's and Gauss's Divergence Theorem (excluding their proof). Verification of Green's–Stoke's and Gauss's Theorems.

TEXT BOOKS:

1. A Text Book of Engineering Mathematics, Vol – 1, T.K.V. Iyengar, B. Krishna Gandhi and Others, S. Chand & Company.
2. A Text Book of Engineering Mathematics, C. Sankaraiah, V.G.S. Book Links.
3. A Text Book of Engineering Mathematics-1,E. Rukmangadachari, E. Keshava Reddy, Pearson Education.

REFERENCES:

1. A Text Book of Engineering Mathematics, B.V. Ramana, Tata Mc Graw Hill.
2. A Text Book of Engineering Mathematics, Thomson Book Collection.
3. A Text Book of Advanced Engineering Mathematics – A Computer Approach, N.Bail, M.Goyal & C. Watkins.
4. Engineering Mathematics, Sarveswara Rao Koneru, Universities Press.

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B.Tech. I Year (E.C.M.)	**Th**	**Tu**	**C**
	3	**1**	**6**

(9A05101) PROGRAMMING IN C AND DATA STRUCTURES
(Common to all Branches)

UNIT I- Overview of Computers and Programming - Electronic Computers then and Now, Computer Hardware, Computer Software, Algorithm, Flowcharts, Software Development Method, Applying the Software Development Method.

UNIT II- Introduction to C Language - C Language Elements, Variable Declarations and Data Types, Executable Statements, General Form of a C Program, Expressions, Precedence and Associativity, Expression Evaluation, Operators and Expressions, Type Conversions, Decision Statements - If and Switch Statements, Loop Control Statements - while, for, do-while Statements, Nested for Loops, Other Related Statements - break, continue, goto.

UNIT III- Functions - Library Functions, Top-Down Design and Structure Charts, Functions with and without Arguments, Communications Among Functions, Scope, Storage Classes - Auto, Register, Static, Extern, Scope rules, Type Qualifiers, Recursion - Recursive Functions, Preprocessor Commands.
Arrays - Declaring and Referencing Arrays, Array Subscripts, Using For Loops for Sequential Access, Using Array Elements as Function Arguments, Arrays Arguments, Multidimensional Arrays.

UNIT IV- Pointers - Introduction, Features of Pointers, Pointer Declaration, Arithmetic Operations With Pointers, Pointers and Arrays, Pointers and Two-Dimensional Arrays, Array of Pointers, Pointers to Pointers, Void Pointers, Memory Allocation Functions, Programming Applications, Pointer to Functions, Command- Line Arguments.

Strings - String Basics, String Library Functions, Longer Strings, String Comparison, Arrays of Pointers, Character operations, String-To-Number and Number-To- String Conversions, Pointers and Strings.

UNIT V- Structure and Union – Introduction, Features of Structures, Declaration and Initialization of Structures, Structure within Structure, Array of Structures, Pointer to Structure, Structure and Functions, typedef, Bit Fields, Enumerated Data Type, Union, Union of Structures.

UNIT VI- Files - Introduction, Streams and File Types, Steps for File Operations, File I/O Structures, Read and Write, Other File function, Searching Errors in Reading/Writing of Files, Low Level Disk I/O, Command Line Arguments, Application of Command Line Arguments, File Status functions (error handling).

UNIT VII- Data Structures - Overview of Data Structure, Representation of a Stack, Stack Related Terms, Operation on a Stack, Implementation of a Stack, Representation of Arithmetic Expressions, Infix, Prefix, and Postfix Notations, Evaluation of Postfix Expression, Conversion of Expression from Infix to Postfix, Recursion, Queues - Various Positions of Queue, Representation of Queue, Insertion, Deletion, Searching Operations.

Linked List - Singly Linked List, Linked List with and without header, Insertion, Deletion and Searching Operations.

UNIT VIII- Searching and Sorting - Exchange (Bubble) Sort, Selection Sort, Quick Sort, Insertion Sort, Merge Sort. Searching- Linear and Binary Search Methods.

TEXT BOOKS :

1. Programming in C and Data Structures, J.R.Hanly, Ashok N. Kamthane and A. Ananda Rao, Pearson Education
2. C Programming & Data Structures, B.A.Forouzan and R.F. Gilberg, Third Edition, Cengage Learning.

REFERENCES :

1. Programming in C – Stephen G. Kochan, III Edition, Pearson Eductaion.

2. C Programming with problem solving, J.A. Jones & K. Harrow, Dreamtech Press
3. C and Data Structures, a snapshot oriented treatise with live engineering examples, Dr. N.B.Venkateswarlu, Dr. E.V.Prasad, S. Chand
4. C and Data Structures, E.Balaguruswamy, Tata Mc Graw Hill
5. Data Structures using C – A.M.Tanenbaum, Y.Langsam, and M.J. Augenstein, Pearson Education / PHI, Eighth Edition.

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B.Tech. I Year (E.C.M.)	**Drg**	**P**	**C**
	6	**0**	**6**

(9A03101) ENGINEERING DRAWING
(Common to all Branches)

UNIT I– INTRODUCTION TO ENGINEERING DRAWING: Principles of Engineering Graphics and their Significance – Drawing Instruments and their Use – Conventions in Drawing – Lettering – BIS Conventions. Curves used in Engineering Practice:

a) Conic Sections including the Rectangular Hyperbola – General method only.
b) Cycloid, Epicycloids and Hypocycloid
c) Involutes.
d) Helices

UNIT II– PROJECTION OF POINTS AND LINES: Principles of Orthographic Projection – Conventions – First and Third Angle Projections. Projections of Points, Lines inclined to one or both planes, Problems on projections, Finding True lengths & traces only.

UNIT III– PROJECTIONS OF PLANES: Projections of regular Plane surfaces/figures, Projection of lines and planes using auxiliary planes.

UNIT IV– PROJECTIONS OF SOLIDS: Projections of Regular Solids inclined to one or both planes – Auxiliary Views.

UNIT V– SECTIONS AND DEVELOPMENTS OF SOLIDS: Section Planes and Sectional views of Right Regular Solids–Prism, Cylinder, Pyramid and Cone. True shapes of the sections.

Development of Surfaces of Right Regular Solids – Prisms, Cylinder, Pyramid, Cone and their Sectional parts.

UNIT VI– ISOMETRIC AND ORTHOGRAPHIC PROJECTIONS: Principles of Isometric Projection – Isometric Scale – Isometric Views– Conventions – Isometric Views of Lines, Plane Figures, Simple and Compound Solids – Isometric Projection of objects having non- isometric lines. Isometric projections of spherical parts.

Conversion of Isometric projections/views to Orthographic Views – Conventions.

UNIT VII– INTERPENETRATION OF RIGHT REGULAR SOLIDS: Projections of curves of Intersection of Cylinder Vs Cylinder, Cylinder Vs Prism, Cylinder Vs Cone, Square Prism Vs Square Prism.

UNIT VIII– PERSPECTIVE PROJECTIONS: Perspective View of Plane Figures and Simple Solids.Vanishing Point Method (General Methods only).

TEXT BOOKS:

1. Engineering Drawing, N.D. Bhat, Charotar Publishers
2. Engineering Drawing, Johle, Tata McGraw-Hill
3. Engineering Drawing, Shah and Rana, 2/e, Pearson Education

REFERENCES:

1. Engineering Drawing and Graphics, Venugopal/ New age
2. Engineering Drawing, B.V.R. Guptha, J.K. Publishesrs
3. Engineering Drawing, K.L. Narayana, P. Khanniah, Scitech Pub.
4. Engineering Drawing, Venkata Reddy, B.S.Publishers.

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B.Tech. I Year (E.C.M.)	**Th**	**Tu**	**C**
	3	**1**	**6**

(9ABS105) MATHEMATICAL METHODS
(EEE, ECE, E.Con.E, E.I.E, CSE, IT, CSS, ECC)

UNIT – I

Matrices: Elementary row transformations – Rank – Echelon form, normal form – Solution of Linear System of Homogenous and Non Homogeneous equations – Direct Methods – Gauss Elimination, Gauss Jordan methods.

Eigen Values, Eigen vectors – Properties – Cayley – Hamilton Theorem – Inverse and powers of a matrix by Cayley–Hamilton theorem – Diagonolization of matrix. Calculation of powers of matrix.

UNIT – II

Real matrices – Symmetric, skew – Symmetric, orthogonal matrices Linear Transformation – Orthogonal Transformation. Complex matrices: Hermitian, Skew-Hermitian and Unitary matrices – Eigen values and Eigen vectors and their properties. Quadratic forms – Reduction of quadratic form to canonical form and their nature.

UNIT – III

Solution of Algebraic and Transcendental Equations: Introduction – The Bisection Method – The Method of False Position – The Iteration Method – Newton-Raphson Method.

Interpolation: Introduction – Finite differences – Forward Differences – backward Differences –Newton's forward and backward difference formulae for interpolation – Lagrange's Interpolation formula.

UNIT – IV

Curve fitting: Fitting a straight line – Second degree curve – Exponentional curve-Power curve by method of least squares. Numerical Differentiation and Integration – Trapezoidal rule – Simpson's 1/3 Rule – Simpson's 3/8 Rule.

UNIT – V

Numerical solution of Ordinary Differential equations: Solution by Taylor's series-Picard's Method of successive Approximations-Euler's Method-Runge-Kutta Methods – Predictor-Corrector Method – Milne's Method.

UNIT – VI

Fourier Series: Determination of Fourier coefficients – Fourier series – Even and odd functions – Fourier series in an arbitrary interval – Even and odd periodic continuation – Half-range Fourier sine and cosine expansions. Fourier integral theorem (only statement) – Fourier sine and cosine integrals. Fourier transform – Fourier sine and cosine transforms – Properties – Inverse transforms – Finite Fourier transforms.

UNIT – VII

Formation of partial differential equations by elimination of arbitrary constants and arbitrary functions – Method of separation of variables – Solutions of one dimensional wave equation, heat equation and two-dimensional Laplace's equation under initial and boundary conditions.

UNIT – VIII

z-transform – Inverse z-transform – Properties – Damping rule – Shifting rule – Initial and final value theorems. Convolution theorem – Solution of difference equations by z-transforms.

TEXT BOOKS:

1. Mathematical Methods, T.K.V. Iyengar, B. Krishna Gandhi and Others, S. Chand & Company.
2. Mathematical Methods, C. Sankaraiah, V.G.S. Book Links.
3. Mathematical Methods, G. Shanker Rao, E. Keshava Reddy, I. K. International Publishing House Pvt. Ltd.

REFERENCES:

1. Numerical Methods for Scientific and Engineering Computation , M.K. Jain, S.R.K. Iyengar R.K. Jain, New Age international Publishers.
2. Mathematical Methods – Pal – Oxford.
3. Introduction to Numerical Analysis – S.S. Sastry Ph - I
4. Mathematical Methods, S.K.V.S. Sri Ramachary, M. Bhujanga Rao, P.B. Bhaskar Rao & P.S. Subramanyam, BS Publications.

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B.Tech. I Year (E.C.M.)	**T**	**P**	**C**
	0	**3**	**4**

(9A05102) C PROGRAMMING AND DATA STRUCTURES LAB (Common to all Branches)

Objectives:

- To make the student learn a programming language.
- To teach the student to write programs in C to solve the problems.
- To introduce the student to simple linear data structures such as lists, stacks, queues.

Recommended Systems/Software Requirements:

- Intel based desktop PC with ANSI C Compiler and Supporting Editors

Week l.

a) Write a C program to find the sum of individual digits of a positive integer.

b) A Fibonacci sequence is defined as follows: the first and second terms in the sequence are 0 and 1. Subsequent terms are found by adding the preceding two terms in the sequence. Write a C program to generate the first n terms of the sequence.

c) Write a C program to generate all the prime numbers between 1 and n, where n is a value supplied by the user.

Week 2.

a) Write a C program to calculate the following Sum:
Sum=1 - $x^2/2!$ + $x^4/4!$ - $x^6/6!$ + $x^8/8!$ - $x^{10}/10!$

b) Write a C program to find the roots of a quadratic equation.

Week 3

a) Write C programs that use both recursive and non-recursive functions

i) To find the factorial of a given integer.

ii) To find the GCD (greatest common divisor) of two given integers. iii) To solve Towers of Hanoi problem.

Week 4

a) The total distance travelled by vehicle in 't' seconds is given by distance $S = ut+1/2at^2$ where 'u' and 'a' are the initial velocity (m/sec.) and acceleration (m/sec^2) respectively. Write C program to find the distance travelled at regular intervals of time given the values of 'u' and 'a'. The program should provide the flexibility to the user to select his own time intervals and repeat the calculations for different values of 'u' and 'a'.

b) Write a C program, which takes two integer operands and one operator from the user, performs the operation and then prints the result. (Consider the operators +,-,*, /, % and use Switch Statement)

Week 5

a) Write a C program to find both the largest and smallest number in a list of integers.

b) Write a C program that uses functions to perform the following:

i) Addition of Two Matrices

ii) Multiplication of Two Matrices

Week 6

a) Write a C program that uses functions to perform the following operations:

i) To insert a sub-string in to a given main string from a given position.

ii) To delete n Characters from a given position in a given string.

b) Write a C program to determine if the given string is a palindrome or not

Week 7

a) Write a C program that displays the position or index in the string S where the string T begins, or – 1 if S doesn't contain T.

b) Write a C program to count the lines, words and characters in a given text.

Week 8

a) Write a C program to generate Pascal's triangle.

b) Write a C program to construct a pyramid of numbers.

Week 9

Write a C program to read in two numbers, x and n, and then compute the sum of the geometric progression:

$1+x+x^2+x^3+\ldots\ldots\ldots\ldots+x^n$

For example: if n is 3 and x is 5, then the program computes 1+5+25+125.

Print x, n, the sum

Perform error checking. For example, the formula does not make sense for negative exponents – if n is less than 0. Have your program print an error message if n<0, then go back and read in the next pair of numbers of without computing the sum. Find if any values of x are also illegal ? If so, test for them too.

Week 10

a) 2's complement of a number is obtained by scanning it from right to left and complementing all the bits after the first appearance of a 1. Thus 2's complement of 11100 is 00100. Write a C program to find the 2's complement of a binary number.

b) Write a C program to convert a Roman numeral to its decimal equivalent.

Week 11

Write a C program that uses functions to perform the following operations:

i) Reading a complex number

ii) Writing a complex number

iii) Addition of two complex numbers

iv) Multiplication of two complex numbers

(Note: represent complex number using a structure.)

Week 12

a) Write a C program which copies one file to another.

b) Write a C program to reverse the first n characters in a file.

(Note: The file name and n are specified on the command line.)

Week 13

a) Write a C programme to display the contents of a file.

b) Write a C programme to merge two files into a third file (i.e., the contents of the first file followed by those of the second are put in the third file)

Week 14

Write a C program that uses functions to perform the following operations on singly linked list.:

i) Creation ii) Insertion iii) Deletion iv) Traversal

Week 15

Write C programs that implement stack (its operations) using

i) Arrays ii) Pointers

Week 16

Write C programs that implement Queue (its operations) using

i) Arrays ii) Pointers

Week 17

Write a C program that uses Stack operations to perform the following:

i) Converting infix expression into postfix expression

ii) Evaluating the postfix expression

Week 18

Write a C program that implements the following sorting methods to sort a given list of integers in ascending order

i) Bubble sort ii) Selection sort

Week 19

Write C programs that use both recursive and non recursive functions to perform the following searching operations for a Key value in a given list of integers:

i) Linear search ii) Binary search

Week 20
Write C program that implements the Quick sort method to sort a given list of integers in ascending order.

Week 21
Write C program that implement the Merge sort method to sort a given list of integers in ascending order.

Week 22
Write C programs to implement the Lagrange interpolation and Newton-Gregory forward interpolation.

Week 23
Write C programs to implement the linear regression and polynomial regression algorithms.

Week 24
Write C programs to implement Trapezoidal and Simpson methods.

REFERENCE BOOKS

1. Programming in C and Data Structures, J.R.Hanly, Ashok N. Kamthane and A. Ananda Rao, Pearson Education
2. The Spirit of C, an introduction to modern programming, M.Cooper, Jaico Publishing House.
3. Mastering C, K.R. Venugopal and S.R. Prasad, TMH Publications.
4. Computer Basics and C Programming, V. Rajaraman, PHI Publications.

JAWAHARLAL NEHRU TECHNOLOGICAL UNIVERSITY ANANTAPUR

B.Tech. I Year (E.C.M.)	**T**	**P**	**C**
	0	**3**	**4**

(9A03102) ENGINEERING AND I.T. WORKSHOP
(Common to all Branches)

ENGINEERING WORKSHOP

Objectives: The budding Engineer may turn out to be a technologist, scientist, entrepreneur, practitioner, consultant etc. There is a need to equip the engineer with the knowledge of common and newer engineering materials as well as shop practices to fabricate, manufacture or work with materials. Essentially he should know the labour involved, machinery or equipment necessary, time required to fabricate and also should be able to estimate the cost of the product or job work. Hence engineering work shop practice is included to introduce some common shop practices and on hand experience to appreciate the use of skill, tools, equipment and general practices to all the engineering students.

1. TRADES FOR EXERCISES:

a. Carpentry shop– Two joints (exercises) involving tenon and mortising, groove and tongue: Making middle lap T joint, cross lap joint, mortise and tenon T joint, Bridle T joint from out of 300 x 40 x 25 mm soft wood stock
b. Fitting shop– Two joints (exercises) from: square joint, V joint, half round joint or dove tail joint out of 100 x 50 x 5 mm M.S. stock
c. Sheet metal shop– Two jobs (exercises) from: Tray, cylinder, hopper or funnel from out of 22 or 20 guage G.I. sheet
d. House-wiring– Two jobs (exercises) from: wiring for ceiling rose and two lamps (bulbs) with independent switch controls with or without looping, wiring for stair case lamp, wiring for a water pump with single phase starter.
e. Foundry– Preparation of two moulds (exercises): for a single pattern and a double pattern.
f. Welding – Preparation of two welds (exercises): single V butt joint, lap joint, double V butt joint or T fillet joint

2. TRADES FOR DEMONSTRATION:

a. Plumbing
b. Machine Shop
c. Metal Cutting

Apart from the above the shop rooms should display charts, layouts, figures, circuits, hand tools, hand machines, models of jobs, materials with names such as different woods, wood faults, Plastics, steels, meters, gauges, equipment, CD or DVD displays, First aid, shop safety etc. (though they may not be used for the exercises but they give valuable information to the student). In the class work or in the examination knowledge of all shop practices may be stressed upon rather than skill acquired in making the job.

REFERENCE BOOKS:

1. Engineering Work shop practice for JNTU, V. Ramesh Babu, VRB Publishers Pvt. Ltd., 2009
2. Work shop Manual / P.Kannaiah/ K.L.Narayana/ SciTech Publishers.
3. Engineering Practices Lab Manual, Jeyapoovan, Saravana Pandian, 4/e Vikas
4. Dictionary of Mechanical Engineering, GHF Nayler, Jaico Publishing House.

I.T. WORKSHOP

Objectives:

The IT Workshop for engineers is a training lab course. The modules include training on PC Hardware, Internet & World Wide Web and Productivity tools including Word, Excel, Power Point and Publisher.

PC Hardware introduces the students to a personal computer and its basic peripherals, the process of assembling a personal computer, installation of system software like MS Windows, Linux and the required device drivers. In addition hardware and software level troubleshooting process, tips and tricks would be covered. **The students should work on a working PC (PIV or higher)to disassemble and assemble back to working condition and install Windows and Linux on the same PC.**

Students are suggested to work similar tasks in the Laptop scenario wherever possible.

Internet & World Wide Web module introduces the different ways of hooking the PC on to the internet from home and workplace for usage of the internet. Usage of web browsers, email, newsgroups and discussion forums would be covered. In addition, awareness of cyber hygiene, i.e., protecting the personal computer from getting infected with the viruses, worms and other cyber attacks would be introduced.

Productivity tools module would enable the students in crafting professional word documents, excel spread sheets, power point presentations and personal web sites using the Microsoft suite of office tools and LaTeX. (**It is recommended to use Microsoft office 2007 in place of MS Office 2003)**

PC Hardware

Week 1 – Task 1: Identify the peripherals of a computer, components in a CPU and its functions. Draw the block diagram of the CPU along with the configuration of each peripheral and submit to your instructor.

Week 2 – Task 2: Every student should disassemble and assemble the PC back to working condition. Lab instructors should verify the work and follow it up with a Viva. Also students need to go through the video which shows the process of assembling a PC. A video shall be given as part of the course content.

Week 3 – Task 3: Every student should individually install MS windows on the personal computer. Lab instructor should verify the installation and follow it up with a Viva.

Week 4 – Task 4: Every student should install Linux on the computer. This computer should have windows installed. The system should be configured as dual boot with both windows and Linux. Lab instructors should verify the installation and follow it up with a Viva

Week 5 – Task 5: Hardware Troubleshooting: Students have to be given a PC which does not boot due to improper assembly or defective

peripherals. They should identify the problem and fix it to get the computer back to working condition. The work done should be verified by the instructor and followed up with a Viva

Week 6 – Task 6: Software Troubleshooting: Students have to be given a malfunctioning CPU due to system software problems. They should identify the problem and fix it to get the computer back to working condition. The work done should be verified by the instructor and followed up with a Viva.

OFFICE TOOLS
LaTeX and Word

Week 7 – Word Orientation: The mentor needs to give an overview of LaTeX and Microsoft (MS) office 2007/ equivalent (FOSS) tool word: Importance of LaTeX and MS office 2007/ equivalent (FOSS) tool Word as word Processors, Details of the four tasks and features that would be covered in each, Using LaTeX and word – Accessing, overview of toolbars, saving files, Using help and resources, rulers, format painter in word.

Task 1 : Using LaTeX and Word to create project certificate. Features to be covered:-Formatting Fonts in word, Drop Cap in word, Applying Text effects, Using Character Spacing, Borders and Colors, Inserting Header and Footer, Using Date and Time option in both LaTeX and Word.

Excel

Week 8 - Excel Orientation: The mentor needs to tell the importance of MS office 2007/ equivalent (FOSS) tool Excel as a Spreadsheet tool, give the details of the four tasks and features that would be covered in each. Using Excel – Accessing, overview of toolbars, saving excel files, Using help and resources.

Task 1: Creating a Scheduler - Features to be covered:- Gridlines, Format Cells, Summation, auto fill, Formatting Text

LaTeX and MS/equivalent (FOSS) tool Power Point

Week 9 - Task1: Students will be working on basic power point utilities and tools which help them create basic power point presentation. Topic covered during this Exercise includes :- PPT Orientation, Slide Layouts, Inserting Text, Word Art, Formatting Text, Bullets and Numbering, Auto Shapes, Lines and Arrows in both LaTeX and Powerpoint. Students will be given model power point presentation which needs to be replicated (exactly how it's asked).

Week 10 - Task 2 : Second Exercise helps students in making their presentations interactive. Topic covered during this Exercise includes : Hyperlinks, Inserting –Images, Clip Art, Audio, Video, Objects, Tables and Charts

Internet & World Wide Web
2 Weeks

Week 11 - Task 1: **Orientation & Connectivity Boot Camp :** Students should get connected to their Local Area Network and access the Internet. In the process they configure the TCP/IP setting. Finally students should demonstrate, to the instructor, how to access the websites and email. If there is no internet connectivity preparations need to be made by the instructors to simulate the WWW on the LAN.
Web Browsers, Surfing the Web: Students customize their web browsers with the LAN proxy settings, bookmarks, search toolbars and pop up blockers.

Week 12 - Task 2: **Search Engines & Netiquette:** Students should know what search engines are and how to use the search engines. A few topics would be given to the students for which they need to search on Google. This should be demonstrated by the student to the satisfaction of instructors.

Cyber Hygiene: Students would be exposed to the various threats on the internet and would be asked to configure their computer to be safe on the

internet. They need to first install an anti virus software, configure their personal firewall and windows update on their computer.

REFERENCES :

1. Introduction to Information Technology, ITL Education Solutions limited, Pearson Education.
2. LaTeX Companion – Leslie Lamport, PHI/Pearson.
3. Introduction to Computers, Peter Norton, 6/e Mc Graw Hill
4. Upgrading and Repairing, PC's 18th e, Scott Muller QUE, Pearson Education
5. Comdex Information Technology course tool kit, Vikas Gupta, WILEY Dreamtech
6. IT Essentials PC Hardware and Software Companion Guide, Third Edition by David Anfinson and Ken Quamme. – CISCO Press, Pearson Education.

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B.Tech. I Year (E.C.M.)	**T**	**P**	**C**
	0	**3/2 each**	**4**

(9ABS106) ENGINEERING PHYSICS LAB and ENGINEERING CHEMISTRY LAB

ENGINEERING PHYSICS LAB

Any TEN of the following experiments are to be performed during the Academic year.

Sl.No. Name of the Experiment

1. Determination of wavelength of given source – spectrometer – normal incidence method.
2. Dispersive power of the prism – Spectrometer.
3. Determination of wavelength of a laser source - Diffraction Grating.
4. Determination of particle size by using a laser source.
5. Determination of thickness of a thin wire using parallel fringes.
6. Newton's Rings.
7. Magnetic field along the axis of a current carrying coil – Stewart and Gee's method.
8. Numerical aperture of an optical fiber.
9. Hall effect.
10. B – H Curve.
11. Energy gap of a material of p-n junction
12. Determination of rigidity modulus of a wire material – Torsional pendulum
13. Determination of dielectric constant.
14. Verification of laws of stretched string – Sonometer.
15. Melde's experiment – Transverse & Longitudinal modes.

Equipment required:

Spectrometer, Grating, Prism, Mercury vapour lamp, Sodium vapour lamp, Travelling Microscope, Wedge arrangement, Newton rings setup, Stewart-Gee's apparatus, He-Ne laser source, Optical fiber, Hall effect kit, B-H loop kit, Energy gap kit (four probe method), Torsional pendulum, Dielectric constant kit, Sonometer, Melde's apparatus

ENGINEERING CHEMISTRY LAB

1. Preparation of Standard Potassium Dichromate and Estimation of Ferrous Iron.
2. Preparation of Standard Potassium Dichromate and Estimation of Copper, by Iodometry.
3. Preparation of Standard EDTA solution and Estimation of Hardness of Water.
4. Preparation of Standard EDTA and Estimation of Copper
5. Determination of Manganese in Steel and Iron in Cement.
6. Determination of strength of the given Hydrochloric acid against standard sodium hydroxide solution by Conducto metric titration
7. Determination of viscosity of the oils through Redwood viscometer
8. Determination of calorific value of fuel using Bomb calorimeter
9. Estimation of dissolved oxygen
10. Determination of Eutectic Temperature of binary system (Urea – Benzoic Acid)

BOOKS:

1. Chemistry-lab manual by Dr K.N.Jayaveera and K.B. Chandra Sekhar, S.M. Enterprizes Ltd.
2. Vogel's Book of Quantitative Inorganic Analysis, ELBS Edition.

Equipment Required:

1. Glass ware: Pipettes, Burettes, Volumetric Flasks, Beakers, Standard flasks, Measuring jars, Boiling Test tubes, reagent bottles, (Borosil)
2. Analytical balance (keroy) (15 Nos)
3. Calorimeter
4. Bomb Calorimeter
5. Redwood viscometer No.1& No.2
6. Conductometer/ Conductivity bridge
7. Wash bottles, test tube stands, burette stands
8. Gas cylinders with Bunsen burners
9. Chemicals: Hydrochloric acid, sodiumhydroxide, EDTA, EBT indicator, fast sulfon black-f, urea, benzoic acid, methanol, Mohr's salt, copper sulphate, magnesium sulphate, ammonia, ammonium sulphate, calcium sulphate etc.,

JAWAHARLAL NEHRU TECHNOLOGICAL UNIVERSITY ANANTAPUR

B.Tech. I Year (E.C.M.)	**T**	**P**	**C**
	0	**3**	**4**

(9ABS107) ENGLISH LANGUAGE AND COMMUNICATION SKILLS LAB

The **Language Lab** focuses on the production and practice of sounds of language and equips students with the use of English in everyday situations and contexts.

Objectives:

1. To train students to use language effectively in everyday conversations, to participate in group discussions, to help them face interviews, and sharpen public speaking skills
2. To expose the students to a varied blend of self-instructional, learner-friendly modes of language learning
3. To enable them to learn better pronunciation through stress on word accent, intonation, and rhythm
4. To initiate them into greater use of the computer in resume preparation, report- writing, format-making etc.
5. To help the students cultivate the habit of reading passages from the computer monitor, thus providing them with the required ability to face computer-based competitive exams such GRE, TOEFL, GMAT etc.

SYLLABUS :

The following course content is prescribed for the **English Language Laboratory** sessions:

1. Introduction to the Sounds of English- Vowels, Diphthongs & Consonants.
2. Introduction to Stress and Intonation.
3. Situational Dialogues (giving directions etc.)
4. Speaking on the mobiles and telephone conversation
5. Role Play.

6. Oral Presentations- Prepared and Extempore.
7. 'Just A Minute' Sessions (JAM).
8. Describing Objects / Situations / People.
9. Information Transfer
10. Debate

Minimum Requirement:

The English Language Lab shall have two parts:

i) **The Computer aided Language Lab** for 60 students with 60 systems, one master console, LAN facility and English language software for self- study by learners.

ii) **The Communication Skills Lab** with movable chairs and audio-visual aids with a P.A System, a T. V., a digital stereo –audio & video system and camcorder etc.

System Requirement (Hardware component):

Computer network with Lan with minimum 60 multimedia systems with the following specifications:

i) P – IV Processor
a) Speed – 2.8 GHZ
b) RAM – 512 MB Minimum
c) Hard Disk – 80 GB
ii) Headphones of High quality

PRESCRIBED SOFTWARE: GLOBARENA

Suggested Software:

• Cambridge Advanced Learners' English Dictionary with CD.
• The Rosetta Stone English Library
• Clarity Pronunciation Power – Part I
• Mastering English in Vocabulary, Grammar, Spellings, Composition
• Dorling Kindersley series of Grammar, Punctuation, Composition etc.
• Language in Use, Foundation Books Pvt Ltd with CD
• Learning to Speak English - 4 CDs
• Microsoft Encarta with CD
• Murphy's English Grammar, Cambridge with CD

- English in Mind, Herbert Puchta and Jeff Stranks with Meredith Levy, Cambridge

Books Suggested for English Language Lab Library (to be located within the lab in addition to the CDs of the text book which are loaded on the systems):

1. **English Pronouncing Dictionary,** Daniel Jones Current Edition with CD.
2. **Spoken English**, R. K. Bansal and J. B. Harrison, Orient Longman 2006 Edn.
3. **Speaking English Effectively,** Krishna Mohan & NP Singh (Macmillan)
4. **A Practical Course in English Pronunciation**, (with two Audio cassettes) by J. Sethi, Kamlesh Sadanand & D.V. Jindal, Prentice-Hall of India Pvt. Ltd., New Delhi.
5. **Body Language- Your Success Mantra , Dr Shalini Verma, S.Chand & Co, 2008**
6. **English Dictionary for Advanced Learners,** (with CD) International edn. Macmillan 2009
7. **A Handbook for Englsih language Laboratories,** E.Sureshkumar, P.Sreehari, Foundation Books, 2009
8. **DELTA's key to the Next Generation TOEFL Test,** 6 audio CDS, New Age International Publishers, 2007

JAWAHARLAL NEHRU TECHNOLOGICAL UNIVERSITY ANANTAPUR

B.Tech II-I Sem. (E.C.M.)	**T**	**P**	**C**
	4	**0**	**4**

(9ABS302) MATHEMATICS – III
(Common to EEE, ECE, E Con E, E.I.E, ECM)

UNIT – I

Special Functions: Gamma and Beta Functions – their properties – Evaluation of improper integrals. Bessel functions – Properties – Recurrence relations – Orthogonal. Legendre polynomials – Properties – Rodrigue's formula – Recurrence relations – Orthogonality.

UNIT – II

Functions of a complex variable – Continuity – Differentiability – Analyticity – Properties – Cauchy-Riemann equations in Cartesian and polar coordinates. Harmonic and conjugate harmonic functions – Milne – Thompson method.

UNIT – III

Elementary functions: Exponential, trigonometric, hyperbolic functions and their properties – General power Z^C (c is complex), principal value.

UNIT – IV

Complex integration: Line integral – Evaluation along a path and by indefinite integration – Cauchy's integral theorem – Cauchy's integral formula – Generalized integral formula.

UNIT – V

Complex power series: Radius of convergence – Expansion in Taylor's series, Maclaurin's series and Laurent series. Singular point – Isolated singular point – Pole of order m – Essential singularity.

UNIT – VI

Residue – Evaluation of residue by formula and by Laurent series – Residue theorem.

Evaluation of integrals of the type

(a) improper real integrals $\int_{-\infty}^{\infty} f(x)dx$

(b) $\int_{0}^{2\pi} f(\cos\theta, \sin\theta)d\theta$

(c) $\int_{-\infty}^{\infty} e^{imx} f(x)dx$.

UNIT – VII

Argument principle – Rouche's theorem – Determination of number of zeros of complex polynomials – maximum Modulus principle – Fundamental theorem of Algebra, Liouville's Theorem.

UNIT – VIII

Conformal mapping: Transformation by e^z, Inz, z^2, Sin z, cos z, Bilinear transformation - Translation, rotation, magnification and inversion – Fixed point – Cross ratio – Determination of bilinear transformation mapping three given points.

TEXT BOOKS:

1. A Text Book of Engineering Mathematics, Vol–III, T.K.V. Iyengar, B. Krishna Gandhi and Others S. Chand & Company.
2. A text Book of Engineering Mathematics, C. Sankaraiah, V.G.S. Book Links.
3. A text Book of Engineering Mathematics-III,E. Rukmangadachari, E. Keshava Reddy, Pearson Education.

REFERENCES:

1. A Text Book of Engineering Mathematics, B.V. Ramana, Tata Mc Graw Hill.
2. Complex Variables – Chruchile and Brown.
3. Complex Variables – Schaum Series.
4. Higher Engineering Mathematics, B.S. Grewal, Khanna Publication.

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B.Tech II-I Sem. (E.C.M.)	**T**	**P**	**C**
	4	**0**	**4**

(9A04301) ELECTRONIC DEVICES AND CIRCUITS
(Common to CSE, CSSE, IT, ECE, E Con E, ECM, EIE, EEE)

UNIT- I

PN JUNCTION DIODE:

PN Junction Diode Equation, Volt-Ampere (V-I) Characteristics, Temperature Dependence of V-I Characteristics, Ideal Versus Practical, Static and Dynamic Resistances, Diode Equivalent circuits, Break down Mechanisms in semiconductor Diodes, Zener Diode Characteristics.

UNIT- II

RECTIFIERS AND FILTERS : PN Junction as a Rectifier, Half wave rectifier, ripple factor, full wave rectifier, Bridge Rectifier, Harmonic components in a rectifier circuit, Inductor filter, Capacitor filter, L-□section filter, Π- section filter, Use of Zener Diode as a Regulator, Problems on rectifier circuits, and voltage regulator.

UNIT- III

TRANSISTOR: Transistor construction, BJT Operation, BJT Symbol, Transistor as an Amplifier, Common Emitter, Common Base and Common Collector Configurations, Limits of Operation, BJT Specifications.

UNIT-IV

TRANSISTOR BIASING AND STABILISATION: Operating Point, DC and AC Load Lines, Importance of Biasing, Fixed Bias, Emitter Feedback Bias, Collector to Emitter Feedback Bias, Voltage Divider Bias, Bias Stability, Stabilization against Variations in V_{BE} and β, Bias Compensation Using Diodes and Transistors, Thermal Runaway, Condition for Thermal Stability in CE configuration, Problems on biasing circuits.

UNIT- V

FIELD EFFECT TRANSISTOR:

The Junction Field Effect Transistor (Construction, Principle of Operation, Symbol) - Pinch-Off Voltage – Volt-Ampere Characteristics, Small Signal Model of JFET & MOSFET, MOSFET Characteristics in Enhancement and Depletion Modes.

UNIT- VI

FET AMPLIFIERS:

Common Source, and Common Drain Amplifiers using FET, Generalized FET Amplifier, Biasing of FET, FET as Voltage Variable Resistor, Comparison between BJT and FET.

UNIT-VII

SMALL SIGNAL ANALYSIS OF BJT AMPLIFIERS:

BJT Modeling, Hybrid Modeling, Determination of h-Parameters from Transistor Characteristics, Measurement of h-Parameters, Analysis of CE, CB and CC configurations using h-Parameters, Comparision of CB, CE and CC configurations, Simplified Hybrid Model, Millers Theorem, Dual of Millers Theorem.

UNIT-VIII

SPECIAL PURPOSE ELECTRONIC DEVICES:

Principle of Operation, and Characteristics of Tunnel Diode (With help of Energy Band Diagram) and Varactor Diode, Principle of Operations of Schottky Barrier Diode, Thermistor, Silicon Control Rectifier, and Uni-Junction Transistor (UJT).

TEXT BOOKS:

1. Electronic Devices and Circuits - J. Millman, Christos C. Halkias, 1991 edition, 2008, TMH.
2. Electronic Devices and Circuits- R.L. Boylestad and Louis Nashelsky, 9th edition, 2006, PHI.
3. Electronic Devices and Circuits – David A. Bell, Fifth Edition, 2008, Oxford University press.

REFERENCES:

1. Integrated Electronic - J.Millman and C.C.Halkias, Satyabratajit, 2nd edition, 1998, TMH.
2. Electronic Devices and Circuits - K. Lal kishore, 2nd edition, 2005, BSP.
3. Introduction to Electronic Devices and Circuits – Rober T. Paynter, PE
4. Electronic Devices and Circuits – S. Salivahana, N.Suresh Kumar, A. Vallavaraj, 2nd Edition, 2008, TMH.

JAWAHARLAL NEHRU TECHNOLOGICAL UNIVERSITY ANANTAPUR

B.Tech II-I Sem. (E.C.M.)	**T**	**P**	**C**
	4	**0**	**4**

(9A02305) ELECTRICAL CIRCUITS
(Common to EEE, ECE, E Con E, E.I.E, ECM)

Objective:

This course introduces the basic concepts of circuit analysis which is the foundation for all subjects of the Electrical Engineering discipline. The emphasis of this course if laid on the basic analysis of circuits which includes single phase circuits, magnetic circuits, theorems, transient analysis and network topology.

UNIT-I Introduction to Electrical Circuits

Circuit concept –R-L-C parameters-Voltage and Current sources-Independent and dependent sources-source transformation-Voltage - Current relationship for passive elements (for different input signals-square, ramp, saw tooth, triangular)

UNIT-II Network Ananlysis

Kirchoff's laws – network reduction Techniques-series, parallel, series parallel, star-to-delta, delta-to-star transformation, Nodal analysis, mesh analysis, super node and super mesh for D.C excitations.

UNIT-III Single Phase A.C Circuits

R.M.S , Average values and form factor for different periodic wave forms – sinusoidal alternating quantities – Phase and Phase difference – Complex and polar forms of representations, J-notation, Steady state analysis of R,L and C (in series, parallel and series parallel combinations) with sinusoidal excitation- Concept of power factor-Concept of Reactance, Impedance, Susceptance and Admittance-Real and Reactive power, Complex Power.

UNIT-IV Locus diagrams & Resonance

Locus diagrams - series R-L, R-C, R-L-C and parallel combination with variation of various parameters - Resonance-series, parallel circuits, concept of band width and Q factor.

UNIT-V Magnetic Circuits

Magnetic circuits-Faraday's laws of electromagnetic induction-concept of self and mutual inductance,dot convention-coefficient of coupling-composite magnetic circuit-analysis of series and parallel magnetic circuits

UNIT-VI Network topology

Graph – Tree, Basic cut-set and Basic Tie-set matrices for planar networks – Loop and Nodal methods of analysis of Networks with dependent & independent voltage and current sources – Duality & Dual networks.

UNIT-VII Network theorems -I

Thevenin's, Norton's, Maximum Power Transfer and Millman's theorems for D.C and sinusoidal excitations.

UNIT-VIII Network theorems - II

Tellegen's, Superposition, Reciprocity and compensation theorems for D.C and sinusoidal excitations.

TEXT BOOKS:

1. Circuits & Networks by A. Sudhakar and Shyammohan S Palli, Tata McGraw- Hill
2. Electric Circuits by N.Sreenivasulu, REEM Publications
3. Electric Circuits- Schuam Series

REFERENCE BOOKS:

1. Network Analysis by M.E Van Valkenberg, Prentice Hall(India), 3rd Edition.
2. Basic circuit analysis by D.R. Cunningham & J.A Stuller, Jaico Publications
3. Engineering circuit analysis by William Hayt and Jack E. Kemmerly, Mc Graw Hill Company, 6th edition.

JAWAHARLAL NEHRU TECHNOLOGICAL UNIVERSITY ANANTAPUR

B.Tech II-I Sem. (E.C.M.)	**T**	**P**	**C**
	4	**0**	**4**

(9A05302) ADVANCED DATA STRUCTURES
(Common to CSE, CSSE, IT, ECM)

UNIT I :
C++ Class Overview- Class Definition, Objects, Class Members, Access Control, Class Scope, Constructors and destructors, parameter passing methods, Inline functions, static class members, this pointer, friend functions, dynamic memory allocation and deallocation (new and delete), exception handling.

UNIT II :
Function Over Loading, Operator Overloading, Generic Programming- Function and class templates, Inheritance basics, base and derived classes, inheritance types, base class access control, runtime polymorphism using virtual functions, abstract classes, streams I/O.

UNIT III :
Algorithms, performance analysis- time complexity and space complexity. Review of basic data structures- The list ADT, Stack ADT, Queue ADT, Implementation using template classes in C++.

UNIT IV :
Dictionaries, linear list representation, skip list representation, operations insertion, deletion and searching, hash table representation, hash functions, collision resolution-separate chaining, open addressing-linear probing, quadratic probing, double hashing, rehashing, extendible hashing, comparison of hashing and skip lists.

UNIT V :
Priority Queues – Definition, ADT, Realizing a Priority Queue using Heaps, Definition, insertion, Deletion, External Sorting- Model for external sorting, Multiway merge, Polyphase merge.

UNIT VI :

Search Trees (Part1):-

Binary Search Trees, Definition, ADT, Implementation, Operations-Searching, Insertion and Deletion, AVL Trees, Definition, Height of an AVL Tree, Operations – Insertion, Deletion and Searching

UNIT VII :

Search trees (prt II) : Introduction to Red –Black and Splay Trees, B-Trees, B-Tree of order m, height of a B-Tree, insertion, deletion and searching, Comparison of Search Trees

UNIT VIII :

Pattern matching and Tries : Pattern matching algorithms-Brute force, the Boyer –Moore algorithm, the Knuth-Morris-Pratt algorithm, Standard Tries, Compressed Tries, Suffix tries.

TEXT BOOKS :

1. Data structures, Algorithms and Applications in C++, S.Sahni, University Press (India) Pvt.Ltd, 2nd edition, Universities Press Orient Longman Pvt. Ltd.
2. Data structures and Algorithms in C++, Michael T.Goodrich, R.Tamassia and .Mount, Wiley student edition, John Wiley and Sons.

REFERENCES :

1. Data structures and Algorithm Analysis in C++, Mark Allen Weiss, Pearson Education. Ltd., Second Edition.
2. Data structures and algorithms in C++, 3rd Edition, Adam Drozdek, Thomson
3. Data structures using C and C++, Langsam, Augenstein and Tanenbaum, PHI.
4. Problem solving with C++, The OOP, Fourth edition, W.Savitch, Pearson education.

JAWAHARLAL NEHRU TECHNOLOGICAL UNIVERSITY ANANTAPUR

B.Tech II-I Sem. (E.C.M.)	**T**	**P**	**C**
	4	**0**	**4**

(9ABS303) ENVIRONMENTAL SCIENCE
(Common to ECE, E Con E, ECM, EIE, EEE, CSSE)

UNIT – I

MULTIDISCIPLINARY NATURE OF ENVIRONMENTAL STUDIES : – Definition, Scope and Importance – Need for Public Awareness.

UNIT – II

NATURAL RESOURCES : Renewable and non-renewable resources – Natural resources and associated problems – Forest resources – Use and over – exploitation, deforestation, case studies – Timber extraction – Mining, dams and other effects on forest and tribal people – Water resources – Use and over utilization of surface and ground water – Floods, drought, conflicts over water, dams – benefits and problems – Mineral resources: Use and exploitation, environmental effects of extracting and using mineral resources, case studies – Food resources: World food problems, changes caused by agriculture and overgrazing, effects of modern agriculture, fertilizer-pesticide problems, water logging, salinity, case studies. – Energy resources:

UNIT – III

ECOSYSTEMS : Concept of an ecosystem. – Structure and function of an ecosystem – Producers, consumers and decomposers – Energy flow in the ecosystem – Ecological sucession – Food chains, food webs and ecological pyramids – Introduction, types, characteristic features, structure and function of the following ecosystem:

a. Forest ecosystem.
b. Grassland ecosystem
c. Desert ecosystem
d. Aquatic ecosystems (ponds, streams, lakes, rivers, oceans, estuaries)

UNIT – IV

BIODIVERSITY AND ITS CONSERVATION : Introduction 0 Definition: genetic, species and ecosystem diversity – Bio-geographical classification of India – Value of biodiversity: consumptive use, Productive use, social, ethical, aesthetic and option values – Biodiversity at global, National and local levels – India as a mega-diversity nation – Hot-soports of biodiversity – Threats to biodiversity: habitat loss, poaching of wildlife, man-wildlife conflicts – Endangered and endemic species of India – Conservation of biodiversity: In-situ and Ex-situ conservation of biodiversity.

UNIT – V

ENVIRONMENTAL POLLUTION : Definition, Cause, effects and control measures of :

a. Air Pollution.
b. Water pollution
c. Soil pollution
d. Marine pollution
e. Noise pollution
f. Thermal pollution
g. Nuclear hazards

SOLID WASTE MANAGEMENT : Causes**,** effects and control measures of urban and industrial wates – Role of an individual in prevention of pollution – Pollution case studies – Disaster management: floods, earthquake, cyclone and landslides.

UNIT – VI

SOCIAL ISSUES AND THE ENVIRONMENT: From Unsustainable to Sustainable development – Urban problems related to energy – Water conservation, rain water harvesting, watershed management – Resettlement and rehabilitation of people; its problems and concerns. Case studies – Environmental ethics: Issues and possible solutions – Climate change, global warming, acid rain, ozone layer depletion, nuclear accidents and holocaust. Case Studies – Wasteland reclamation. – Consumerism and waste products. – Environment Protection Act. – Air (Prevention and Control of Pollution) Act. – Water (Prevention and

control of Pollution) Act – Wildlife Protection Act – Forest Conservation Act – Issues involved in enforcement of environmental legislation – Public awareness.

UNIT – VII

HUMAN POPULATION AND THE ENVIRONMENT : Population growth, variation among nations. Population explosion – Family Welfare Proggramme. – Environment and human health – Human Rights – Value Education – HIV/AIDS – Women and Child Welfare – Role of information Technology in Environment and human health – Case studies.

UNIT – VIII

FIELD WORK : Visit to a local area to document environmental assets River/forest grassland/hill/mountain – Visit to a local polluted site-Urban/Rural/Industrial/Agricultural Study of common plants, insects, birds – river, hill slopes, etc..

TEXT BOOKS :

1. Text book of Environmental Studies for Undergraduate Courses by Erach Bharucha for University Grants Commission, Universities Press.
2. Environmental Studies by R.Rajagopalan, Oxford University Press.
3. Environmental Studies by Benny Joseph,Mc.graHill Publications.

REFERENCES :

1. Text book of Environmental Sciences and Technology by M.Anji Reddy, BS Publication.
2. Comprehensive Environmental studies by J.P.Sharma, Laxmi publications.
3. Environmental sciences and engineering – J. Glynn Henry and Gary W. Heinke – Printice hall of India Private limited.
4. Introduction to Environmental engineering and science by Gilbert M. Masters and Wendell P. Ela - Printice hall of India Private limited.
5. Environmental Studies by Anindita Basak – Pearson Education.

JAWAHARLAL NEHRU TECHNOLOGICAL UNIVERSITY ANANTAPUR

B.Tech II-I Sem. (E.C.M.)	**T**	**P**	**C**
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(9A04304) SIGNALS AND SYSTEMS
(Common to ECE, E Con E, EIE, ECM)

UNIT I

SIGNAL ANALYSIS: Analogy between vectors and signals, Orthogonal signal space, Signal approximation using orthogonal functions, Mean square error, Closed or complete set of orthogonal functions, Orthogonality in complex functions, Exponential and sinusoidal signals, Concepts of Impulse function, Unit step function, Signum function.

UNIT II

FOURIER SERIES REPRESENTATION OF PERIODIC SIGNALS: Representation of Fourier series, Continuous time periodic signals, properties of Fourier series, Dirichlet's conditions, Trigonometric Fourier series and Exponential Fourier series, Complex Fourier spectrum.

UNIT III

FOURIER TRANSFORMS: Deriving Fourier Transform from Fourier series, Fourier transform of arbitrary signal, Fourier transform of standard signals, Fourier transform of periodic signals, properties of Fourier transforms, Fourier transforms involving impulse function and Signum function, Introduction to Hilbert Transform.

UNIT IV

SIGNAL TRANSMISSION THROUGH LINEAR SYSTEMS: Linear system, impulse response, Response of a linear system, linear time-invariant (LTI) system, linear time variant (LTV) system, Transfer function of a LTI system. Filter characteristics of linear systems. Distortion less transmission through a system, Signal bandwidth, system bandwidth, Ideal LPF, HPF and BPF characteristics, Causality and Poly-Wiener criterion for physical realization, Relationship between bandwidth and rise time.

UNIT V

SAMPLING: Sampling theorem – Graphical and analytical proof for Band Limited Signals, impulse sampling, Natural and Flat top Sampling, Reconstruction of signal from its samples, Effect of under sampling – Aliasing, Introduction to Band Pass sampling

UNIT VI

CONVOLUTION AND CORRELATION OF SIGNALS: Concept of convolution in time domain and Frequency domain, Graphical representation of convolution, Convolution property of Fourier transforms. Cross correlation and auto correlation of functions, properties of correlation function, Energy density spectrum, Parseval's theorem, Power density spectrum, Relation between auto correlation function and energy/power spectral density function, Relation between convolution and correlation, Detection of periodic signals in the presence of noise by correlation, Extraction of signal from noise by filtering.

UNIT VII

LAPLACE TRANSFORMS: Review of Laplace transforms (L.T), Partial fraction expansion, Inverse Laplace transform, Concept of region of convergence (ROC) for Laplace transforms, Constraints on ROC for various classes of signals, Properties of L.T's relation between L.T's, and F.T. of a signal. Laplace transform of certain signals using waveform synthesis.

UNIT VIII

Z–TRANSFORMS : Fundamental difference between continuous and discrete time signals, discrete time signal representation using complex exponential and sinusoidal components, Periodicity of discrete time using complex exponential signal, Concept of z-transform of a discrete sequence, Distinction between Laplace, Fourier and z-transforms, Region of convergence in z-transform, constraints on ROC for various classes of signals, Inverse z-transform, properties of z-transforms.

TEXT BOOKS:

1. Signals, Systems & Communications - B.P. Lathi, 2009,BS Publications.
2. Signals and Systems - A.V. Oppenheim, A.S. Willsky and S.H. Nawab, PHI, 2nd Edn.
3. Signals & Systems - Simon Haykin and Van Veen, Wiley, 2nd Edition.

REFERENCES:

1. Signals and Systems – A. Ramakrishna Rao - 2008, TMH.
2. Linear Systems and Signals – B. P. Lathi, Second Edition, Oxford University press, 2008.
3. Fundamentals of Signals and Systems Michel J. Robert, MGH International Edition, 2008.
4. Signals, Systems and Transforms - C. L. Philips, J. M. Parr and Eve A. Riskin, Pearson education.3rd

JAWAHARLAL NEHRU TECHNOLOGICAL UNIVERSITY ANANTAPUR

B.Tech II-I Sem. (E.C.M.)	**T**	**P**	**C**
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(9A19301) ADVANCED DATA STRUCTURES (C++) LAB

Objectives:
To make the student learn a object oriented way of solving problems.
To make the student write ADTS for all data structures.

Recommended Systems/Software Requirements:

Intel based desktop PC with minimum of 166 MHZ or faster processor with atleast 64 MB RAM and 100 MB free disk space
C++ compiler and STL Recommended

Week1 :
C++ programs to implement the following using an array.
a) Stack ADT b) Queue ADT

Week2 :
Write C++ programs to implement the following using a singly linked list.
a) Stack ADT b) Queue ADT

Week3 :
Write C++ programs to implement the deque (double ended queue) ADT using a doubly linked list and an array.

Week 4 :
Write a C++ program to perform the following operations:
a) Insert an element into a binary search tree.
b) Delete an element from a binary search tree.
c) Search for a key element in a binary search tree.

Week5 :
Write C++ programs that use non-recursive functions to traverse the given binary tree in
a) Preorder b) inorder and c) postorder.

Week6 :
Write C++ programs for the implementation of bfs and dfs for a given graph.

Week7 :
Write C++ programs for implementing the following sorting methods:
a) Merge sort b) Heap sort

Week8 :
Write a C++ program to perform the following operations
a) Insertion into a B-tree b) Deletion from a B-tree

Week9 :
Write a C++ program to perform the following operations
a) Insertion into an AVL-tree b) Deletion from an AVL-tree

Week10 :
Write a C++ program to implement all the functions of a dictionary (ADT) using hashing.

Week11 :
Write a C++ program for implementing Knuth-Morris- Pratt pattern matching algorithm.

Week12 :
Write a C++ program for implementing Boyer – Moore Patten matching algorithm

(Note: Use Class Templates in the above Programs)

TEXT BOOKS :
1. Data Structures and Algorithms in C++, Third Edition, Adam Drozdek, Thomson.
2. Data Structures using C++, D.S. Malik, Thomson

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B.Tech II-I Sem. (E.C.M.)	**T**	**P**	**C**
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(9A04302) ELECTRONIC DEVICES AND CIRCUITS LAB
(Common to ECE, E Con E, EIE, ECM, EEE)

ELECTRONIC WORKSHOP PRACTICE (in 3 lab sessions):

1. Identification, Specifications, Testing of R, L, C Components (Colour Codes), Potentiometers, Switches (SPDT, DPDT, and DIP), Coils, Gang Condensers, Relays, Bread Boards, PCB s
2. Identification, Specifications and Testing of Active Devices, Diodes, BJTs, Lowpower JFETs, MOSFETs, Power Transistors, LEDs, LCDs, SCR, UJT.
3. Study and operation of
   - Multimeters (Analog and Digital)
   - Function Generator
   - Regulated Power Supplies
   - Study and Operation of CRO.

(For Laboratory examination – Minimum of 10 experiments)

1. Forward and Reverse bias characteristics of PN Junction diode
2. Zener diode characteristics and Zener as Voltage Regulator.
3. Input and Output characteristics of Transistor in CB Configuration.
4. Input and Output characteristics of Transistor in CE Configuration.
5. Half Wave Rectifier With and without filter.
6. Full wave Rectifier With and without filter.
7. FET characteristics
8. Measurement of h parameters of transistor in CB, CE, CC configurations
9. Frequency response of CE Amplifier.
10. Frequency response of CC Amplifier.
11. Frequency response of Common Source FET Amplifier.
12. SCR Characteristics.
13. UJT Characteristics.

Equipment required for Laboratories:

1. Regulated Power supplies (RPS)	-	0-30v.
2. CROs	-	0-20M Hz.
3. Function Generators	-	0-1 M Hz.
4. Multimeters		-
5. Decade Resitance Boxes/Rheostats		-
6. Decade Capacitance Boxes		-
7. Micro Ammeters (Analog or Digital)	-	0-20 µA, 0-50µA, 0-100µA, 0-200µA.
8. Voltmeters (Analog or Digital)	-	0-50V, 0-100V, 0-250V.
9. Electronic Components	-	Resistors, Capacitors, BJTs, LCDs, SCRs, UJTs, FETs, LEDs, MOSFETs, Diodes (Ge& Si type), transistors] (NPN & PNP type)

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B.Tech II-II Sem. (E.C.M.)	**T**	**P**	**C**
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(9A02401) PRINCIPLES OF ELECTRICAL ENGINEERING (Common to ECE, E Con E, EIE, ECM)

UNIT I

TRANSIENT ANALYSIS (First and Second Order Circuits)

Transient Response of RL, RC Series, RLC Circuits for DC excitations, Initial Conditions, Solution Using Differential Equations approach and Laplace Transform Method.

UNIT II

Two Port Networks

Impedance Parameters, Admittance Parameters, Hybrid Parameters, Transmission (ABCD) Parameters, Conversion of one Parameter to another, Conditions for Reciprocity and Symmetry, Interconnection of two port networks in series, parallel and cascaded configurations, Image, Iterative impendence, Illustrative problems.

UNIT III

Filters

Classification of Filters, Filter Networks, Classification of Pass Band and Stop Band, Characteristic Impedance in the pass and stop Bands, Constant-k Low Pass Filter, High Pass Filter, m-derived, T-section, Band Pass Filter and Band Elimination filter, Illustrative problems.

UNIT IV

Symmetrical Attenuators

Symmetrical Attenuators – T-type Attenuator, π-type Attenuator, Bridged T type Attenuator, Lattice Attenuator.

UNIT V

DC Generators

Principle of Operation of DC Machines, EMF equation, Types of Generators, Magnetisation and Load Characteristics of DC Generators.

UNIT VI

DC Motors

DC Motors, Type of DC Motors, Characteristics of DC Motors, Losses and Efficiency, Swinburne's Test, Speed Control of DC Shunt Motor, Flux and Armature Voltage Control Methods.

UNIT VII

Transformers and Their Performance

Principle of Operation of Single Phase transformer, Types, Constructional Features, Phasor Diagram on NLoad and Load, Equivalent Circuit, Losses and Efficiency of Transformer and Regulation, OC and SC Tests, Predetermination of Efficiency and Regulation(Simple Problems).

UNIT VIII

Special Machines

Principle of Operation, Shaded Pole motors, Capacitor motors, AC Servomotor, AC Tachometers, Synchros, Stepper Motors, Characteristics.

TEST BOOKS:

1. Engineering Circuit Analysis – W.H.Hayt and J.E Kemmerly and S.M.Durbin, 6th Edition, 2008, TMH.
2. Fundamentals of Electric Circuits – Charles K.Alexander, Mathew N.O. Sadiku, 3rd Edition, 2008, TMH.
3. Network Analysis – A.Sudhakar, Shyammohan S.Pilli, 3rd edition, 2009, TMH.
4. Introduction to Electrical Engineering – M.S.Naidu and S.Kamakshaiah, 2008, TMH.

REFERENCE BOOKS:

1. Networks, Lines and Fields – John.D.Ryder, 2nd edition, 2008(Reprint), PHI.
2. Network Analysis and Synthesis -C.L Wadhwa, 3rd Edition, 2007, New Age International Publishers.
3. Network Analysis – N.C.Jagan and C.Lakshmi Narayana, BSP, 2006.
4. Electric Circuits – Nilsson, Riedel, 8th edithon,PE.

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B.Tech II-II Sem. (E.C.M.)	**T**	**P**	**C**
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(9A04303) PROBABILITY THEORY AND STOCHASTIC PROCESSES
(Common to ECE, E Con E, EIE, ECM)

UNIT I

PROBABILITY:

Probability introduced through Sets and Relative Frequency: Experiments and Sample Spaces, Discrete and Continuous Sample Spaces, Events, Probability Definitions and Axioms, Mathematical Model of Experiments, Probability as a Relative Frequency, Joint Probability, Conditional Probability, Total Probability, Bayes' Theorem, and Independent Events.

UNIT II

THE RANDOM VARIABLE:

Definition of a Random Variable, Conditions for a Function to be a Random Variable, Discrete, Continuous and Mixed Random Variable, Distribution and Density functions and their Properties- Binomial, Poisson, Uniform, Gaussian, Exponential, Rayleigh and Conditional Distribution, Methods of defining Conditioning Event, Conditional Density, Properties.

UNIT III

OPERATION ON ONE RANDOM VARIABLE – EXPECTATIONS:

Introduction, Expected Value of a Random Variable, Function of a Random Variable, Moments about the Origin, Central Moments, Variance and Skew, Chebychev's Inequality, Characteristic Function, Moment Generating Function, Transformations of a Random Variable: Monotonic Transformations for a Continuous Random Variable, Nonmonotonic Transformations of Continuous Random Variable, Transformation of a Discrete Random Variable.

UNIT IV

MULTIPLE RANDOM VARIABLES:

Vector Random Variables, Joint Distribution Function, Properties of Joint Distribution, Marginal Distribution Functions, Conditional Distribution and Density – Point Conditioning, Conditional Distribution and Density – Interval conditioning, Statistical Independence, Sum of Two Random Variables, Sum of Several Random Variables, Central Limit Theorem (Proof not expected), Unequal, and Equal Distributions.

UNIT V

OPERATIONS ON MULTIPLE RANDOM VARIABLES:

Expected Value of a Function of Random Variables: Joint Moments about the Origin, Joint Central Moments, Joint Characteristic Functions, Jointly Gaussian Random Variables: Two Random Variables case, N Random Variable case, Properties, Transformations of Multiple Random Variables, Linear Transformations of Gaussian Random Variables.

UNIT VI

STOCHASTIC PROCESSES:

Concept of Stochastic Process, Classification of Processes, Deterministic and Nondeterministic Processes, Distribution and Density Functions, concept of Stationarity and Statistical Independence, First-Order Stationary Processes, Second- Order and Wide-Sense Stationarity, Nth-Order and Strict-Sense Stationarity.

UNIT VII

STOCHASTIC PROCESSES-TEMPORAL CHARACTERISTICS:

Time Averages and Ergodicity, Mean-Ergodic Processes, Correlation-Ergodic Processes, Autocorrelation Function and its Properties, Cross-Correlation Function and its Properties, Covariance and its Properties, Linear system Response of Mean and Mean-Squared value, Autocorrelation Function, Cross-Correlation Functions, Gaussian Random Processes, and Poisson Random Process.

UNIT VIII

STOCHASTIC PROCESSES – SPECTRAL CHARACTERISTICS:

Power Spectrum: Properties, Relationship between Power Spectrum and Autocorrelation Function, Cross-Power Density Spectrum, Properties. Relationship between Cross-Power Spectrum and Cross-Correlation Function, Spectral Characteristics of System Response: Power Density Spectrum of Response, Cross-Power Spectral Density of Input and Output of a Linear System.

TEXT BOOKS:

1. Probability, Random Variables & Random Signal Principles - Peyton Z. Peebles, TMH, 4th Edition, 2001.
2. Probability, Random Variables and Stochastic Processes – Athanasios Papoulis and S. Unnikrishna Pillai, Tata McGraw-Hill, 4th edition, 2002.
3. Principles of Communication Systems-H.Taub. Donlad.L. Schilling, Goutam saha, 3ed. 2007, TMH.
4. Theory of Probability and Stochastic Processes-Pradip Kumar Ghosh, University Press.

REFERENCES:

1. Probability Theory and Stochastic Processes-Mallikarjuna Reddy, cengage Learning.
2. Probability and Random Processes with Application to Signal Processing – Henry Stark and John W. Woods, Pearson Education, 3rd Edition.
3. Probability Methods of Signal and System Analysis. George R. Cooper, Clave D. MC Gillem, 3rd Edition, 1999, Oxford.
4. Statistical Theory of Communication - S.P. Eugene Xavier, New Age Publications, 2003.

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B.Tech II-II Sem. (E.C.M.)	**T**	**P**	**C**
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(9A04401) SWITCHING THEORY AND LOGIC DESIGN
(Common to ECE, E Con E, EIE, ECM)

UNIT I
NUMBER SYSTEMS & CODES

Philosophy of number systems – complement representation of Negative numbers, Binary arithmetic, Binary codes, Error Detecting & Error Correcting codes, Hamming codes.

UNIT II
BOOLEAN ALGEBRA AND SWITCHING FUNCTIONS

Fundamental postulates of Boolean Algebra, Basic theorems and properties, Switching Functions, Canonical and Standard forms, Algebraic simplification Digital Logic Gates, properties of XOR gates, Universal Gates, Multilevel NAND/NOR realizations.

UNIT III
MINIMIZATION OF SWITCHING FUNCTIONS

Map Method, Prime Implicants, Don't care combinations, Minimal SOP and POS forms, Tabular Method, Prime–Implicant chart, Simplification Rules.

UNIT IV
COMBINATIONAL LOGIC DESIGN

Design using conventional Logic Gates, Encoder, Decoder, Multiplexer, De-Multiplexer, Modular design using IC chips, MUX Realization of switching functions Parity bit generator, Code-converters, Hazards and hazard free realizations.

UNIT V

PROGRAMMABLE LOGIC DEVICES, THRESHOLD LOGIC

Basic PLD's-ROM, PROM, PLA, PAL Realization of Switching functions using PLD's. Capabilities and limitations of Threshold gate, Synthesis of Threshold functions, Multigate Synthesis.

UNIT VI

SEQUENTIAL CIRCUITS - I

Classification of sequential circuits (Synchronous, Asynchronous, Pulse mode, Level mode with examples) Basic Flip-Flops, Triggering and Excitation Tables. Steps in Synchronous Sequential Circuit Design. Design of modulo-N Ring & Shift counters, Serial Binary Adder, Sequence Detector.

UNIT VII

SEQUENTIAL CIRCUITS - II

Finite state machine-capabilities and Limitations, Mealy and Moore models, Minimization of completely Specified and Incompletely Specified Sequential Machines, Partition Techniques and Merger chart Methods Concept of Minimal cover table.

UNIT VIII

ALGOROTHIMIC STATE MACHINES : Salient features of the ASM chart, Simple examples, System design using data path and control subsystems, control Implementations, Examples of Weighing machine and Binary multiplier.

TEXTBOOKS:

1. Switching & Finite Automata theory – Zvi Kohavi, TMH,2nd Edition.
2. Digital Design – Morris Mano, PHI, 3rd Edition, 2006.
3. Switching Theory and Logic Design-A.Anand kumar, 2008, PHI

REFERENCES:

1. An Engineering Approach to Digital Design – Fletcher, PHI.
2. Fundamentals of Logic Design – Charles H. Roth, 5th Edition, 2004, Thomson Publications.
3. Digital Logic Applications and Design – John M. Yarbrough, 2006, Thomson Publications.

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B.Tech II-II Sem. (E.C.M.)	**T**	**P**	**C**
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(9A04404) PULSE AND DIGITAL CIRCUITS
(Common to ECE, E Con E, EIE, ECM)

UNIT I
LINEAR WAVESHAPING
High pass, low pass RC circuits, their response for sinusoidal, step, pulse, square and ramp inputs. High Pass RC network as Differentiator, Low Pass RC network as integrator, attenuators and its applications as a CRO probe, RL and RLC circuits and their response for step input, Ringing circuit. Problem solving.

UNIT II
NON-LINEAR WAVE SHAPING
Diode clippers, Transistor clippers, Clipping at two independent levels, Comparators, applications of voltage comparators, clamping operation, clamping circuits taking source and Diode resistances into account, Clamping circuit theorem, practical clamping circuits, Effect of diode characteristics on clamping voltage, Synchronized Clamping.

UNIT III
SWITCHING CHARACTERISTICS OF DEVICES
Diode as a switch, piecewise linear diode characteristics, Diode Switching Times, Transistor as a switch, Break down voltages, transistor in saturation, temperature variations of Saturation Parameters, Transistor-Switching Times, Silicon- Controlled- Switch Circuits.

UNIT IV
MULTIVIBRATOR CIRCUITS
Analysis and Design of Bistable, Monostable, Astable Multivibrators and Schmitt trigger circuit using BJT.

UNIT V

TIME BASE GENERATORS

General features of a time base signal, methods of generating time base waveform, Miller and Bootstrap time base generators – basic principles, Transistor Miller-time base generator, Transistor Bootstrap time base generator, Transistor Current time base generators, Methods of linearity Improvements.

UNIT VI

SAMPLING GATES

Basic operating principles of sampling gates, Unidirectional and Bi-directional sampling gates, Four Diode Sampling Gate, Reduction of pedestal in gate circuits, Six Diode Gate, Application of Sampling Gates.

UNIT VII

SYNCHRONIZATION AND FREQUENCY DIVISION

Pulse Synchronization of relaxation Devices, Frequency division in sweep circuit, Stability of relaxation Devices, Astable relaxation circuits, Monostable relaxation circuits, Synchronization of a sweep circuit with symmetrical signals, Sine wave frequency division with a sweep circuit, A Sinusoidal Divider using Regeneration and Modulation.

UNIT VIII

REALIZATION OF LOGIC GATES USING DIODES & TRANSISTORS

AND, OR, & NOT gates using Diodes, and Transistors, DCTL, RTL. DTL, TTL, and CMOS Logic Families, and Comparison between the logic families.

TEXT BOOKS:

1. Millman's Pulse, Digital and Switching Waveforms – J.Millman, H.Taub and Mothiki S. Prakash Rao, 2nd Edition, 2008 TMH.
2. Solid State Pulse Circuits-David A. Bell, 4th edition, 2002 PHI.
3. Integrated Electronics – Jacob Millman, Christos C Halkias

REFERENCES:

1. Pulse and Digital Circuits – A. Anand Kumar, PHI, 2005.
2. Fundamentals of Pulse and Digital Circuits – Ronald J. Tocci, 3rd edition, 2008.
3. Pulse Circuits – Michel
4. Wave Generation and Shaping - L. Strauss.

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B.Tech II-II Sem. (E.C.M.)	**T**	**P**	**C**
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(9A19401) CONTROL SYSTEMS

Objective :

In this course it is aimed to introduce to the students the principles and applications of control systems in every day life. The basic concepts of block diagram reduction, time domain analysis solutions to time invariant systems and also deals with the different aspects of stability analysis of systems in frequency domain and time domain.

UNIT – I - INTRODUCTION

Concepts of Control Systems- Open Loop and closed loop control systems and their differences- Different examples of control systems- Classification of control systems, Feed-Back Characteristics, Effects of feedback.

Mathematical models – Differential equations, Impulse Response and transfer functions - Translational and Rotational mechanical systems

UNIT – II - TRANSFER FUNCTION REPRESENTATION

Transfer Function of DC Servo motor - AC Servo motor- Synchro transmitter and Receiver, Block diagram representation of systems considering electrical systems as examples -Block diagram algebra – Representation by Signal flow graph - Reduction using mason's gain formula.

UNIT-III - TIME RESPONSE ANALYSIS

Standard test signals - Time response of first order systems – Characteristic Equation of Feedback control systems, Transient response of second order systems - Time domain specifications – Steady state response - Steady state errors and error constants – Effects of proportional derivative, proportional integral systems.

UNIT – IV - STABILITY ANALYSIS IN S-DOMAIN

The concept of stability – Routh's stability criterion – qualitative stability and conditional stability – limitations of Routh's stability

Root Locus Technique:

The root locus concept - construction of root loci-effects of adding poles and zeros to G(s)H(s) on the root loci.

UNIT – V - FREQUENCY RESPONSE ANALYSIS

Introduction, Frequency domain specifications-Bode diagrams-Determination of Frequency domain specifications and transfer function from the Bode Diagram-Phase margin and Gain margin-Stability Analysis from Bode Plots.

UNIT – VI - STABILITY ANALYSIS IN FREQUENCY DOMAIN

Polar Plots, Nyquist Plots Stability Analysis.

UNIT – VII - CLASSICAL CONTROL DESIGN TECHNIQUES

Compensation techniques – Lag, Lead, Lead-Lag Controllers design in frequency Domain, PID Controllers.

UNIT – VIII State Space Analysis of Continuous Systems

Concepts of state, state variables and state model, derivation of state models from block diagrams, Diagonalization- Solving the Time invariant state Equations- State Transition Matrix and it's Properties – Concepts of Controllability and Observability

TEXT BOOKS:

1. Automatic Control Systems 8th edition– by B. C. Kuo 2003– John wiley and son's.,
2. Control Systems Engineering – by I. J. Nagrath and M. Gopal, New Age International (P) Limited, Publishers, 2nd edition.

REFERENCE BOOKS:

1. Modern Control Engineering – by Katsuhiko Ogata – Prentice Hall of India Pvt. Ltd., 3rd edition, 1998.
2. Control Systems by N.K.Sinha, New Age International (P) Limited Publishers, 3rd Edition, 1998.
3. Control Systems Engg. by NISE 3rd Edition – John wiley
4. " Modelling & Control Of Dynamic Systems" by Narciso F. Macia George J. Thaler, Thomson Publishers.

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B.Tech II-II Sem. (E.C.M.)	**T**	**P**	**C**
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(9A05406) COMPUTER ORGANIZATION
(Common to CSE, ECM)

UNIT-I

BASIC STRUCTURE OF COMPUTERS: Computer Types, Functional units, Basic operational concepts, Bus structures, Software, Performance, multiprocessors and multi computers.

Data types, Complements, Data Representation. Fixed Point Representation. Floating – Point Representation. Error Detection codes.

UNIT-II

REGISTER TRANSFER LANGUAGE AND MICROOPERATIONS: Register Transfer language. Register Transfer, Bus and memory transfer, Arithmetic Mircro operatiaons, logic micro operations, shift micro operations, Arithmetic logic shift unit. Instruction codes. Computer Registers Computer instructions – Instruction cycle. Memory – Reference Instructions. Input – Output and Interrupt.

CENTRAL PROCESSING UNIT - Stack organization. Instruction formats. Addressing modes. DATA Transfer and manipulation. Program control. Reduced Instruction set computer

UNIT-III

MICRO PROGRAMMED CONTROL: Control memory, Address sequencing, micro program example, Design of control unit-Hard wired control. Micro programmed control

UNIT-IV

COMPUTER ARITHMETIC : Addition and subtraction, multiplication Algorithms, Division Algorithms, Floating – point Arithmetic operations. Decimal Arithmetic unit, Decimal Arithmetic operations.

UNIT-V

THE MEMORY SYSTEM : Memory Hierarchy, Main memory, Auxiliary memory, Associative memory, Cache memory, Virtual memory, Memory management hardware

UNIT-VI

INPUT-OUTPUT ORGANIZATION : Peripheral Devices, Input-Output Interface, Asynchronous data transfer Modes of Transfer, Priority Interrupt, Direct memory Access, Input –Output Processor (IOP), Serial communication;

UNIT-VII

PIPELINE AND VECTOR PROCESSING: Parallel Processing, Pipelining, Arithmetic Pipeline, Instruction Pipeline, RISC Pipeline Vector Processing, Array Processors.

UNIT-VIII

MULTI PROCESSORS: Characteristics of Multiprocessors, Interconnection Structures, Interprocessor Arbitration. Interprocessor Communication and Synchronization, Cache Coherance.

TEXT BOOKS:

1. Computer System Architecture – M.Moris Mano, IIIrd Edition, PHI / Pearson, 2006.
2. Computer Organization – Car Hamacher, Zvonks Vranesic, Safwat Zaky, V Edition, McGraw Hill, 2002.

REFERENCE:

Computer Organization and Architecture – William Stallings Seventh Edition, PHI/Pearson, 2006.
Computer Architecture and Organization – John P. Hayes, Mc Graw Hill International editions, 1998.

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B.Tech II-II Sem. (E.C.M.)	**T**	**P**	**C**
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(9A02402) ELECTRICAL ENGINEERING LAB
(Common to ECE, E Con E, EIE, ECM)

PART – A

1. Verification of KVL and KCL.
2. Serial and Parallel Resonance – Timing, Resonant frequency, Bandwidth and Q-factor determination for RLC network.
3. Time response of first order RC/RL network for periodic non-sinusoidal inputs – time constant and steady state error determination.
4. Two port network parameters – Z-Y Parameters, chain matrix and analytical verification.
5. Two port network parameters – ABCD and h-Parameters.
6. Verification of Superposition and Reciprocity theorems.
7. Verification of maximum power transfer theorem. Verification on DC, verification on AC with Resistive and Reactive loads.
8. Experimental determination of Thevenin's and Norton's equivalent circuits and verification by direct test.
9. Constant – k Low Pass Filter and High Pass Filter – Design and Test.

PART – B

1. Magnetization characteristics of D.C. Shunt generator. Determination of critical field resistance.
2. Swinburne's Test on DC shunt machine (Predetermination of efficiency of a given DC Shunt machine working as motor and generator).
3. Brake test on DC shunt motor. Determination of performance characteristics.
4. OC & SC tests on Single-phase transformer (Predetermination of efficiency and regulation at given power factors and determination of equivalent circuit).
5. Load test on Single Phase transformer.

Note: Any **12** of the above experiments are to be conducted

JAWAHARLAL NEHRU TECHNOLOGICAL UNIVERSITY ANANTAPUR

B.Tech II-II Sem. (E.C.M.)	**T**	**P**	**C**
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(9A19402) PULSE AND DIGITAL CIRCUITS LAB

Minimum Twelve experiments to be conducted:

1. Linear wave shaping.
2. Non Linear wave shaping – Clippers.
3. Non Linear wave shaping – Clampers.
4. Transistor as a switch.
5. Study of Logic Gates & Some applications.
6. Study of Flip-Flops & some applications.
7. Sampling Gates.
8. Astable Multivibrator.
9. Monostable Multivibrator.
10. Bistable Multivibrator.
11. Schmitt Trigger.
12. UJT Relaxation Oscillator.
13. Bootstrap sweep circuit.

Equipment required for Laboratories:

1. RPS - 0 – 30 V
2. CRO - 0 – 20 M Hz.
3. Function Generators - 0 – 1 M Hz
4. Components
5. Multi Meters

JAWAHARLAL NEHRU TECHNOLOGICAL UNIVERSITY ANANTAPUR

B.Tech III-I Sem. (E.C.M.) **T P C**
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(9AHS401) MANAGERIAL ECONOMICS & FINANCIAL ANALYSIS
(Common to BOT, CE, ECM, EEE, ME)

Unit I: INTRODUCTION TO MANAGERIAL ECONOMICS
Definition, nature and scope of managerial economics- relation with other disciplines- Demand Analysis: Demand Determinants, Law of Demand and its exceptions

Unit II: ELASTICITY OF DEMAND
Definition, Types, Measurement and Significance of Elasticity of Demand. Demand forecasting, factors governing demand forecasting, methods of demand forecasting (Survey methods, Statistical methods, Expert opinion method, Test marketing, Controlled experiments, Judgmental approach to Demand Forecasting)

UNIT III :THEORY OF PRODUCTION AND COST ANALYSIS
Production Function – Isoquants and Isocosts, MRTS, least cost combination of inputs, Cobb-Douglas production function, laws of returns, internal and external economies of scale.

Cost Analysis: Cost concepts, opportunity cost, fixed Vs variable costs, explicit costs Vs Implicit costs, out of pocket costs Vs Imputed costs. Break-Even Analysis (BEA) - Determination of Break Even Point (Simple Problems)- Managerial significance and limitations of BEA.

UNIT IV: INTRODUCTION TO MARKETS AND PRICING POLICIES
Market structures: Types of competition, features of perfect competition, monopoly- monopolistic competition. Price-Output determination under perfect competition and monopoly - Methods of Pricing-cost plus pricing, marginal cost, limit pricing, skimming pricing, bundling pricing, sealed bid pricing and peak load pricing.

UNIT V: BUSINESS ORGANISATIONS AND NEW ECONOMIC ENVIRONMENT

Characteristic features of business, features and evaluation of sole proprietorship, partnership, Joint Stock Company, public enterprises and their types, changing business environment in post-liberalization scenario.

UNIT VI: CAPITAL AND CAPITAL BUDGETING

Capital and its significance, types of capital, estimation of fixed and working capital requirements, methods and sources of raising finance.

Nature and scope of capital budgeting, features of capital budgeting proposal, methods of capital budgeting – payback method, accounting rate of return (ARR) and Net present value method (Simple problems).

UNIT VII: INTRODUCTION TO FINANCIAL ACCOUNTING

Double-Entry Book Keeping, Journal, Ledger, Trial Balance- Final Accounts (Trading Account, Profit and Loss Account and Balance Sheet with simple adjustments).

UNIT VIII: FINANCIAL ANALYSIS THROUGH RATIOS

Computation, Analysis and Interpretation of financial statements through Liquidity Ratios (Current and Quick ratio), Activity ratios (Inventory Turnover Ratio and Debtor Turnover Ratio), Capital Structure Ratios (Debt- Equity Ratio, Interest Coverage Ratio) and Profitability ratios (Gross Profit Ratio, Net Profit Ratio, Operating Ratio, P/E Ratios and EPS), Du Pont Chart.

TEXT BOOKS:

1. Aryasri: Managerial Economics and Financial Analysis, 4/e, TMH, 2009.
2. Varshney & Maheswari: Managerial Economics, Sultan Chand, 2009.

REFERENCES

1. Premchand Babu, Madan Mohan:Financial Accounting and Analysis,Himalaya, 2009
2. S.A. Siddiqui and A.S. Siddiqui: Managerial Economics and Financial Analysis, New Age International,. 2009.

3. Joseph G. Nellis and David Parker: Principles of Business Economics, Pearson, 2/e, New Delhi.
4. Domnick Salvatore: Managerial Economics in a Global Economy, Cengage, 2009.
5. H.L.Ahuja: Managerial Economics, S.Chand, 3/e, 2009

JAWAHARLAL NEHRU TECHNOLOGICAL UNIVERSITY ANANTAPUR

(9A05501) PRINCIPLES OF PROGRAMMING LANGUAGES
(Common to CSE, ECM)

B.Tech III-I Sem. (E.C.M.)	**T**	**P**	**C**
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UNIT I
PRELIMINARY CONCEPTS:
Reasons for studying, concepts of programming languages, Programming domains, Language Evaluation Criteria, influences on Language design, Language categories, Programming Paradigms: Imperative, Object Oriented, functional Programming, Logic Programming. Programming Language Implementation, Compilation and Virtual Machines, programming environments.

UNIT II
SYNTAX AND SEMANTICS:
General Problem of describing Syntax and Semantics, formal methods of describing syntax, BNF, EBNF for common programming languages features, parse trees, ambiguous grammars, attribute grammars, denotational semantics and axiomatic semantics for common programming language features.

UNIT III
DATA TYPES:
Introduction, primitive, character, user defined, array, associative, record, union, pointer and reference types, design and implementation uses related to these types. Names, Variable, concept of binding, type checking, strong typing, type compatibility, named constants, variable initialization.

UNIT IV
EXPRESSIONS AND STATEMENTS:
Arithmetic relational and Boolean expressions, Short circuit evaluation mixed mode assignment, Assignment Statements, Control Structures: Statement Level, Compound Statements, Selection, Iteration, Unconditional Statements, guarded commands.

UNIT V
SUBPROGRAMS AND BLOCKS:
Fundamentals of sub-programs, Scope and lifetime of variable, static and dynamic scope, Design issues of subprograms and operations, local referencing environments, parameter passing methods, overloaded sub-programs, generic sub-programs, parameters that are sub-program names, design issues for functions user defined overloaded operators, co routines.

UNIT VI
ABSTRACT DATA TYPES:
Abstractions and encapsulation, introductions to data abstraction, design issues, language examples, C++ parameterized ADT, object oriented programming in small talk, C++, Java, C#, Ada 95, Concurrency: Subprogram level concurrency, semaphores, monitors, massage passing, Java threads, C# threads.

UNIT VII
EXCEPTION HANDLING:
Exceptions, exception Propagation, Exception handler in Ada, C++ and Java. Logic Programming Language: Introduction and overview of logic programming, basic elements of prolog, application of logic programming.

UNIT VIII
FUNCTIONAL PROGRAMMING LANGUAGES:
Introduction, fundamentals of FPL, LISP, ML, Haskell, application of Functional Programming Languages and comparison of functional and imperative Languages. Scripting Language: Pragmatics, Key Concepts, Case Study: Python – Values and Types, Variables, Storage and Control, Bindings and Scope, Procedural Abstraction, Data Abstraction, Separate Compilation, Module Library.

TEXT BOOKS:

1. Concepts of Programming Languages Robert .W. Sebesta 8/e, Pearson Education, 2008.

2. Programming Language Design Concepts, D. A. Watt, Wiley dreamtech, rp-2007.

REFERENCES:

1. Programming Languages, 2nd Edition, A.B. Tucker, R.E. Noonan, TMH.
2. Programming Languages, K. C.Louden, 2nd Edition, Thomson, 2003.
3. LISP, Patric Henry Winston and Paul Horn, Pearson Education.
4. Programming in Prolog, W.F. Clocksin, & C.S.Mellish, 5th Edition, Springer.
5. Programming Python, M.Lutz, 3rd Edition, O'reilly, SPD, rp-2007.
6. Core Python Programming, Chun, 2nd Edition, Pearson Education, 2007.
7. Guide to Programming with Python, Michael Dawson, Thomson, 2008.

JAWAHARLAL NEHRU TECHNOLOGICAL UNIVERSITY ANANTAPUR

(9A04602) MICROPROCESSORS AND MICROCONTROLLERS
(Common to CSSE, IT, ECM)

B.Tech III-I Sem. (E.C.M.)	**T**	**P**	**C**
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UNIT-I

INTRODUCTION

Architecture of 8086 microprocessor, special functions of general purpose registers.8086 flag register and function of 8086 flags, addressing modes of 8086,instruction set of 8086.assembler directives, simple programs, procedures and macros.

UNIT-II

ASSEMBLY LANGUAGE PROGRAMMING

Assembly language programs involving logical, branch and call instructions, sorting, evaluation of arithmetic expressions, string manipulation.

UNIT-III

ARCHITECTURE OF 8086 & INTERFACING

Pin diagram of 8086-Minimum mode and maximum mode of operation, Timing diagram, memory interfacing to 8086(static RAM and EPROM). Need for DMA. DMA data transfer method. Interfacing with 8237/8257.

UNIT-IV

PROGRAMMABLE INTERFACING DEVICES

8255 PPI-various modes of operation and interfacing to 8086.interfacing keyboard and display controller- 8279, stepper motor and actuators. D/A and A/D converter interfacing, Interrupt structure of 8086, Vector interrupt table. Interrupt service routines. Introduction to DOS and BIOS interrupts. 8259 PIC architecture and interfacing cascading of interrupt controller and its importance.

UNIT-V

SERIAL DATA TRANSFER SCHEMES

Asynchronous and synchronous data transfer schemes.8251 USART architecture and interfacing.TTL to RS232C and RS232C to TTL conversion. Sample program of serial data transfer. Introduction to high-speed serial communications standards, USB.

UNIT-VI

PROGRAMMABLE INTERRUPT CONTROLLERS

PIC 8259, Programming with 8259, Programmable interval timer 8253, Modes of 8253, Programming examples with 8253.

UNIT-VII

8051 MICROCONTROLLER AND ITS PROGRAMMING

Architecture of micro controller-8051 Microcontroller-internal and external memories-counters and timers-synchronous serial-cum asynchronous serial communication-interrupts. Addressing modes of 8051, Instructor set of 8051, Assembly Language Programming examples using 8051.

UNIT-VIII

ADVANCED MICROCONTROLLERS

MCS – 96 Microcontrollers: Important Features, Pin Diagram, Internal Architecture, Memory Map, Addressing Modes, Instruction set. ARM Microcontrollers: ARM Core Architecture, Versions of ARM, Important Features.

TEXT BOOKS:

1. Advanced microprocessor and peripherals-A.K. Ray and K.M.Bhurchandi, 2nd edition, TMH, 2000.
2. Microcontrollers-Deshmukh, Tata Mc-Graw Hill Edition, 2004.
3. Microcontrollers Architecture, programming, interfacing and system Design-Raj kamal, Pearson Education, 2005.

REFERENCES:

1. Microprocessors Interfacing-Douglas V.Hall, 2nd edition, 2007.
2. The 8088 and 8086 Microprocessors- Walter A. Triebel, Avtar Singh, PHI, 4th Edition, 2003.
3. Micro computer system 8066/8088 family Architecture, programming and Design-By Liu and GA Gibson, PHI, 2nd Ed.
4. 8051 Microcontroller-Internals, Instructions, Programming and Interfacing by Subrata Ghoshal, Pearson, 2010.

JAWAHARLAL NEHRU TECHNOLOGICAL UNIVERSITY ANANTAPUR

Electronics and Computer Engineering
(9A10504) LINEAR AND DIGITAL IC APPLICATIONS
(Common to E.I.E, E.Con.E, ECM)

B.Tech III-I Sem. (E.C.M.)	**T**	**P**	**C**
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UNIT I

Differential Amplifier-Characteristics of OP-Amps, Integrated circuits-Types, Classification, Package Types and temperature ranges, Power supplies, Op-amp Block Diagram, ideal and practical Op-amp specifications, DC and AC characteristics, 741 op-amp & its features, FET input. Op-Amps, Op-Amp parameters & Measurement, Input & Out put Off set voltages & currents, slew rates, CMRR, PSRR, drift, Frequency Compensation technique.

UNIT II

LINEAR & NON-LINEAR APPLICATIONS OF OP- AMPS: Inverting and Non-inverting amplifier, Integrator and differentiator, Difference amplifier, Instrumentation amplifier, AC amplifier, V to I, I to V converters, Buffers. Non- Linear function generation, Comparators, Multivibrators, Triangular and Square wave generators, Log and Anti log amplifiers, Precision rectifiers.

UNIT III

TIMERS & PHASE LOCKED LOOPS: Introduction to 555 timer, functional diagram, Monostable and Astable operations and applications, Schmitt Trigger. PLL - introduction, block schematic, principles and description of individual blocks, 565 PLL, Applications of PLL – frequency multiplication, frequency translation, AM, FM & FSK.

UNIT IV

CMOS LOGIC: Introduction to logic families, CMOS logic, CMOS steady state electrical behavior, CMOS dynamic electrical behavior, CMOS logic families.

UNIT V

BIPOLAR LOGIC AND INTERFACING: Bipolar logic, Transistor logic, TTL families, CMOS/TTL interfacing, low voltage CMOS logic and interfacing, Emitter coupled logic, Comparison of logic families, Familiarity with standard 74XX and CMOS 40XX series-ICs – Specifications.

UNIT VI

THE VHDL HARDWARE DESCRIPTION LANGUAGE: Design flow, program structure, types and constants, functions and procedures, libraries and packages. Structural design elements, data flow design elements, behavioral design elements, time dimension and simulation synthesis.

UNIT VII

COMBINATIONAL LOGIC DESIGN: Decoders, encoders, three state devices, multiplexers and demultiplexers, Code Converters, EX-OR gates and parity circuits, comparators, adders & subtractors, ALUs, Combinational multipliers, VHDL modes for the above ICs.

UNIT VIII

SEQUENTIAL LOGIC DESIGN: Latches and flip-flops, PLDs, counters, shift register, and their VHDL models, synchronous design methodology, impediments to synchronous design.

TEXT BOOKS:

1. Op-Amps & Linear ICs - Ramakanth A. Gayakwad, PHI, 1987.
2. Digital Design Principles & Practices – John F. Wakerly, PHI/ Pearson Education Asia, 3rd Ed., 2005.
3. Digital System Design Using VHDL – Charles H. Roth Jr., Cengage Publications, 1st Edition.

REFERENCES:

1. Op amps & Linear Integrated Circuits Concepts & Applications, James M.Fiore Cengage 2009.
2. Linear Integrated Circuits – D. Roy Chowdhury, New Age International (p) Ltd, 2nd Edition, 2003.
3. VHDL Primer – J. Bhasker, Pearson Education/ PHI, 3rd Edition.

JAWAHARLAL NEHRU TECHNOLOGICAL UNIVERSITY ANANTAPUR

(9A05503) COMPUTER GRAPHICS
(Common to CSE, ECM)

B.Tech III-I Sem. (E.C.M.)	**T**	**P**	**C**
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UNIT I

INTRODUCTION:

Image Processing as Picture Analysis, the Advantages of Interactive Graphics, Representative Uses of Computer Graphics, Classification of Applications, Development of Hardware and Software for Computer Graphics, Conceptual Framework for Interactive Graphics, Drawing with SRGP, Basic Interaction Handling, Raster Graphics Features, Limitations of SRGP.

UNIT II

BASIC RASTER GRAPHICS ALGORITHMS FOR DRAWING 2D PRIMITIVES: Overview, Scan Converting Lines, Scan Converting Circles, Scan Converting Ellipses, Filling Rectangles, Filling Polygons, Filling Ellipse Arcs, Pattern Filling, Thick Primitives, Line Style and Pen Style, Clipping in a Raster World, Clipping Lines, Clipping Circles and Ellipses, Clipping Polygons, Generating Characters, SRGP_Copy Pixel, Antialiasing.

UNIT III

GEOMETRICAL TRANSFORMATIONS:

2D Transformations, Homogeneous Coordinates and Matrix Representation of 2D Transformations, Composition of 2D Transformations, The Window-to-Viewport Transformation, Efficiency, Matrix Representation of 3D Transformations, Composition of 3D Transformations, Transformation as a change in Coordinate System, Viewing in 3D: Projections, Specifying an Arbitrary 3D View, Examples of 3D Viewing, The Mathematics of Planar Geometric Projections, Implementing Planar Geometric Projections, Coordinate Systems.

UNIT IV

OBJECT HIERARCHY AND SIMPLE PHIGS (SPHIGS):

Geometric Modeling, Characteristics of Retained-Mode Graphics Packages, Defining and Displaying Structures, Modeling Transformations, Hierarchical Structure Networks, Matrix Composition in Display Traversal, Appearance-Attribute Handling in Hierarchy, Screen Updating and Rendering Modes, Structure Network Editing for Dynamic Effects, Interaction, Additional Output Features, Implementation Issues, Optimizing Display of Hierarchical Models, Limitations of Hierarchical Modeling in PHIGS, Alternative Forms of Hierarchical Modeling, Input Devices, Interaction Techniques, and Interaction Tasks: Interaction Hardware, Basic Interaction Tasks, Composite Interaction Tasks.

UNIT V

REPRESENTING CURVES AND SURFACES:

Polygon Meshes, Parametric Cubic Curves, Parametric Bicubic Surfaces, Quadratic Surfaces.

UNIT VI

SOLID MODELING:

Representing Solids, Regularized Boolean Set Operations, Primitive Instancing, Sweep Representations, Boundary Representations, Spatial-Partitioning Representations, Constructive Solid Geometry, Comparison of Representations, User Interfaces for Solid Modeling.

UNIT VII

ACHROMATIC LIGHT AND COLORED LIGHT:

Achromatic Light, Chromatic Color, Color Models for Raster Graphics, Reproducing Color, Using Color in Computer Graphics.

UNIT VIII

ILLUMINATION AND SHADING:

Illumination Models, Shading Models for Polygons, Surface Detail, Shadows , Transparency, Interobject Reflections, Physically Based Illumination Models, Extended Light Sources, Spectral Sampling, Improving the Camera Model, Global Illumination Algorithms, Recursive Ray Tracing, Radiosity Methods, The Rendering Pipeline.

TEXT BOOKS:

1. Computer Graphics Principles and Practice, Second Edition in C, James D.Foley, Andries Van Dam, Steven K.Feiner, Jhon F.Hughes, Pearson Education.
2. Computer Graphics C version, Donald Hearn and M. Pauline Baker, Pearson Education.

REFERENCES:

1. Computer Graphics Second edition, Zhigand xiang, Roy Plastock, Schaum's Outlines, Tata Mc-Graw Hill.
2. Principles of Interactive Computer Graphics, Neuman and Sproul, TMH.
3. Principles of Computer Graphics, Shalini, Govil-Pai, Springer.
4. Computer Graphics, Steven Harrington, TMH
5. Computer Graphics, F.S.Hill, S.M.Kelley, PHI.
6. Computer Graphics, P.Shirley,Steve Marschner & Others, Cengage Learning.
7. An Integrated Introduction to Computer Graphics and Geometric Modelling, R.Goldman, CRC Press, Taylor & Francis Group.
8. Computer Graphics, Rajesh K.Maurya, Wiley India.

JAWAHARLAL NEHRU TECHNOLOGICAL UNIVERSITY ANANTAPUR

(9A05505) OPERATING SYSTEMS
(Common to CSE, IT, ECM)

B.Tech III-I Sem. (E.C.M.)	**T**	**P**	**C**
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UNIT I

OPERATING SYSTEMS OVERVIEW:

Operating systems functions, Overview of computer operating systems, protection and security, distributed systems, special purpose systems, operating systems structures: operating system services and systems calls, system programs, operating system structure, operating systems generation.

UNIT II

PROCESS MANAGEMENT:

Process concepts, threads, scheduling-criteria, algorithms, their evaluation, Thread scheduling, case studies UNIX, Linux, Windows.

UNIT III

CONCURRENCY:

Process synchronization, the critical-section problem, Peterson's Solution, synchronization Hardware, semaphores, classic problems of synchronization, monitors, Synchronization examples, atomic transactions. Case studies UNIX, Linux, Windows.

UNIT IV

MEMORY MANAGEMENT:

Swapping, contiguous memory allocation, paging, structure of the page table , segmentation, virtual memory, demand paging, page-replacement, algorithms, Allocation of frames, Thrashing case studies UNIX, Linux, Windows.

UNIT V

PRINCIPLES OF DEADLOCK:

System model, deadlock characterization, deadlock prevention, detection and avoidance, recovery form deadlock.

UNIT VI

FILE SYSTEM INTERFACE:

The concept of a file, Access Methods, Directory structure, File system mounting, file sharing, protection. File System implementation**:** File system structure, file system implementation, directory implementation, allocation methods, free-space management, efficiency and performance, case studies. UNIX, Linux, Windows.

UNIT VII

MASS-STORAGE STRUCTURE:

Overview of Mass-storage structure, Disk structure, disk attachment, disk scheduling, swap-space management, RAID structure, stable-storage implementation, Tertiary storage structure. I/O systems: Hardware, application I/O interface, kernel I/O subsystem, Transforming I/O requests to Hardware operations, STREAMS, performance.

UNIT VIII

PROTECTION:

Protection, Goals of Protection, Principles of Protection, Domain of protection Access Matrix, Implementation of Access Matrix, Access control, Revocation of Access Rights, Capability- Based systems, Language – Based Protection, Security: The Security problem, program threats, system and network threats cryptography as a security tool, user authentication, implementing security defenses, firewalling to protect systems and networks, computer –security classifications, case studies UNIX, Linux, Windows.

TEXT BOOKS:

1. Operating System Concepts- Abraham Silberchatz, Peter B. Galvin, Greg Gagne, 8th edition, John Wiley.

2. Operating systems- A Concept based Approach-D.M.Dhamdhere, 2nd Edition, TMH.

REFERENCES:

1. Operating Systems – Internals and Design Principles, Stallings, 6th edition–2009, Pearson education.
2. Modern Operating Systems, Andrew S Tanenbaum 2nd edition, PHI.
3. Operating Systems,S.Haldar, A.A.Aravind, Pearson education.
4. Principles of Operating Systems, B.L.Stuart, Cengage learning, India Edition.
5. Operating Systems, A.S.Godbole, 2nd Edition, TMH.
6. An Introduction to Operating Systems, P.C.P. Bhatt, PHI.
7. Operating Systems, G.Nutt, N.Chaki and S.Neogy, 3rd Edition, Pearson Education.
8. Operating Systems, R.Elmasri, A,G.Carrick and D.Levine, Mc Graw Hill.

JAWAHARLAL NEHRU TECHNOLOGICAL UNIVERSITY ANANTAPUR

(9A04505) LINEAR AND DIGITAL IC APPLICATIONS LAB
(Common to ECE, ECM)

B.Tech III-I Sem. (E.C.M.)	**T**	**P**	**C**
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Minimum Twelve Experiments to be conducted:

Part A (IC Application Lab):

1. OP AMP Applications – Adder, Subtractor, Comparator Circuits.
2. Active Filter Applications – LPF, HPF (first order)
3. Function Generator using OP AMPs.
4. IC 555 Timer – Monostable and Astable Operation Circuit.
5. IC 566 – VCO Applications.
6. Voltage Regulator using IC 723.
7. 4 bit DAC using OP AMP.

Part B (ECAD Lab):

Simulate the internal structure of the following Digital IC's using VHDL / VERILOG and verify the operations of the Digital IC's (Hardware) in the Laboratory

1. Logic Gates- 74XX.
2. Half Adder, Half Subtractor, Full Adder, Full Subtractor & Ripple Carry Adder.
3. 3-8 Decoder -74138 & 8-3 Encoder- 74X148.
4. 8 x 1 Multiplexer -74X151 and 2x4 Demultiplexer-74X155.
5. 4 bit Comparator-74X85.
6. D Flip-Flop 74X74.
7. JK Flip-Flop 74X109.
8. Decade counter-74X90.
9. Universal shift register -74X194.

Equipment required for Laboratories:

1. RPS
2. CRO
3. Function Generator
4. Multi Meters

5. IC Trainer Kits (Optional)
6. Bread Boards
7. Components: - IC741, IC555, IC566, 7805, 7809, 7912 and other essential components.
8. Analog IC Tester

For Software Simulation

1. Computer Systems
2. LAN Connections (Optional)
3. Operating Systems
4. VHDL/ VERILOG
5. FPGAS/CPLDS (Download Tools)

JAWAHARLAL NEHRU TECHNOLOGICAL UNIVERSITY ANANTAPUR

(9A19501) MICROPROCESSORS AND INTERFACING LAB (Common to ECM, CSSE, IT)

B.Tech III-I Sem. (E.C.M.)	**T**	**P**	**C**
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I. Microprocessor 8086:

1. Introduction to MASM/TASM.
2. Arithmetic operation – Multi byte Addition and Subtraction, Multiplication and Division – Signed and unsigned Arithmetic operation, ASCII – arithmetic operation.
3. Logic operations – Shift and rotate – Converting packed BCD to unpacked BCD, BCD to ASCII conversion.
4. By using string operation and Instruction prefix: Move Block, Reverse string, Sorting, Inserting, Deleting, Length of the string, String comparison.
5. DOS/BIOS programming: Reading keyboard (Buffered with and without echo) – Display characters, Strings.

II. Interfacing:

1. 8259 – Interrupt Controller: Generate an interrupt using 8259 timer.
2. 8279 – Keyboard Display: Write a small program to display a string of characters.
3. 8255 – PPI: Write ALP to generate sinusoidal wave using PPI.
4. 8251 – USART: Write a program in ALP to establish Communication between two processors.

III. Microcontroller 8051

1. Reading and Writing on a parallel port.
2. Timer in different modes.
3. Serial communication implementation.

Equipment required for Laboratories:

1. 8086 µP Kits

2. 8051 Micro Controller kits
3. Interfaces/peripheral subsystems
    i) 8259 PIC
    ii) 8279-KB/Display
    iii) 8255 PPI
    iv) 8251 USART

4. ADC Interface
5. DAC Interface
6. Traffic Controller Interface
7. Elevator Interface

JAWAHARLAL NEHRU TECHNOLOGICAL UNIVERSITY ANANTAPUR

(9AHS701) MANAGEMENT SCIENCE
(Common to ECM, EEE)

B.Tech III-II Sem. (E.C.M.)	**T**	**P**	**C**
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UNIT I
INTRODUCTION TO MANAGEMENT:
Concepts of Management and organization- nature, importance and Functions of Management, Taylor's Scientific Management Theory, Fayol's Principles of Management, Mayo's Hawthorne Experiments, Maslow's Theory of Human Needs, Douglas McGregor's Theory X and Theory Y, Herzberg's Two-Factor Theory of Motivation, Systems Approach to Management, Leadership Styles, Social responsibilities of Management.

UNIT II
DESIGNING ORGANIZATIONAL STRUCTURES:
Basic concepts related to Organisation - Departmentation and Decentralisation, Types of mechanistic and organic structures of organisation (Line organization, Line and staff organization, functional organization, Committee organization, matrix organization, Virtual Organisation, Cellular Organisation, team structure, boundaryless organization, inverted pyramid structure, lean and flat organization structure) and their merits, demerits and suitability.

UNIT III
OPERATIONS MANAGEMENT:
Principles and Types of Plant Layout-Methods of production (Job, batch and Mass Production), Work Study -Basic procedure involved in Method Study and Work Measurement- Statistical Quality Control: chart, R chart, *c* chart, *p* chart, (simple Problems), Acceptance Sampling, Deming's contribution to quality.

UNIT IV

MATERIALS MANAGEMENT:

Objectives, Need for Inventory control, EOQ, ABC Analysis, Purchase Procedure, Stores Management and Stores Records.

Marketing: Functions of Marketing, Marketing Mix, Marketing Strategies based on Product Life Cycle, Channels of distribution

UNIT V

HUMAN RESOURCES MANAGEMENT (HRM):

Concepts of HRM, HRD and Personnel Management and Industrial Relations (PMIR), HRM vs. PMIR, Basic functions of HR Manager: Manpower planning, Recruitment, Selection, Training and Development, Placement, Wage and Salary Administration, Promotion, Transfer, Separation, Performance Appraisal, Grievance Handling and Welfare Administration, Job Evaluation and Merit Rating.

UNIT VI

PROJECT MANAGEMENT (PERT/CPM):

Network Analysis, Programme Evaluation and Review Technique (PERT), Critical Path Method (CPM), Identifying critical path, Probability of Completing the project within given time, Project Cost Analysis, Project Crashing. (simple problems).

UNIT VII

STRATEGIC MANAGEMENT:

Mission, Goals, Objectives, Policy, Strategy, Programmes, Elements of Corporate Planning Process, Environmental Scanning, Value Chain Analysis, SWOT Analysis, Steps in Strategy Formulation and Implementation, Generic Strategy alternatives.

UNIT VIII

CONTEMPORARY MANAGEMENT PRACTICES:

Basic concepts of MIS, End User Computing, Materials Requirement Planning (MRP), Just-In-Time (JIT) System, Total Quality Management (TQM), Six sigma and Capability Maturity Model (CMM) Levels, Supply Chain Management, Enterprise Resource Planning (ERP), Performance

Management, Business Process outsourcing (BPO), Business Process Re-engineering and Bench Marking, Balanced Score Card.

TEXT BOOKS:

1. Aryasri: Management Science, TMH, 2004.
2. Stoner, Freeman, Gilbert, Management, 6th Ed, Pearson Education, New Delhi, 2004.

REFERENCES:

1. Kotler Philip & Keller Kevin Lane: Marketing Mangement 12/e, PHI, 2005.
2. Koontz & Weihrich: Essentials of Management, 6/e, TMH, 2005.
3. Thomas N.Duening & John M.Ivancevich Management—Principles and Guidelines, Biztantra, 2003.
4. Kanishka Bedi, Production and Operations Management, Oxford University Press, 2004.
5. Memoria & S.V.Gauker, Personnel Management, Himalaya, 25/e, 2005.
6. Samuel C.Certo: Modern Management, 9/e, PHI, 2005.
7. Schermerhorn, Capling, Poole & Wiesner: Management, Wiley, 2002.
8. Parnell: Strategic Management, Biztantra, 2003.
9. Lawrence R Jauch, R.Gupta &William F.Glueck:Business Policy and Strategic Management, Frank Bros. 2005.
10. L.S.Srinath: PERT/CPM, Affiliated East-West Press, 2005.

JAWAHARLAL NEHRU TECHNOLOGICAL UNIVERSITY ANANTAPUR

(9A05506) COMPUTER NETWORKS
(Common to CSSE, ECM)

B.Tech III-II Sem. (E.C.M.)	**T**	**P**	**C**
	4	**0**	**4**

UNIT I

Introduction: Network Hardware, Network Software, References Models. The Physical Layer: The Theoretical Basis for Data Communication Guided Transmission Media, Communication Satellites, The public Switched Telephone Network- The Local Loop: Modern ADSL, and wireless, Trunks and Multiplexing, Switching

UNIT II

The Data Link Layer: Data link Layer Design Issues, Elementry Data Link Protocols, Sliding Window Protocols

UNIT III

The Medium Access Control Sublayer: The Channel allocation Problem, Multiple Access protocols, Ethernet- Ethernet Cabling, Manchester Encoding, The Ethernet MAC Sublayer Protocol. The Binary Exponential Backoff Algorithm, Ethernet Performance, Switched Ethernet, Fast Ethernet. Wireless Lans- The 802.11 Protocol Stack, The 802.11 Physical Layer, The 802.11 MAC SubLayer Protocol, The 802.11 Frame Structure.

UNIT IV

The Network Layer**:** Network Layer Design Issues, Routing Algorithms, Congestion Control Algorithms.

UNIT V

Internetworking, The Network Layer in the Internet.

UNIT VI

The Transport Layer: The Transport Service, Elements of Transport Protocols, The Internet Transport Protocols: UDP, The Internet Transport Protocols: TCP.

UNTI VII

The Application Layer: DNS-The Domain Name System, Electronic Mail. The World Wide web, Multimedia.

UNTI VIII

Network Security: Cryptography, Symmetric-Key Algorithms, Public-Key Algorithms, Digital Signatures.

TEXT BOOKS:

1. Computer Networks, Andrew S. Tanenbaum, 4e, Pearson Education.

REFERENCES:

1. Computer Communications and Networking Technologies, Michael A. Gallo, William M. Hancock, Cengage Learning.
2. Computer Networks-Principles, Technologies and Protocols for Network Design, Natalia Olifer, Victor Olifer, Wiley India.
3. Data Communications and Networking, Behrouz A. Forouzan, Fourth Edition, Tata McGraw Hill.
4. Understanding Communications and Networks, 3rd Edition, W.A.Shay, Cengage Learning.
5. Computer and Communication Networks ,Nader F. Mir, Pearson Education
6. Computer Networking: A Top-Down Approach Featuring the Internet, James F.Kurose, K.W.Ross, 3rd Edition, Pearson Education.
7. Data and Computer Communications, G.S.Hura and M.Singhal, CRC Press, Taylor and Francis Group.

JAWAHARLAL NEHRU TECHNOLOGICAL UNIVERSITY ANANTAPUR

(9A04701) EMBEDDED REAL TIME OPERATING SYSTEMS

B.Tech III-II Sem. (E.C.M.)	**T**	**P**	**C**
	4	**0**	**4**

UNIT I

INTRODUCTION

History of Embedded Systems, Major Application Areas of Embedded Systems, Purpose of Embedded Systems, Core of the Embedded System, Sensors and Actuators, Communication Interface, Embedded Firmware.

UNIT II

HARDWARE SOFTWARE Co-DESIGN and PROGRAMME MODELLING

Characteristics of an Embedded System, Quality Attributes of Embedded Systems, Fundamental Issues in Hardware Software Co-Design, Computational Models in Embedded Design, Introduction to Unified Modeling Language (UML),Hardware Software Trade-offs.

UNIT III

EMBEDDED HARDWARE DESIGN AND DEVELOPMENT

Analog Electronic Components, Digital Electronic Components, VLSI and Integrated Circuit Design, Electronic Design Automation (EDA) Tools, Embedded Firmware Design Approaches, Embedded Firmware Development Languages.

UNIT IV

REAL-TIME OPERATING SYSTEMS (RTOS) BASED EMBEDDED SYSTEM DESIGN

Operating System Basics, Types of Operating Systems, Tasks, Process and Threads, Multiprocessing and Multitasking, Task Scheduling, Threads, Processes and Scheduling :Putting them Altogether, Task Communication, Task Synchronization, Device Drivers, How to Choose an RTOS.

UNIT V

DEVICES AND COMMUNICATION BUSES FOR DEVICES NETWORK

IO Types and Examples, Serial Communication Devices, Parallel Device Ports, Sophisticated Interfacing Features in Device Ports, Wireless Devices, Timer and Counting Devices, Watchdog Timer, Real Time Clock, Networked Embedded Systems, Serial Bus Communication Protocols, Parallel Bus Device Protocols- Parallel Communication Network Using ISA, PCI, PCI-X and Advanced Buses, Internet Enabled Systems- Network Protocols, Wireless and Mobile System Protocols.

UNIT VI

PROGRAM MODELING CONCEPTS

Program Models, DFG Models, State Machine Programming Models for Event-controlled Program Flow, Modeling of Multiprocessor Systems, UML Modeling.

UNIT VII

REAL TIME OPERATING SYSTEMS

OS Services, Process Management, Timer .Functions, Event Functions, Memory Management, Device, File and IO Subsystems Management, Interrupt Routines in RTOS Environment and Handling of Interrupt Source Calls, Real-time Operating Systems, Basic-Design an RTOS, RTOS Task Scheduling Models, Interrupt Latency and Response of the Tasks as Performance Matrices, OS Security Issues.

UNIT VIII

DESIGN EXAMPLES AND CASE STUDIES OF PROGAM MODELING AND PROGRAMMING WITH RTOS-2

Case study of Communication between Orchestra Robots, Embedded Systems in Automobile, Case study of an Embedded System for an Adaptive Cruise Control(ACC) System in a Car, Case study of an Embedded System for a Smart Card, Case study of a Mobile Phone Software for Key Inputs.

TEXT BOOKS:

1. Introduction to Embedded System- Shibu KV, Mc-Graw Hill Higher Edition.
2. Embedded Systems Architecture, Programming and Design- Raj Kamal, Second Edition, McGraw-Hill Companies.
3. Embedded System Design by Peter Marwedel, Springer.

REFERENCES:

1. Embedded System Design – A Unified Hardware/Software Introduction-Frank Vahid, Tony D. Givargis, John Wiley, 2002.
2. Embedded/ Real Time Systems-KVKK Prasad, Dreamtech Press, 2005.
3. An Embedded Software Primer- David E. Simon, Pearson Ed. 2005.

JAWAHARLAL NEHRU TECHNOLOGICAL UNIVERSITY ANANTAPUR

(9A04603) DIGITAL SIGNAL PROCESSING
(Common to ECE, E Con E, EIE, ECM)

B.Tech III-II Sem. (E.C.M.)	**T**	**P**	**C**
	4	**0**	**4**

UNIT-I
INTRODUCTION
Introduction to digital signal processing: discrete time signals and sequences, linear shift invariant systems, stability and causality, linear constant coefficient difference equations. Frequency domain representation of discrete time signals and systems.

UNIT-II
DISCRETE FOURIER SERIES
Properties of discrete Fourier series, DFS representation of periodic sequences, discrete Fourier transforms: properties of DFT, linear convolution of sequences using DFT, computation of DFT. Relation between Z-Transform and DFS.

UNIT-III
FAST FOURIER TRANSFORMS
Fast Fourier transforms (FFT)-Radix2 decimation in time and decimation in frequency FFT algorithms, inverse FFT and FFT for composite N.

UNIT-IV
REALIZATION OF DIGITAL FILTERS
Review of Z-transforms, applications of Z-Transforms, solution of difference equations of digital filters, block diagram representation of linear constant-coefficient difference equations, basic structures of IIR systems, transposed forms, basic structures of FIR systems, system function.

UNIT-V

IIR DIGITAL FILTERS

Analog filter approximations-Butterworth and chebyshev, design of IIR digital filters from analog filters, design examples: analog-digital transformations, Illustrative Problems.

UNIT-VI

FIR DIGITAL FILTERS

Characteristics of FIR digital filters, frequency response. Design of FIR digital filters using window techniques, frequency sampling technique, comparison of IIR and FIR filters, , Illustrative Problems.

UNIT-VII

MULTIRATE DIGITAL SIGNAL PROCESSING FUNDAMENTALS:

Basic sample rate alteration devices, Multirate Structures for sampling rate Converters, Multistage design of decimator and Interpolator, Polyphase Decomposition, Nyquist filters.

UNIT-VIII

APPLICATIONS OF DIGITAL SIGNAL PROCESSING

Spectral analysis of nonstationary Signals, Musical Sound processing, signal Compression, Transmultiplexers, Discrete Multitone Transmission of digital data.

TEXT BOOKS:

1. Digital signal processing, principles, Algorithms and applications: John G. Proakis, Dimitris G. Manolakis, Pearson Education/PHI, 2007.
2. Digital signal processing , A computer base approach- Sanjit K Mitra, Tata McGraw Hill, 3rd edition, 2009.
3. Discrete Time Signal Processing-A.V. Oppenheim and R.W. Schaffer, PHI.

REFERENCES:

1. Digital signal processing: Andreas Antoniou, TATA McGraw Hill, 2006.

2. A Text book on Digital Signal processing – R S Kaler, M Kulkarni, Umesh Gupta, I K International Publisthing House Pvt. Ltd.
3. Digital signal processing: M H Hayes, Schaum's outlines, TATA Mc-Graw Hill, 2007.

JAWAHARLAL NEHRU TECHNOLOGICAL UNIVERSITY ANANTAPUR

(9A05502) SOFTWARE ENGINEERING

B.Tech III-II Sem. (E.C.M.)	**T**	**P**	**C**
	4	**0**	**4**

UNIT I

INTRODUCTION TO SOFTWARE ENGINEERING:

The evolving role of software, Changing Nature of Software, legacy software, Software myths. A Generic view of process: Software engineering- A layered technology, a process framework, The Capability Maturity Model Integration (CMMI), Process patterns, process assessment, personal and team process models.

UNIT II

PROCESS MODELS:

The waterfall model, Incremental process models, Evolutionary process models, Specialized process models, The Unified process. Software Requirements: Functional and non-functional requirements, User requirements, System requirements, Interface specification, the software requirements document.

UNIT III

REQUIREMENTS ENGINEERING PROCESS:

Feasibility studies, Requirements elicitation and analysis, Requirements validation, Requirements management. System models: Context Models, Behavioral models, Data models, Object models, structured methods.

UNIT IV

DESIGN ENGINEERING:

Design process and Design quality, Design concepts, the design model, pattern based software design. Creating an architectural design: software architecture, Data design, Architectural styles and patterns, Architectural Design, assessing alternative architectural designs, mapping data flow into a software architecture.

UNIT V

MODELING COMPONENT-LEVEL DESIGN:

Designing class-based components, conducting component-level design, Object constraint language, designing conventional components.Performing User interface design: Golden rules, User interface analysis and design, interface analysis, interface design steps, Design evaluation.

UNIT VI

TESTING STRATEGIES:

A strategic approach to software testing, test strategies for conventional software, Black-Box and White-Box testing, Validation testing, System testing, the art of Debugging. Product metrics: Software Quality, Frame work for Product metrics, Metrics for Analysis Model, Metrics for Design Model, Metrics for source code, Metrics for testing, Metrics for maintenance.

UNIT VII

METRICS FOR PROCESS AND PRODUCTS:

Software Measurement, Metrics for software quality.Risk management: Reactive vs Proactive Risk strategies, software risks, Risk identification, Risk projection, Risk refinement, RMMM, RMMM Plan.

UNIT VIII

QUALITY MANAGEMENT:

Quality concepts, Software quality assurance, Software Reviews, Formal technical reviews, Statistical Software quality Assurance, Software reliability, The ISO 9000 quality standards.

TEXT BOOKS:

1. Software Engineering: A practitioner's Approach, Roger S Pressman, 6th edition. McGrawHill International Edition, 2005.
2. Software Engineering, Ian Sommerville, 7th edition, Pearson Education, 2004.

REFERENCES:

1. Fundamentals of Software Engineering, Rajib Mall, PHI, 2005.
2. Software Engineering, A Precise Approach, Pankaj Jalote, Wiley India, 2010.
3. Software Engineering: A Primer, Waman S Jawadekar, Tata McGraw-Hill, 2008.
4. Software Engineering, Principles and Practices, Deepak Jain, Oxford University Press.
5. Software Engineering1: Abstraction and modeling, Diner Bjorner, Springer International edition, 2006.
6. Software Engineering2: Specification of systems and languages, Diner Bjorner, Springer International edition, 2006.
7. Software Engineering Foundations, Yingxu Wang, Auerbach Publications, 2008.
8. Software Engineering Principles and Practice, Hans Van Vliet, 3rd edition, John Wiley &Sons Ltd.

JAWAHARLAL NEHRU TECHNOLOGICAL UNIVERSITY ANANTAPUR

(9A05403) DESIGN AND ANALYSIS OF ALGORITHMS

B.Tech III-II Sem. (E.C.M.)	T	P	C
	4	0	4

UNIT I

Introduction: Algorithm, Psuedocode for Expressing Algorithms, Performance Analysis-Space Complexity, Time Complexity, Asymptotic Notation- Big oh Notation, Omega Notation, Theta Notation and Little oh Notation, Basics of Probability Theory , Amortized Complexity

UNIT II

Disjoint Sets, Disjoint Set Operations, Union and Find Algorithms, Spanning Trees, Connected Components and Biconnected components.

UNIT III

Divide and Conquer: General Method, Applications-Binary Search, Quick Sort, Merge Sort, Strassen's Matrix Multiplication.

UNIT IV

Greedy Method: General Method, Applications-Job Sequencing with Dead-Lines, Knapsack Problem, Minimum-Cost Spanning Trees, Single Source Shortest Path.

UNIT V

Dynamic Programming: General Method, Applications-Matrix Chain Multiplication, Optimal Binary Search Trees, 0/1 Knapsack, All Pairs Shortest Path, The Travelling Sales Person Problem, Reliability Design.

UNIT VI

Backtracking: General Method, Applications-8-Queen Problem, Sum of Subsets, Graph Coloring, Hamiltonian Cycles.

UNIT VII

Branch and Bound: General Method, Applications - Travelling Sales Person(*),0/1 Knapsack Problem- LC Branch and Bound Solution, FIFO Branch and Bound Solution.

UNIT VIII

NP-Hard and NP-Complete Problems: Basic Concepts, Nondeterministic Algorithms, The Classes-NP - Hard and NP-Complete, Cook's Theorem (*).

TEXT BOOKS:

1. Fundamentals of Computer Algorithms, Ellis Horowitz,Satraj Sahni and Rajasekharam, Galgotia publications pvt. Ltd.
2. Design and Analysis Algorithms - Parag Himanshu Dave, Himanshu Bhalchandra Dave Publisher: Pearson.
3. Algorithm Design: Foundations, Analysis and Internet examples, M.T.Goodrich and R.Tomassia,John wiley and sons.

REFERENCES:

1. Introduction to Algorithms, secondedition, T.H.Cormen,C.E.Leiserson, R.L.Rivest, and C.Stein, PHI Pvt. Ltd./ Pearson Education.
2. Introduction to Design and Analysis of Algorithms A strategic approach, R.C.T.Lee, S.S.Tseng, R.C.Chang and T.Tsai, Mc-Graw Hill.
3. Data structures and Algorithm Analysis in C++, Allen Weiss, Second edition, Pearson education.
4. Design and Analysis of algorithms, Aho, Ullman and Hopcroft, Pearson education.
5. Algorithms – Richard Johnson baugh and Marcus Schaefer, Pearson Education.

JAWAHARLAL NEHRU TECHNOLOGICAL UNIVERSITY ANANTAPUR

(9AHS601) ADVANCED ENGLISH COMMUNICATION SKILLS LAB
(Common to ECE, E Con E, ECM, EIE, EEE, ME, AE)

B.Tech III-II Sem. (E.C.M.)	**T**	**P**	**C**
	0	**3**	**2**

1. Introduction

The Advanced English Language Skills Lab introduced at the 3rd year B.Tech level is considered essential for the student for focusing on his/her career. At this stage it is imperative for the student to start preparing for the ever growing competition in the job market. In this scenario, in order to be on par with the best, he/she needs to improve his/her Communication and soft skills

This course focuses on the practical aspects of English incorporating all the four (LRSW) skills relevant to the requirements of the prospective employers in view of globalization. The proposed course will enable the students to perform the following:

- Intensive reading to improve comprehension and communication
- Attentive listening for better understanding
- Write project/research/technical reports
- Write Resume' to attract attention
- Discuss ideas / opinions for better solutions
- Face interviews confidently
- Gather information, organize ideas, and present them effectively before an audience
- To help the students cultivate the habit of reading passages from the computer monitor, thus providing them with the required ability to face computer-based competitive exams such GRE, TOEFL,CAT, GMAT etc.

2. Objectives:

Keeping in mind the previous exposure of the student to English, this lab focuses on improving the student's proficiency in English at all levels. The lab intends t**o** train students to use language effectively, to participate in group discussions, to help them face interviews, and sharpen public speaking skills and enhance the confidence of the student by exposing him/her to various situations and contexts which he/she would face in his/her career

3 Syllabus

The following course content is prescribed for the Advanced Communication Skills Lab:

Reading Comprehension -- Reading for facts, guessing meanings from context, speed reading, scanning, skimming for building vocabulary(synonyms and antonyms, one word substitutes, prefixes and suffixes, idioms and phrases.)

Listening Comprehension-- Listening for understanding, so as to respond relevantly and appropriately to people of different backgrounds and dialects in various personal and professional situations.

Technical Report Writing—Types of formats and styles, subject matter, organization, clarity, coherence and style, data-collection, tools, analysis

Resume' Writing—Structure, format and style, planning, defining the career objective, projecting one's strengths, and skills, creative self marketing, cover letter

Group Discussion-- Communicating views and opinions, discussing, intervening. providing solutions on any given topic across a cross-section of individuals,(keeping an eye on modulation of voice, clarity, body language, relevance, fluency and coherence) in personal and professional lives.

Interview Skills—Concept and process, pre-interview planning, mannerisms, body language, organizing, answering strategies, interview through tele and video-conferencing

Technical Presentations (Oral)— Collection of data, planning, preparation, type, style and format ,use of props, attracting audience, voice modulation, clarity, body language, asking queries.

4. Minimum Requirements

The English Language Lab shall have two parts:

The Computer aided Language Lab for 60 students with 60 systems, one master console, LAN facility and English language software for self-study by learners.

The Communication Skills Lab with movable chairs and audio-visual aids with a P.A System, a TV, A digital stereo-audio and video system, Camcorder etc

System Requirement (Hardware Component):

Computer network with LAN with a minimum of 60 multimedia systems with the following specifications:

P-IV Processor, Speed-2.8 GHz, RAM_512 MB minimum, Hard Disk-80 GB, Headphones

Prescribed Software: GLOBARENA

Books Suggested for English Language Lab Library (to be located within the lab in addition to the CDs of the text book which are loaded on the systems):

1. **Technical writing and professional communication, Huckin and Olsen** Tata Mc Graw-Hil 2009.
2. **Speaking about Science**, A Manual for Creating Clear Presentations **by Scott Morgan and Barrett Whitener, Cambridge University press, 2006**
3. **Books on** TOEFL/GRE/GMAT/CAT/ IELTS **by Barron's/DELTA/Cambridge University Press.**

4. **Handbook for Technical Writing** by David A McMurrey & Joanne Buckely CENGAGE Learning 2008
5. **Technical Communication** by Meenakshi Raman & Sangeeta Sharma, Oxford University Press 2009.
6. **The ACE of Soft Skills** by Gopal Ramesh and Mahadevan Ramesh, Pearson Education, 2010
7. **Cambridge English for Job-Hunting** by Colm Downes, Cambridge University Press, 2008
8. **Resume's and Interviews** by M.Ashraf Rizvi, Tata Mc Graw-Hill, 2008
9. **From Campus To Corporate** by KK Ramachandran and KK Karthick, Macmillan Publishers India Ltd, 2010
10. **English Language Communication : A Reader cum Lab Manual** Dr A Ramakrishna Rao, Dr G Natanam & Prof SA Sankaranarayanan, Anuradha Publications, Chennai 2008.
11. **Managing Soft Skills** by K R Lakshminarayan and T.Muruguvel, Sci-Tech Publications, 2010
12. **Business Communication** by John X Wang, CRC Press, Special Indian Edition,2008

JAWAHARLAL NEHRU TECHNOLOGICAL UNIVERSITY ANANTAPUR

(9A05507) COMPUTER NETWORKS AND OPERATING SYSTEMS LAB

B.Tech III-II Sem. (E.C.M.)	**T**	**P**	**C**
	0	**3**	**2**

Part – A

1. Implement the data link layer framing methods such as character, character stuffing and bit stuffing.
2. Implement on a data set of characters the three CRC polynomials – CRC 12, CRC 16 and CRC CCIP.
3. Implement Dijkstra's algorithm to compute the Shortest path thru a graph.
4. Take an example subnet graph with weights indicating delay between nodes. Now obtain Routing table art each node using distance vector routing algorithm.
5. Take an example subnet of hosts. Obtain broadcast tree for it.
6. Take a 64 bit playing text and encrypt the same using DES algorithm.
7. Write a program to break the above DES coding.
8. Using RSA algorithm encrypt a text data and Decrypt the same.

Part – B

1. Simulate the following CPU scheduling algorithms
   a) Round Robin b) SJF c) FCFS d) Priority
2. Simulate all file allocation strategies
   a) Sequential b) Indexed c) Linked
3. Simulate MVT and MFT
4. Simulate all File Organization Techniques
   a) Single level directory b) Two level c) Hierarchical d) DAG
5. Simulate Bankers Algorithm for Dead Lock Avoidance
6. Simulate Bankers Algorithm for Dead Lock Prevention
7. Simulate all page replacement algorithms
   a) FIFO b) LRU c) LFU Etc…
8. Simulate Paging Technique of memory management.

JAWAHARLAL NEHRU TECHNOLOGICAL UNIVERSITY ANANTAPUR

(9A04604) ELECTRONIC MEASUREMENTS AND INSTRUMENTATION

B.Tech IV-I Sem. (E.C.M.)	T	P	C
	4	0	4

UNIT-I

Performance characteristics of instruments : static characteristics, Accuracy, precision, Resolution, Sensitivity, static and dynamic calibration, Errors in Measurement, and their statistical analysis, dynamic characteristics-speed of Response, fidelity, Lag and dynamic error. DC voltmeters-multirange, range extension/solid state and differential voltmeters, AC voltmeters –multirange, range extension, shunt. Thermocouple type RF ammeter, ohm meters, series type, shunt type, multimeter for voltage, current and resistance measurements.

UNIT-II

Signal generator-fixed and variable, AF oscillators, function generators, pulse, random noise, sweep, and arbitrary waveform generators, their standards, specifications and principles of working (Block diagram approach).

UNIT-III

Wave analyzers, harmonic distortion analyzers, FFT analyzers, and Logic analyzers.

UNIT-IV

Oscilloscopes: Standard specifications of CRO, CRT features, vertical and horizontal amplifiers, horizontal and vertical deflection systems, sweep trigger pulse, delay line, sync selector circuits, probes for CRO – active, passive, and attenuator type, triggered sweep CRO, and Delayed sweep, dual trace/beam CRO, Measurement of amplitude, frequency and phase (Lissajous method).

UNIT-V

Principles of sampling oscilloscope, storage oscilloscope, and digital storage oscilloscope, Digital frequency counter, time and period measurement, Digital Multimeter (A to D converter used in DMM and its principle).

UNIT-VI

Review of DC Bridges: Wheatstone bridge. Wein Bridge, errors and precautions in using bridges, AC bridges: Measurement of inductance-Maxwell's bridge, Anderson Bridge. Measurement of capacitance-Schearing Bridge. Kelvin Bridge, Q-meter, EMI and EMC, Interference and noise reduction techniques.

UNIT-VII

Sensors and Transducers - Active and passive transducers: Measurement of displacement (Resistance, capacitance, inductance; LVDT) Force (strain gauges) Pressure (piezoelectric transducers) Temperature (resistance thermometers, thermocouples, and thermistors), Velocity, Acceleration, Vibration, pH measurement Signal Conditioning Circuits.

UNIT-VIII

Data Acquisition System, Analogue and digital data recording techniques, strip chart and XY recording methods, Over view of PC Based instrumentation, Bus standards for measuring instruments(GPIB,RS232,USB).

TEXT BOOKS:

1. Electronic instrumentation, second edition - H.S.Kalsi, Tata McGraw Hill, 2004.
2. Modern Electronic Instrumentation and Measurement Techniques – A.D. Helfrick and W.D. Cooper, PHI, 5th Edition, 2002.
3. Electronic Instrumentation & Measurements - David A. Bell, PHI, 2nd Edition, 2003.

REFERENCES:

1. Measurement Systems Application and Design-Ernest O Doebelin and Dhanesh N Manik TMH, 5th Edition, 2009.
2. Electronic Measurement and Instrumentation, Oliver and Cage, TMH.
3. Electronic Test Instruments, Analog and Digital Measurements - Robert A.Witte, Pearson Education, 2nd Ed., 2004.
4. Electronic Measurements & Instrumentations by K. Lal Kishore, Pearson Education - 2005.

JAWAHARLAL NEHRU TECHNOLOGICAL UNIVERSITY ANANTAPUR

(9A12601) LINUX PROGRAMMING

B.Tech IV-I Sem. (E.C.M.)	**T**	**P**	**C**
	4	**0**	**4**

UNIT I

Linux Utilities: File Handling Utilities, Security by File Permissions, Process Utilities, Disk Utilities, Networking Commands, Filters, Text Processing Utilities and Backup Utilities, Sed: Scripts, Operation, Addresses, Commands, Applications, Awk: Execution, Fields and Records, Scripts, Operation, Patterns, Actions, Functions, Using System Commands in Awk.

UNIT II

Working with the Bourne Again Shell(Bash): Introduction, Shell Responsibilities, Pipes and Input Redirection, Output Redirection, Here Documents, Running a Shell Script, The Shell as a Programming Language, Shell Meta Characters, File Name Substitution, Shell Variables, Command Substitution, Shell Commands, The Environment, Quoting, Test Command, Control Structures, Arithmetic in Shell, Shell Script Examples, Interrupt Processing, Functions, Debugging Shell Scripts.

UNIT III

Files: File Concept, File System Structure, Inodes, File Attributes, File Types, Library Functions, The Standard I/O and Formatted I/O in C, Stream Errors, Kernel Support for Files, System Calls, File Descriptors, Low Level File Access – File Structure Related System Calls(File Apis), File and Record Locking, File and Directory Management – Directory File Apis, Symbolic Links & Hard Links.

UNIT IV

Process, Process Concept, Kernel Support for Process, Process Attributes, Process Control, Process Creation, Waiting for a Process, Process Termination, Zombie Process, Orphan Process, Process Apis. Signals: Introduction to Signals, Signal Generation and Handling, Kernel Support

for Signals, Signal Function, Unreliable Signals, Reliable Signals, Kill, Raise, Alarm, Pause, Abort, Sleep Functions.

UNIT V

Interprocess Communication: Introduction to IPC, Pipes, Fifos, Introduction to Three Types of IPC-Message Queues, Semaphores and Shared Memory. Message Queues: Kernel Support for Messages, Unix System V Apis for Messages, Client/Server Example.

UNIT VI

Semaphores: Kernel Support for Semaphores, Unix System V Apis for Semaphores. Shared Memory: Kernel Support for Shared Memory, Unix System V Apis for Shared Memory, Semaphore and Shared Memory Example.

UNIT VII

Multithreaded Programming: Differences Between Threads and Processes, Thread Structure and Uses, Threads and Lightweight Processes, POSIX Thread Apis, Creating Threads, Thread Attributes, Thread Synchronization with Semaphores and with Mutexes, Example Programs.

UNIT-VIII

Sockets: Introduction to Sockets, Socket Addresses, Socket System Calls for Connection Oriented Protocol and Connectionless Protocol, Example-Client/Server Programs.

TEXT BOOKS:

1. Unix System Programming using C++, T.Chan, PHI. (UNIT III to UNIT VIII).
2. Unix Concepts and Applications, 4th Edition, Sumitabha Das, TMH.
3. Beginning Linux Programming, 4th Edition, N.Matthew, R.Stones, Wrox, Wiley India Edition.

REFERENCES:

1. Linux System Programming, Robert Love, O'Reilly, SPD.
2. Advanced Programming in the Unix environment, 2nd Edition, W.R.Stevens, Pearson Education.
3. Unix Network Programming , W.R.Stevens, PHI.
4. Unix for programmers and users, 3rd Edition, Graham Glass, King Ables, Pearson Education.
5. Unix and Shell programming, B.A.Forouzan and R.F.Gilberg, Cengage Learning.
6. Unix The Text book, 2nd edition, S.M.Sarwar, R.Koretsky, S.A.Sarwar, Pearson Education.
7. Unix Internals, U.Vahalia, Pearson Education.
8. Unix shell Programming, S.G.Kochan and P.Wood, 3rd edition, Pearson Education.

JAWAHARLAL NEHRU TECHNOLOGICAL UNIVERSITY ANANTAPUR

(9A05401) DATABASE MANAGEMENT SYSTEMS

B.Tech IV-I Sem. (E.C.M.)	**T**	**P**	**C**
	4	**0**	**4**

UNIT I

Database Systems: Data vs Information-Introducing the Database and the DBMS-Why Database Design is Important-Files and File Systems-Problems with File System Data Management-Database Systems. Data Models: Data Modeling and Data Models-The Importance of Data Models-Data Model Basic Building Blocks-Business Rules-The Evolution of Data Models-Degree of Data Abstraction.

UNIT II

Entity Relationship Modeling: The Entity Relationship Model (ERM)-Developing an ER Diagram-Database Design Challenges: Conflicting Goals-The Extended Entity Relationship Model-Entity Clustering- Entity Integrity: Selecting Primary Keys-Learning Flexible Database Design-Data Modeling Checklist.

UNIT III

The Relational Database Model: A Logical View of Data-Keys, Integrity Rules, Relational Set Operators, The Data Dictionary and the System Catalog-Relationships within the Relational Database, Data Redundancy Revisited, Indexes, Codd's Relational Database Rules.

UNIT IV

Structured Query Language (SQL): Introduction to SQL-Data Definition Commands, Data Manipulation Commands, SELECT Queries, Advanced Data Definition Commands, Advanced SELECT Queries, Virtual Tables: Creating a View, Joining Database Tables. Advanced SQL: Relational Set Operators, SQL Join Operators, Subqueries and Correlated Queries, SQL Functions, Oracle Sequences, Updatable Views, Procedural SQL, Embedded SQL.

UNIT V

Normalization of Database Tables: Database Tables and Normalization, The Need for Normalization-The Normalization Process, Improving the Design-Surrogate Key Considerations, Higher-Level Normal Forms, Normalization and Database Design, Denormalization.

UNIT VI

Transaction Management and Concurrency Control: What is a Transaction?, Transaction State, Implementation of atomicity and durability, Concurrency Control, Serializability, Testing for Serializability, Concurrency Control with Locking Methods, Concurrency Control with Time Stamping Methods, Concurrency Control with Optimistic Methods, Database Recovery Management-Validation Based Protocols-Multiple Granularity.

UNIT VII

Recovery System: Recovery and Atomicity, Log-Based Recovery, Recovery with Concurrent Transactions, Buffer Management-Failure with loss of nonvolatile storage, Advance Recovery Techniques, Remote Backup Systems.

UNIT VIII

File Structure and Indexing: Overview of Physical Storage Media, Magnetic Disks, RAID-Tertiary Storage, Storage Access, File Organization, Organization of Records in Files, Data-Dictionary Storage, Basic Concepts of Indexing, Ordered Indices, B^+-Tree Index Files, B-Tree Index Files, Multiple Key Access, Static Hashing, Dynamic Hashing, Comparison of Ordered Indexing and Hashing, Bitmap Indices, Indexed Sequential Access Methods (ISAM).

TEXT BOOKS:

1. Database Management Systems, Peter Rob, A.Ananda Rao and Carlos Coronel, Cengage Learning.
2. Database System Concepts, Silberschatz, Korth, McGraw hill, 5^{th} edition.

REFERENCES:

1. Database Management Systems, Raghurama Krishnan, Johannes Gehrke, TATA McGraw Hill, 3rd Edition.
2. Fundamentals of Database Systems, Elmasri,Navrate Pearson Education.
3. Introduction to Database Systems, C. J. Date Pearson Education.
4. Oracle for Professionals, The X Team, S. Shah and V. Shah, SPD.
5. Database Systems Using Oracle: A Simplified guide to SQL and PL/SQL, Shah, PHI.
6. Fundamentals of Database Management Systems, M. L. Gillenson, Wiley Student Edition.

JAWAHARLAL NEHRU TECHNOLOGICAL UNIVERSITY ANANTAPUR

(9A12501) AUTOMATA AND COMPILER DESIGN

B.Tech IV-I Sem. (E.C.M.)	**T**	**P**	**C**
	4	**0**	**4**

UNIT I

Formal Language and Regular Expressions: Languages, Definition Languages regular expressions, Finite Automata – DFA, NFA, Conversion of regular expression to NFA, NFA to DFA. Applications of Finite Automata to lexical analysis, lex tools.

UNIT II

Context Free grammars and parsing: Context free grammars, derivation, parse trees, ambiguity LL(K) grammars and LL(1) parsing.

UNIT III

Bottom up parsing handle pruning LR Grammar Parsing, LALR parsing, parsing ambiguous grammars, YACC programming specification.

UNIT IV

Semantics: Syntax directed translation, S-attributed and L-attributed grammars, Intermediate code – abstract syntax tree, translation of simple statements and control flow statements.

UNIT V

Context Sensitive features – Chomsky hierarchy of languages and recognizers, Type checking, type conversions, equivalence of type expressions, overloading of functions and operations.

UNIT VI

Run time storage: Storage organization, storage allocation strategies scope access to now local names, parameters, language facilities for dynamics storage allocation.

UNIT VII

Code optimization: Principal sources of optimization, optimization of basic blocks, peephole optimization, flow graphs, Data flow analysis of flow graphs.

UNIT VIII

Code generation: Machine dependent code generation, object code forms, generic code generation algorithm, Register allocation and assignment. Using DAG representation of Block.

TEXT BOOKS:

1. Introduction to Theory of computation, Sipser, 2nd Edition, Thomson.
2. Compilers Principles, Techniques and Tools Aho, Ullman, Ravisethi, Pearson Education.

REFERENCES:

1. Modern Compiler Construction in C, Andrew W.Appel Cambridge University Press.
2. Compiler Construction, LOUDEN, Cengage Learning.
3. Elements of Compiler Design, A.Meduna, Auerbach Publications, Taylor and Francis Group.
4. Principles of Compiler Design, V.Raghavan, TMH.
5. Engineering a Compiler, K.D.Cooper, L.Torczon, ELSEVIER.
6. Introduction to Formal Languages and Automata Theory and Computation – Kamala Krithivasan and Rama R, Pearson.
7. Modern Compiler Design, D.Grune and others, Wiley-India.
8. A Text book on Automata Theory, S.F.B.Nasir, P.K.Srimani, Cambridge Univ. Press.
9. Automata and Languages, A.Meduna, Springer.

JAWAHARLAL NEHRU TECHNOLOGICAL UNIVERSITY ANANTAPUR

(9A04802) DIGITAL IMAGE PROCESSING
(ELECTIVE - I)

B.Tech IV-I Sem. (E.C.M.)	**T**	**P**	**C**
	4	**0**	**4**

UNIT-I

DIGITAL IMAGE FUNDAMENTALS:

Image Sensing and Acquisition, Image Sampling & quantization, some basic Relationships between pixels. Mathematical tools used in digital image processing – array vs matrix operations, linear vs non linear operations, arithmetic operations, set and logical operations, spatial operations, vector and matrix operations, Probabilistic methods.

UNIT-II

IMAGE TRANSFORMS:

2D-DFT and properties, Walsh Transform, Hadamard Transform, Discrete cosine Transform, Haar-Transform, Slant Transform, KL transform, comparison of different image transforms.

UNIT-III

IMAGE ENHANCEMENT IN THE SPATIAL DOMAIN:

Basic Intensity transformations functions, histogram Processing, fundamentals of Spatial Filtering, Smoothing Spatial filters, Sharpening spatial filters, Combining spatial enhancement methods.

UNIT-IV

IMAGE ENHANCEMENT IN FREQUENCY DOMAIN:

Basics of filtering in frequency domain, additional characteristics of the frequency domain, correspondence between filtering in the spatial and frequency domains. Image smoothing using frequency domain filters, image sharpening using frequency domain filters – Gaussian High pass filters, laplacian in the frequency domain, Homomorphic filtering.

UNIT-V

IMAGE DEGRADATION / RESTORATION:

Noise models, Restoration in the presence of Noise only-spatial filtering,-mean ,order- statistic and adaptive filters, Estimating the Degradation function, Inverse filtering, Weiner filtering, Constrained Least squares filtering.

UNIT-VI

IMAGE SEGMENTATION:

Point, line and edge Detection, Thresholding, Region based segmentation, the use of motion in segmentation.

UNIT-VII

IMAGE COMPRESSION:

Need for Image compression, Classification of Redundancy in Images, Image compression models, Classification of image compression schemes, Run length coding, arithmetic coding, Block truncation coding, Dictionary based compression, transform based compression, Image compression standards, Scalar quantization, vector quantization.

UNIT-VIII

COLOR IMAGE PROCESSING:

Color models, pseudo color image processing, color transformations, Smoothing and sharpening, image segmentation based on color.

TEXT BOOKS:

1. Digital Image Processing-R. C .Gonzalez & R.E. Woods, Addison Wesley/Pearson education, 3rd Edition, 2010.
2. Digital Image processing– S jayaraman, S Esakkirajan, T Veerakumar, Tata Mcgraw Hill.

REFERENCES:

1. Digital Image processing using MATLAB-Rafael C.Gonzalez, Richard E woods and Steven L.Edition, Tata Mcgraw Hill, 2010.
2. Fundamentals of Digital Image processing-A .K. Jain, PHI.

JAWAHARLAL NEHRU TECHNOLOGICAL UNIVERSITY ANANTAPUR

(9A05605) ARTIFICIAL INTELLIGENCE (ELECTIVE - I)

B.Tech IV-I Sem. (E.C.M.)	**T**	**P**	**C**
	4	**0**	**4**

UNIT I

What is Artificial Intelligence: The AI Problems, The Underlying Assumption, What is an AI Technique?, The Levels of the Model, Criteria of Success, Some General References, One Final Word and Beyond. Problems, Problem Spaces, and Search: Defining the Problem as a State Space Search, Production Systems, Problem Characteristics, Production System Characteristics, Issues in the Design of Search Programs.

UNIT II

Problem-Solving**:** Uninformed Search Strategies, Avoiding Repeated States. Informed Search and Exploration: Informed (Heuristic) Search Strategies, Heuristic Functions, Local Search Algorithms and Optimization Problems, Local Search in Continuous Spaces, Backtracking Search for CSPs.

UNIT III

Knowledge and Reasoning: Logical Agents, Knowledge–Based Agents, The Wumpus World, Logic, Propositional Logic a Very Simple Logic, Reasoning Patterns in Propositional Logic, Effective Propositional Inference, Agents Based on Propositional Logic.

UNIT IV

First-Order Logic: Representation Revisited, Syntax and Semantic of First-Order Logic, Using First-Order Logic, Knowledge Engineering in First-Order Logic. Inference in First-Order Logic: Propositional vs. First-Order Inference, Unification and Lifting, Forward Chaining, Backward Chaining, Resolution.

UNIT V

Knowledge Representation: Ontological Engineering, Categories and Objects, Actions, Situations, and Events, Mental Events and Mental Objects, The Internet Shopping World, Reasoning Systems for Categories, Reasoning with Default Information, Truth Maintenance Systems.

UNIT VI

Uncertain Knowledge and Reasoning: Uncertainty, Acting Under Uncertainty, Basic Probability Notation, The Axioms of Probability, Inference Using Full Joint Distributions, Independence, Bayes' Rule and Its Use.

UNIT VII

Learning: Learning from Observations, Forms of Learning, Inductive Learning, Learning Decision Trees, Ensemble Learning, Why Learning Works: Computational Learning Theory, Knowledge in Learning: A Logical Formulation of Learning, Knowledge in Learning.

UNIT VIII

Statistical Learning Methods: Neural Networks. Fuzzy Logic Systems: Introduction, Crisp Sets, Fuzzy Sets, Some Fuzzy Terminology, Fuzzy Logic Control, Sugeno Style of Fuzzy Inference Processing, Fuzzy Hedges, α Cut Threshold.

TEXT BOOKS:

1. Artificial Intelligence, Third Edition, Elaine Rich, Kevin Knight and Shivashankar B Nair, Tata McGraw Hill.
2. Artificial Intelligence A Modern Approach, 2nd Edition, Stuart Russell and Peter Norvig Pearson Education.

REFERNCES:

1. Artificial Intelligence and Intelligent Systems, N.P.Padhy, Oxford University Press.
2. Artificial Intelligence-Structures and Strategies for Complex Problem Solving, 4th Edition, George F. Luger, Pearson Education.

JAWAHARLAL NEHRU TECHNOLOGICAL UNIVERSITY ANANTAPUR

(9A10505) PRINCIPLES OF COMMUNICATIONS (ELECTIVE - I)

B.Tech IV-I Sem. (E.C.M.)	T	P	C
	4	0	4

UNIT I

INTRODUCTION: Block diagram of Electrical communication system, Radio communication: Types of communications, Analog, pulse and digital Types of signals, Fourier Transform for various signals, Fourier Spectrum, Power spectral density, Autocorrelation, correlation, convolution.

UNIT II

AMPLITUDE MODULATION: Need for modulation, Types of Amplitude modulation, AM, DSB SC, SSB SC, Power and BW requirements, generation of AM, DSB SC, SSB SC, Demodulation of AM: Diode detector, Product demodulation for DSB SC & SSB SC.

UNIT III

ANGLE MODULATION: Frequency & Phase modulations, advantages of FM over AM, Bandwidth consideration, Narrow band and Wide band FM, Comparison of FM & PM.

UNIT IV

PULSE MODULATIONS: Sampling, Nyquist rate of sampling, Sampling theorem for Band limited signals, PAM, regeneration of base band signal, PWM and PPM, Time Division Multiplexing, Frequency Division Multiplexing, Asynchronous Multiplexing.

UNIT V

DIGITAL COMMUNICATION: Advantages, Block diagram of PCM, Quantization, effect of quantization, quantization error, Base band digital signal, DM, ADM, ADPCM and comparison.

UNIT VI

DIGITAL MODULATION: ASK, FSK, PSK, DPSK, QPSK demodulation, coherent and incoherent reception, Modems.

UNIT VII

INFORMATION THEORY: Concept of information, rate of information and entropy, Source coding for optimum rate of information, Coding efficiency, Shanon-Fano and Huffman coding.

UNIT VIII

ERROR CONTROL CODING: Introduction, Error detection and correction codes, block codes, convolution codes.

TEXT BOOKS:

1. Communication Systems Analog and Digital – R.P. Singh and SD Sapre, TMH, 20th reprint, 2004.
2. Principles of Communications – H. Taub and D. Schilling, TMH, 2003.

REFERENCES

1. Electronic Communication Systems – Kennedy and Davis, TMH, 4th edition, 2004.
2. Communication Systems Engineering – John. G. Proakis and Masoud Salehi, PHI, 2nd Ed. 2004.

JAWAHARLAL NEHRU TECHNOLOGICAL UNIVERSITY ANANTAPUR

(9A04704) DSP PROCESSORS AND ARCHITECTURES
(Common to ECE, ECM)
(ELECTIVE – II)

B.Tech IV-I Sem. (E.C.M.)	**T**	**P**	**C**
	4	**0**	**4**

UNIT I

INTORODUCTION TO PRGRAMMABLE DSPs:

Multiplier & Multiplier accumulator, Modified bus structures & memory access schemes in P – DSPs, Multiple access memory, Multi ported memory, VLIW architecture, Pipelining, Special addressing modes in P – DSPs, On chip peripherals.

UNIT II

COMPUTATIONAL ACCURACY IN DSP IMPLEMENTATIONS:

Number formats for signals and coefficients in DSP systems, Dynamic Range and Precision, Sources of error in DSP implementations, A/D Conversion errors, DSP Computational errors, D/A Conversion Errors, Compensating filter.

UNIT III

ARCHITECTURES FOR PROGRAMMABLE DSP DEVICES:

Basic Architectural features, DSP Computational Building Blocks, Bus Architecture and Memory, Data Addressing Capabilities, Address Generation Unit, Programmability and Program Execution, Speed Issues, Features for External interfacing.

UNIT IV

PROGRAMMABLE DIGITAL SIGNAL PROCESSORS:

Commercial Digital signal-processing Devices, Data Addressing modes of TMS320C54XX DSPs, Data Addressing modes of TMS320C54XX Processors, Memory space of TMS320C54XX Processors, Program Control, TMS320C54XX instructions and Programming, On-Chip Peripherals, Interrupts of TMS320C54XX processors, Pipeline Operation of TMS320C54XX Processors.

UNIT V

IMPLEMENTATIONS OF BASIC DSP ALGORITHMS:

The Q-notation, FIR Filters, IIR Filters, Interpolation Filters, Decimation Filters, PID Controller, Adaptive Filters, 2-D Signal Processing.

UNIT VI

IMPLEMENTATION OF FFT ALGORITHMS:

An FFT Algorithm for DFT Computation, A Butterfly Computation, Overflow and scaling, Bit-Reversed index generation, An 8-Point FFT implementation on the TMS320C54XX, Computation of the signal spectrum.

UNIT VII

INTERFACING MEMORY AND I/O PERIPHERALS TO PROGRAMMABLE DSP DEVICES:

Memory space organization, External bus interfacing signals, Memory interface, Parallel I/O interface, Programmed I/O, Interrupts and I/O, Direct memory access (DMA). A Multichannel buffered serial port (McBSP), McBSP Programming, a CODEC interface circuit, CODEC programming, A CODEC-DSP interface example.

UNIT VIII

RECENT TRENDS IN DSP SYSTEM DESIGN:

An over - view of the application nodes on DSP systems, An over - view of open multimedia applications platform (OMAP), An Introduction to FPGA, Design flow for an FPGA based system design, Cad tools for FPGA based system design, soft core processors, FPGA based DSP system design, New algorithms for Implementation of filters in VLSI, Distributed arithmetic algorithm, Case studies, Comparison of the performances of the systems designed using FPGAs and digital signals processors.

TEXT BOOKS:

1. Digital Signal Processing – Avtar Singh and S. Srinivasan, Thomson Publications, 2004.

2. Digital Signal Processors, Architecture, Programming and Applications – B. Venkata Ramani and M. Bhaskar, TMH, 2004.

REFERENCES:

1. Digital Signal Processing – Jonathan Stein, John Wiley, 2005.
2. DSP Processor Fundamentals, Architectures & Features – Lapsley et al. S. Chand & Co, 2000.

JAWAHARLAL NEHRU TECHNOLOGICAL UNIVERSITY ANANTAPUR

(9A15801) HIGH SPEED NETWORKING
(ELECTIVE – II)

B.Tech IV-I Sem. (E.C.M.)	**T**	**P**	**C**
	4	**0**	**4**

UNIT I

NETWORK SERVICES & LAYERED ARCHITECTURE: Traffic characterization and quality of service, Network services, High performance networks, Network elements, Basic network mechanisms, layered architecture.

UNIT II

ISDN & B-ISDN: Over view of ISDN, ISDN channels, User access, ISDN protocols, Brief history of B-ISDN and ATM, ATM based services and applications, principles and building block of B-ISDN, general architecture of B-ISDN, frame relay.

UNIT III

ATM NETWORKS: Network layering, switching of virtual channels and virtual paths, applications of virtual channels and connections.

UNIT IV

QOS parameters, traffic descriptors, ATM service categories, ATM cell header, ATM layer, ATM adaptation layer.

UNIT V

INTERCONNECTION NETWORKS: Introduction, Banyan Networks, Routing algorithm & blocking phenomenon, Batcher-Banyan networks, crossbar switch, three stage class networks.

UNIT VI:

REARRANGEABLE NETWORKS: Rearrangeable class networks, folding algorithm, bens network, looping algorithm.

UNIT VII

ATM SIGNALING, ROUTING AND TRAFFIC CONTROL: ATM addressing, UNI signaling, PNNI signaling, PNNI routing, ABR Traffic management.

UNIT VIII

TCP/IP NETWORKS: History of TCP/IP, TCP application and Services, Motivation, TCP, UDP, IP services and Header formats, Internetworking, TCP congestion control, Queue management: Passive & active, QOS in IP networks: differentiated and integrated services.

TEXT BOOKS:

1. ISDN & B-ISDN with Frame Relay – William Stallings, PHI.
2. Communication Networks - Leon Garcia widjaja, TMH, 2000.
3. ATM Fundamentals – N. N. Biswas, Adventure books publishers, 1998.

REFERENCES:

1. High Performance TCP/IP Networking – Mahbub Hassan, Raj Jain, PHI, 2005.
2. ATM Networks-Rainer Handel, Manfred N.Hubber, Stefan Schroder, Pearson Edu 2002.
3. High Speed Networks and Internets – William Stallings, Pearson edu., 2002.
4. High Performance Communication Networks – T. Walrand & P. Varaiya, 2nd ed., Harcourt Asia Publ.

JAWAHARLAL NEHRU TECHNOLOGICAL UNIVERSITY ANANTAPUR

(9A19701) MOBILE COMPUTING
(ELECTIVE – II)

B.Tech IV-I Sem. (E.C.M.)	**T**	**P**	**C**
	4	**0**	**4**

UNIT - I

INTRODUCTION TO MOBILE COMMUNICATIONS AND COMPUTING: MOBILE COMPUTING (MC): Introduction to MC, novel applications, limitations, and architecture.

GSM: Mobile services, System architecture, Radio interface, Protocols, Localization and calling, Handover, Security, and New data services.

UNIT – II

(WIRELESS) MEDIUM ACCESS CONTROL: Motivation for a specialized MAC (Hidden and exposed terminals, Near and far terminals), SDMA, FDMA, TDMA, CDMA.

UNIT - III

MOBILE NETWORK LAYER: Mobile IP (Goals, assumptions, entities and terminology, IP packet delivery, agent advertisement and discovery, registration, tunneling and encapsulation, optimizations), Dynamic Host Configuration Protocol (DHCP).

UNIT - IV

MOBILE TRANSPORT LAYER: Traditional TCP, Indirect TCP, Snooping TCP, Mobile TCP, Fast retransmit/fast recovery, Transmission /time-out freezing, Selective retransmission, Transaction oriented TCP.

UNIT - V

DATABASE ISSUES: Hoarding techniques, caching invalidation mechanisms, client server computing with adaptation, power-aware and context-aware computing, transactional models, query processing, recovery, and quality of service issues.

UNIT - VI

DATA DISSEMINATION: Communications asymmetry, classification of new data delivery mechanisms, push-based mechanisms, pull-based mechanisms, hybrid mechanisms, selective tuning (indexing) techniques.

UNIT - VII

MOBILE AD HOC NETWORKS (MANETs): Overview, Properties of a MANET, spectrum of MANET applications, routing and various routing algorithms, security in MANETs.

UNIT - VIII

PROTOCOLS AND TOOLS: Wireless Application Protocol-WAP. (Introduction, protocol architecture, and treatment of protocols of all layers), Bluetooth (User scenarios, physical layer, MAC layer, networking, security, link management) and J2ME.

TEXT BOOKS:

1. Jochen Schiller,"Mobile Communications", Addison-Wesley. (Chapters 4, 7, 9, 10, 11), 2nd edition, 2004.
2. Stojmenovic and Cacute, "Handbook of Wireless Networks and Mobile Computing", Wiley, 2002, ISBN 0471419028. (Chapters 11, 15, 17, 26 and 27).

REFERENCES:

1. Reza Behravanfar, "Mobile Computing Principles: Designing and Developing Mobile Applications with UML and XML", ISBN: 0521817331, Cambridge University Press, October 2004.
2. Adelstein, Frank, Gupta, Sandeep KS, Richard III, Golden , Schwiebert, Loren, "Fundamentals of Mobile and Pervasive Computing", ISBN: 0071412379, McGraw-Hill Professional, 2005.
3. Hansmann, Merk, Nicklous, Stober, "Principles of Mobile Computing", Springer, 2nd edition, 2003.
4. Martyn Mallick, "Mobile and Wireless Design Essentials", Wiley DreamTech, 2003.

JAWAHARLAL NEHRU TECHNOLOGICAL UNIVERSITY ANANTAPUR

(9A19702) DIGITAL SIGNAL PROCESSING LAB

B.Tech IV-I Sem. (E.C.M.)	**T**	**P**	**C**
	0	**3**	**2**

LIST OF EXPERIMENTS:

1. To study the architecture of DSP chips – TMS 320C 5X/6X Instructions.
2. To verify linear convolution.
3. To verify the circular convolution.
4. To design FIR filter (LP/HP) using windowing technique

a) Using rectangular window
b) Using triangular window
c) Using Kaiser Window

5. To Implement IIR filter (LP/HP) on DSP Processors
6. N-point FFT algorithm.
7. MATLAB program to generate sum of sinusoidal signals.
8. MATLAB program to find frequency response of analog LP/HP filters.
9. To compute power density spectrum of a sequence.
10. To find the FFT of given 1-D signal and plot.

JAWAHARLAL NEHRU TECHNOLOGICAL UNIVERSITY ANANTAPUR

(9A10706) ELECTRONIC DESIGN AND AUTOMATION LAB
(Common to EIE, ECM)

B.Tech IV-I Sem. (E.C.M.)	**T**	**P**	**C**
	0	**3**	**2**

ANALOG CIRCUIT SIMULATION:

Design and simulate the following experiments using Multisium or Pspice or equivalent simulation software.

1. Common Emitter and Common Source Amplifier.
2. Two stage RC Coupled Amplifier.
3. Any two of the following:
   (i). Current Series Feedback amplifier.
   (ii). Voltage Series Feedback amplifier.
   (iii). Voltage Shunt Feedback amplifier.
   (iv). Current Shunt Feedback amplifier.
4. RC Phase Shift Oscillator.
5. Class A / Class B Power Amplifier.
6. High Frequency Common Base (BJT) / Common Gate (JFET) amplifier.

LINEAR INTEGRATED CIRCUITS SIMULATION:

1. Pspice modeling of Inverting Summing Amplifier.
2. Pspice modeling of Instrumentation Amplifier using Op-Amp.
3. Pspice modeling of Triangular Waveform generator.
4. Pspice modeling and simulation of the absolute value output circuit using Op-Amp.
5. Pspice modeling of Astable Multivibrator using 555 Timer.
6. Pspice modeling of Zero-cross detector.
7. Pspice modeling of A/D Converter using Op-Amp.
8. Pspice modeling of D/A Converter using Op-Amp.

JAWAHARLAL NEHRU TECHNOLOGICAL UNIVERSITY ANANTAPUR

(9A05704) ADVANCED COMPUTER ARCHITECTURE

B.Tech IV-II Sem. (E.C.M.)	**T**	**P**	**C**
	4	**0**	**4**

UNIT I

Parallel Computer Models: The state of computing-Multiprocessors and Multi computers- Multivector and SIMD Computers, PRAM and VLSI Models, Architectural Development tracks. Program and Networks Properties: Conditions of Parallelism, Program Partitioning and Scheduling, Program Flow Mechanisms, System Interconnect Architectures.

UNIT II

Principles of Scalable Performance: Performance Metrics and Measures, Parallel Processing Applications, Speedup Performance Laws, Scalability Analysis and Approaches. Processors and Memory Hierarchy: Advanced Processor Technology, Superscalar and Vector Processors, Memory Hierarchy Technology.

UNIT III

Bus, Cache and Shared Memory: Bus Systems, Cache Memory Organizations, Shared-Memory Organizations. Pipelining and Super Scalar Techniques: Linear Pipeline Processors, Nonlinear Pipeline Processors, Instruction Pipeline Design, Arithmetic Pipeline Design.

UNIT IV

Multiprocessors and Multicomputer: Multiprocessor System Interconnects Cache Coherence and Synchronization Mechanisms, Three Generations of Multicomputers, Message-Passing Mechanisms.

UNIT V

Multivector and SIMD Computers: Vector Processing Principles, Multivector MultiProcessors, Compound Vector Processing, SIMD Computer Organizations, The Connection Machine CM-5.

UNIT VI

Scalable, Multithreaded, and Dataflow Architectures: Latency, Hiding Techniques, Principles of Multithreading, Fine-Grain Multicomputers, Scalable and Multithreaded Architectures, Dataflow and Hybrid Architectures.

UNIT VII

Instruction Level Parallelism: Introduction, Basic Design Issues, Problem Definition, Model of a Typical Processor, Operand Forwarding, Reorder Buffer, Register Renaming-Tomasulo's Algorithm, Branch Prediction, Limitations in Exploiting Instruction Level Parallelism, Thread Level Parallelism.

UNIT VIII

Trends in Parallel Systems: Brief Overview of Technology, Forms of Parallelism, Case Studies.

TEXT BOOKS:

1. Advanced Computer Architecture- by Kai Hwang & Jotwani, 2nd Edition, McGraw-Hill Publications.

REFERENCES:

1. Advanced Computer Architecture, D.Sima, T.Fountain, P.Kacsuk, Pearson Education.
2. Computer Architecture A quantitative approach 3rd edition john L.Hennessy & David A. Patterson, Morgan Kufmann (An Imprint of Elsevier).
3. Computer Architecture and parallel processing by Hwang and Briggs.

JAWAHARLAL NEHRU TECHNOLOGICAL UNIVERSITY ANANTAPUR

(9A04605) VLSI DESIGN

B.Tech IV-II Sem. (E.C.M.)	**T**	**P**	**C**
	4	**0**	**4**

UNIT I

INTRODUCTION

Introduction to IC technology-MOS,PMOS,NMOS,CMOS and BI-CMOS technologies-oxidation, lithiography, diffusion, Ion implantation, metallisation, Encapsulation, probe testing, integrated resistors and capacitors.

UNIT II

BASIC ELECTRICAL PROPERTIES

Basic electrical properties of MOS and BI-CMOS circuits: I_{ds}-V_{ds} relationships, MOS transistor threshold voltage, g_m, g_{ds}, figure of merit; pass transistor, NMOS inverter, various pull-ups, CMOS inverter analysis and design, BI-CMOS inverters.

UNIT III

VLSI CIRCUIT DESIGN PROCESSES

VLSI design flow, MOS layers, stick diagrams, design rules and lay out,2 m CMOS design rules for wires, contacts and transistors layout diagrams for NMOS and CMOS inverters and gates, scaling of MOS circuits, limitations of scaling.

UNIT IV

GATE LEVEL DESIGN

Logic gates and other complex gates, switch logic, alternate gate circuits, basic circuit concepts, sheet resistance RS and its concept to MOS, area capacitance units, calculations-(Micro)-delays, driving large capacitive loads, wiring capacitances, fan-in and fan-out, choice of layers.

UNIT V

SUB SYSTEM DESIGN

Sub system design, shifters, adders, ALUs, multipliers, parity generators, comparators, zero/one detectors, counters, high density memory elements.

UNIT VI

SEMICONDUCTOR INTEGRATED CIRCUIT DESIGN

PLAs, FPGAs, CPLDs, standard cells, programmable array logic, design approach.

UNIT VII

VHDL SYNTHESIS

VHDL synthesis, circuit design flow, circuit synthesis, simulation, layout, design capture tools, design verification tools, test principles.

UNIT VIII

CMOS TESTING

CMOS testing need for testing, test principles, design strategies for test, chip level test techniques, system-level test techniques, layout design for improved testability.

TEXT BOOKS:

1. Essentials of VLSI circuits and systems-kamran Eshraghian, Eshraghian Dougles and A. pucknell, PHI, 2005 Edition.
2. Principles of CMOS VLSI design-Weste and EShraghian, Pearson Education, 1999.

REFERENCES:

1. Introduction to VLSI circuits and systems-John P.Uyemura, John Wiley, 2003.
2. Digital Integrated circuits-John M. Rabaey, PHI, EEE, 1997.
3. Modern VLSI design-Wayne wolf, Pearson Education, 3rd Edition, 1997.

JAWAHARLAL NEHRU TECHNOLOGICAL UNIVERSITY ANANTAPUR

(9A19801) CLIENT SERVER COMPUTING (ELECTIVE III)

B.Tech IV-II Sem. (E.C.M.)	**T**	**P**	**C**
	4	**0**	**4**

UNIT I

Introduction to client server computing: Evolution of Corporate Computing Models from Centralized to Distributed Computing, Client Server Models. Benefits of Client Server Computing, Pitfalls of Client Server Programming.

UNIT II

Review of Java concepts: Java Fundamentals, Exceptions, Threads, Strings, Java Applets, AWT, Swings, Stream I/O, Socket Interface, RMI, RMI API, JDBC.

UNIT III

CORBA with Java: Client/Server CROBA-style, The Object Web: CORBA with Java, CORBA/Java.

UNIT IV

Core CORBA /Java: Two Types of Client/ Server Invocations –Static, Dynamic. The Static CORBA, First CORBA Program, ORBlets with Applets, Dynamic CORBA- The Portable Count, The Dynamic Count, Multi count.

UNIT V

Other Technologies: Creating Client/ Server Application Using-Java Sockets, HTTP/CGI. Servlets, RMI, Caffeine

UNIT VI

Existential CORBA: CORBA Initialization Protocol, CORBA Activation Services, CORBAIDL Mapping CORBA Java – to- IDL Mapping, The Introspective CORBA/Java Object.

UNIT VII

Java Bean Component Model**:** Events, Properties, Persistency, Interspection of Beans, CORBA Beans.

UNIT VIII

EJBs and CORBA: Object Transaction Monitors CORBA OTM's, EJB and CORBA OTM's, EJB Container Framework, Session and Entity Beans, The EJB Client/Server Development Process, The EJB Container Protocol, Support for Transaction EJB Packaging EJB Design Guidelines.

TEXT BOOKS:

1. Client Server Programming with Java and CORBA, Robert Orfali, Dan Harkey, 2nd Edition, John Wiley.
2. Java Programming with CORBA, G. Brose, A. Vogel, K. Duddy, 3rd Edition, Wiley-DreamTech.

REFERENCES:

1. Java: Complete Reference, Seventh Editon, Herbert Schildt, TMH.
2. Distributed Computing- Principles and Applications, M.L.Liu, Pearson Education.
3. Client/Server Survival Guide, Robert Orfali, Dan Harkey, Jeri Edwards, 3rd Edition, John Wiley.
4. Client/Server Computing, DT Dewire, TMH.
5. IBM Webspere Starter Kit, Ron Ben Natan, Ori Sasson, TMH.

JAWAHARLAL NEHRU TECHNOLOGICAL UNIVERSITY ANANTAPUR

(9A04806) WIRELESS COMMUNCIATIONS AND NETWORKS
(Common to ECE, ECM)
(ELECTIVE – III)

B.Tech IV-II Sem. (E.C.M.)	**T**	**P**	**C**
	4	**0**	**4**

UNIT I

MULTIPLE ACCESS TECHNIQUES FOR WIRELESS COMMUNICATION: Introduction, FDMA, TDMA, Spread Spectrum, Multiple access, SDMA, Packet radio, Packet radio protocols, CSMA protocols, Reservation protocols.

UNIT II

INTRODUCTION TO WIRELESS NETWORKING:
Introduction, Difference between wireless and fixed telephone networks, Development of wireless networks, Traffic routing in wireless networks.

UNIT III

WIRELESS DATA SERVICES:
CDPD, ARDIS, RMD, Common channel signaling, ISDN, BISDN and ATM, SS7, SS7 user part, signaling traffic in SS7.

UNIT IV

MOBILE IP AND WIRELESS ACCESS PROTOCOL:
Mobile IP Operation of mobile IP, Co-located address, Registration, Tunneling, WAP Architecture, overview, WML scripts, WAP service, WAP session protocol, wireless transaction, Wireless datagram protocol.

UNIT V

WIRELESS LAN TECHNOLOGY:
Infrared LANs, Spread spectrum LANs, Narrow bank microwave LANs, IEEE 802 protocol Architecture, IEEE802 architecture and services, 802.11 medium access control, 802.11 physical layer.

UNIT VI

BLUE TOOTH:

Overview, Radio specification, Base band specification, Links manager specification, Logical link control and adaptation protocol, Introduction to WLL Technology.

UNIT VII

MOBILE DATA NETWORKS:

Introduction, Data oriented CDPD Network, GPRS and higher data rates, Short messaging service in GSM, Mobile application protocol.

UNIT VIII

WIRELESS ATM & HIPER LAN:

Introduction, Wireless ATM, HIPERLAN, Adhoc Networking & WPAN.

TEXT BOOKS:

1. Wireless Communication and Networking – William Stallings, PHI, 2003.
2. Wireless Communications, Principles, Practice – Theodore, S. Rappaport, PHI, 2nd Edn., 2002.

REFERENCES:

1. Wireless Digital Communications – Kamilo Feher, PHI, 1999.
2. Principles of Wireless Networks – Kaveh Pah Laven & P. Krishna Murthy, Pearson Education, 2002.

JAWAHARLAL NEHRU TECHNOLOGICAL UNIVERSITY ANANTAPUR

(9A05709) INFORMATION SECURITY
(Common to ECE, ECM)
(ELECTIVE - III)

B.Tech IV-II Sem. (E.C.M.)	**T**	**P**	**C**
	4	**0**	**4**

UNIT I

Is There A Security Problem In Computing: What Does Security Mean?, Attacks, The Meaning Of Computer Security, Computer Criminals, Methods of Defense, Terminology and Background, Substitution Ciphers, Transpositions(Permutations), Making good Encryption Algorithm, The Data Encryption Standard.

UNIT II

Program Security: Secure Programs, NonMalicious Program Errors, Viruses and Other Malicious Code, Targeted Malicious Code.

UNIT III

Public-Key Cryptography and RSA, Key Management; Other public key Cryptosystems, Message Authentication and Hash Functions: Authentication Requirements, Authentication Functions, Message Authentication Codes, Hash Functions, Security Hash Functions and MACs

Hash and MAC Algorithms: Secure Hash Algorithm, Whirlpool.

UNIT IV

Digital Signatures and Authentication Protocols: Digital Signatures, Authentication Protocols.

UNIT V

Authentication Applications: Kerberos, Electronic Mail Security: Pretty Good Privacy, S/MIME.

UNIT VI

IP Security: IP Security Overview, IP Security Architecture, Authentication Header, Encapsulating Security Payload, Combing Security Associations, Key Management.

UNIT VII

Web Security: Web Security Considerations, Secure Socket Layer and Transport Layer Security, Secure Electronic Transaction.

UNIT VIII

Intruders: Intruders, Intrusion Detection, Password Management, Firewalls: Firewall Design and Principles, Trusted Systems.

TEXT BOOKS:

1. Security in Computing, Charles P. Pfleeger, Shari Lawrence Pfleeger, Deven Shah; Pearson Education.
2. Cryptography and Network Security, William Stallings 4th Edition, Pearson Education.

REFERENCES:

1. Cryptography and Information Security, V.K.Pachghare, PHI.
2. Information Security -Theory and Practice, Dhiren R. Patel, PHI.
3. Introduction to Cryptography, Buchmann, Springer.
4. Principles and Practices of Information Security, Michael E. Whitman and Herbert J. Mattord, Cengage Learning.
5. Information Systems Security, Godbole, Wiley Student Edition.

JAWAHARLAL NEHRU TECHNOLOGICAL UNIVERSITY ANANTAPUR

(9A04706) DIGITAL DESIGN THROUGH VERILOG HDL (ELECTIVE – IV)

B.Tech IV-II Sem. (E.C.M.)	**T**	**P**	**C**
	4	**0**	**4**

UNIT–I

INTRODUCTION TO VERILOG:

Verilog as HDL, Levels of design description, concurrency, simulation and synthesis, functional verification, system tasks, programming language interface(PLI), module, simulation and synthesis tools, test benches.

LANGUAGE CONSTRUCTS AND CONVENTIONS:

Introduction, keywords, identifiers, whitespace characters, comments, numbers, strings, logic values, strengths, data types, scalars and vectors, parameters, memory, operators, system tasks, exercises.

UNIT-II

GATE LEVEL MODELLING:

Introduction, AND gate primitive, module structure, other gate primitives, illustrative examples, tristate gates, array of instances of primitives, additional examples, design of Flip flops with gate primitives, delays, strengths and contention resolution, net types, design of basic circuits, exercises.

UNIT-III

BEHAVIORAL MODELLING:

Introduction, operations and assignments, functional Bifurcation, initial construct, always construct, examples, assignments with delays, wait construct, multiple always blocks, designs at behavioral level, blocking and non-blocking assignments, the case statement, simulation flow, if and if else constructs, assign-De assign construct, repeat construct, FOR loop, the disable construct, While loop, Forever loop, parallel blocks, force-release construct, event.

UNIT-IV

MODELLING AT DATAFLOW LEVEL:

Introduction, continuous assignment structures, delays and continuous assignments, assignment to vectors, operators.

SWITCH LEVEL MODELLING: Introduction, basic transistor switches, CMOS switch, Bidirectional gates, time delays with switch primitives, instantiations with strengths and delays, strength contention with trireg nets, exercises.

UNIT-V

SYSTEM TASKS, FUNCTIONS AND COMPILER DIRECTIVES:

Introduction, parameters ,path delays, module parameters, system tasks and functions, file–based tasks and Functions, Compiler Directives, Hierarchical Access, General Observations, Exercises.

FUNCTIONS, TASKS, AND USER-DEFINED PRIMITIVES:

Introduction, Function, Tasks, User- Defined Primitives (UDP), FSM Design (Moore and Mealy Machines).

UNIT VI

DIGITAL DESIGN WITH SM CHARTS:

State Machine Charts, Derivation of SM Charts, Realization of SM Charts, Implementation of the Dice Game, Alternative realizations for SM Charts using Microprogramming, Linked State Machines.

UNIT VII

DESIGNING WITH PROGRAMMABLE GATE ARRAYS AND COMPLEX PROGRAMMABLE LOGIC DEVICES:

Xilinx 3000 Series FPGAs, Designing with FPGAs, Using a One-Hot State Assignment, Altera Complex Programmable Logic Devices (CPLDs), Altera FLEX 10K Series CPLDs.

UNIT VIII

VERILOG MODELS:

Static RAM Memory, A simplified 486 Bus Model, Interfacing Memory to a Microprocessor Bus, UART Design, Design of Microcontroller CPU.

TEXT BOOKS:

1. Design through Verilog HDL – T.R. Padmanabhan and B. Bala Tripura Sundari, WSE, 2004 IEEE Press.
2. A Verilog Primier – J. Bhasker, BSP, 2003.

REFERENCES:

1. Fundamentals of Logic Design with Verilog – Stephen. Brown and Zvonko Vranesic, TMH, 2005.
2. Advanced Digital Design with Verilog HDL – Michael D. Ciletti, PHI, 2005.

JAWAHARLAL NEHRU TECHNOLOGICAL UNIVERSITY ANANTAPUR

(9A19802) FAULT TOLERANT SYSTEMS
(ELECTIVE - IV)

B.Tech IV-II Sem. (E.C.M.)	**T**	**P**	**C**
	4	**0**	**4**

UNIT I

BASIC CONCEPTS: Reliability concepts, Failure & Faults, Reliability and failure rate, Relation between reliability and Mean time between failure, Maintainability and Availability, Reliability of series, Parallel and Parallel-Series combinational circuits.

UNIT II

FAULT TOLERANT DESIGN: Basic concepts – Static, dynamic, hybrid, Triple Modular Redundant System, Self purging redundancy, Siftout redundancy (SMR), SMR Configuration, Use of error correcting code, Time redundancy and software redundancy.

UNIT III

SELF CHECKING CIRCUITS: Basic concepts of Self checking circuits, Design of Totally Self Checking checker, Checkers using m out of n codes, Berger code, Low cost residue code.

UNIT IV

FAIL SAFE DESIGN: Strongly fault secure circuits, fail safe design of sequential circuits using partition theory and Berger code, Totally self checking PLA design.

UNIT V

DESIGN FOR TESTABILITY FOR COMBINATIONAL CIRCUITS: Basic concepts of testability, controllability and observability, the Reed Muller's expansion technique, OR-AND-OR design, use of control and syndrome testable design.

UNIT VI:

Theory and operation of LFSR, LFSR as Signature analyzer, Multiple-input Signature Register.

UNIT VII

DESIGN FOR TESTABILITY FOR SEQUENTIAL CIRCUITS: Controllability and observability by means of scan register, Storage cells for scan design, classic scan design, Level Sensitive Scan Design (LSSD).

UNIT VIII

BUILT IN SELF TEST: BIST concepts, Test pattern generation for BIST exhaustive testing, Pseudorandom testing, pseudo exhaustive testing, constant weight patterns, Generic offline BIST architecture.

TEXT BOOKS:

1. Parag K. Lala – "Fault Tolerant & Fault Testable Hardware Design", (PHI).
2. M. Abramovili, M.A. Breues, A. D. Friedman – "Digital Systems Testing and Testable Design", Jaico publications.

JAWAHARLAL NEHRU TECHNOLOGICAL UNIVERSITY ANANTAPUR

(9A05706) DATA WAREHOUSING AND DATA MINING (ELECTIVE - IV)

B.Tech IV-II Sem. (E.C.M.)	T	P	C
	4	0	4

UNIT I

Introduction: Fundamentals of data mining, Data Mining Functionalities, Classification of Data Mining systems, Data Mining Task Primitives, Integration of a Data Mining System with a Database or a Data Warehouse System, Major issues in Data Mining. Data Preprocessing: Need for Preprocessing the Data, Data Cleaning, Data Integration and Transformation, Data Reduction, Discretization and Concept Hierarchy Generation.

UNIT II

Data Warehouse and OLAP Technology for Data Mining: Data Warehouse, Multidimensional Data Model, Data Warehouse Architecture, Data Warehouse Implementation, Further Development of Data Cube Technology, From Data Warehousing to Data Mining. Data Cube Computation and Data Generalization: Efficient Methods for Data Cube Computation, Further Development of Data Cube and OLAP Technology, Attribute-Oriented Induction.

UNIT III

Mining Frequent Patterns, Associations and Correlations: Basic Concepts, Efficient and Scalable Frequent Itemset Mining Methods, Mining various kinds of Association Rules, From Association Mining to Correlation Analysis, Constraint-Based Association Mining.

UNIT IV

Classification and Prediction: Issues Regarding Classification and Prediction, Classification by Decision Tree Induction, Bayesian Classification, Rule-Based Classification, Classification by Backpropagation, Support Vector Machines, Associative Classification,

Lazy Learners, Other Classification Methods, Prediction, Accuracy and Error measures, Evaluating the accuracy of a Classifier or a Predictor, Ensemble Methods.

Unit V

Cluster Analysis Introduction :Types of Data in Cluster Analysis, A Categorization of Major Clustering Methods, Partitioning Methods, Hierarchical Methods, Density-Based Methods, Grid-Based Methods, Model-Based Clustering Methods, Clustering High-Dimensional Data, Constraint-Based Cluster Analysis, Outlier Analysis.

UNIT VI

Mining Streams, Time Series and Sequence Data: Mining Data Streams, Mining Time-Series Data, Mining Sequence Patterns in Transactional Databases, Mining Sequence Patterns in Biological Data, Graph Mining, Social Network Analysis and Multirelational Data Mining.

UNIT VII

Mining Object, Spatial, Multimedia, Text and Web Data: Multidimensional Analysis and Descriptive Mining of Complex Data Objects, Spatial Data Mining, Multimedia Data Mining, Text Mining, Mining the World Wide Web.

UNIT VIII

Applications and Trends in Data Mining: Data Mining Applications, Data Mining System Products and Research Prototypes, Additional Themes on Data Mining and Social Impacts of Data Mining.

TEXT BOOKS:

1. Data Mining – Concepts and Techniques - Jiawei Han & Micheline Kamber, Morgan Kaufmann Publishers, Elsevier, 2nd Edition, 2006.
2. Introduction to Data Mining – Pang-Ning Tan, Michael Steinbach and Vipin Kumar, Pearson education.

REFERENCES:

1. Data Mining Techniques – Arun K Pujari, 2nd edition, Universities Press.
2. Data Warehousing in the Real World – Sam Aanhory & Dennis Murray Pearson Edn, Asia.
3. Insight into Data Mining, K.P.Soman, S.Diwakar, V.Ajay, PHI, 2008.
4. Data Warehousing Fundamentals – Paulraj Ponnaiah Wiley student Edition
5. The Data Warehouse Life cycle Tool kit – Ralph Kimball Wiley student edition
6. Building the Data Warehouse by William H Inmon, John Wiley & Sons Inc, 2005.
7. Data Mining Introductory and advanced topics –Margaret H Dunham, Pearson Education.
8. Data Mining, V.Pudi and P.Radha Krishna, Oxford University Press.
9. Data Mining: Methods and Techniques, A.B.M Shawkat Ali and S.A.Wasimi, Cengage Learning.
10. Data Warehouse 2.0,The Architecture for the next generation of Data Warehousing, W.H.Inmon, D.Strauss, G.Neushloss, Elsevier, Distributed by SPD.